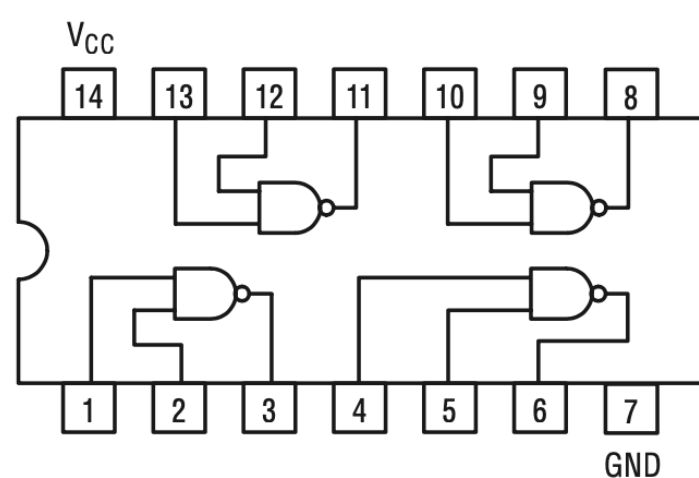
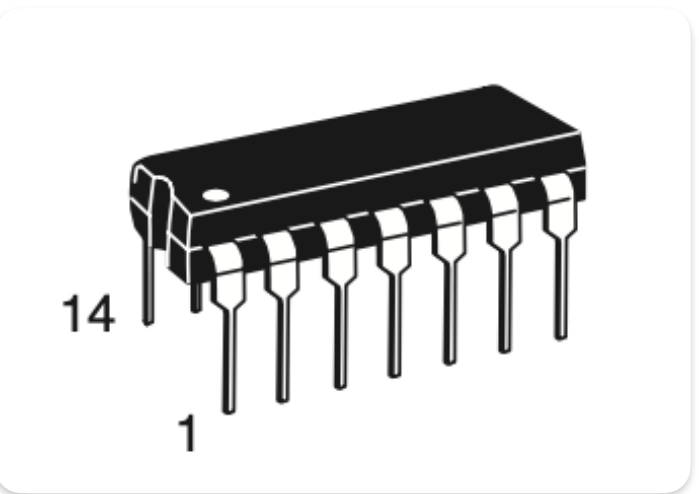


SN74LS00

Quad 2-Input NAND Gate

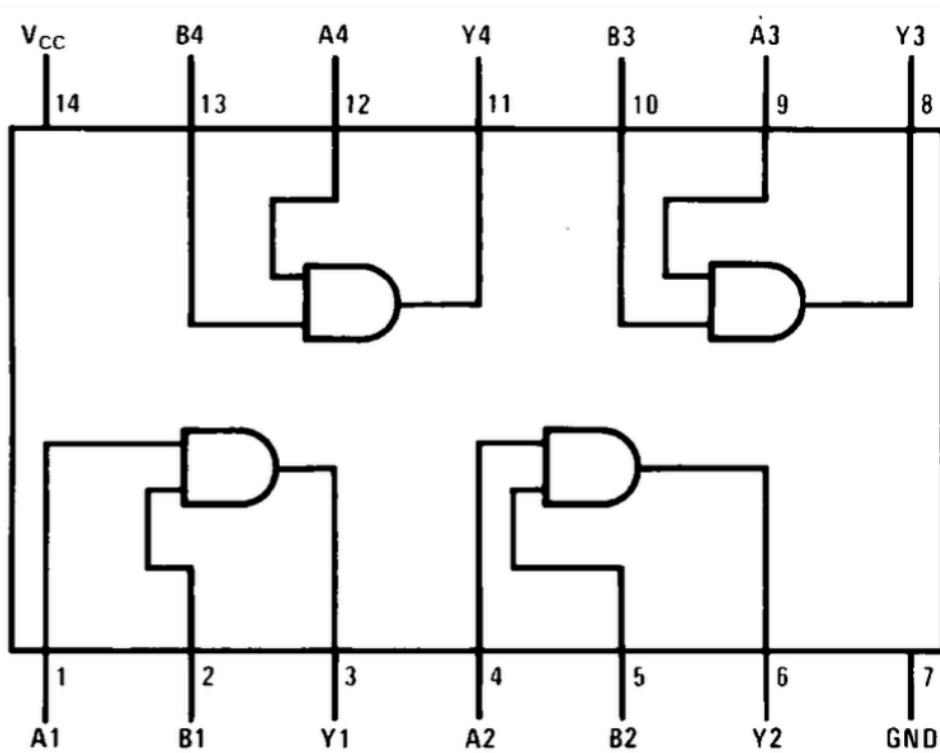


GUARANTEED OPERATING RANGES						
Symbol	Parameter	Min	Typ	Max	Unit	
V _{CC}	Supply Voltage	4.75	5.0	5.25	V	
T _A	Operating Ambient Temperature Range	0	25	70	°C	
I _{OH}	Output Current – High			–0.4	mA	
I _{OL}	Output Current – Low			8.0	mA	



DM74LS08

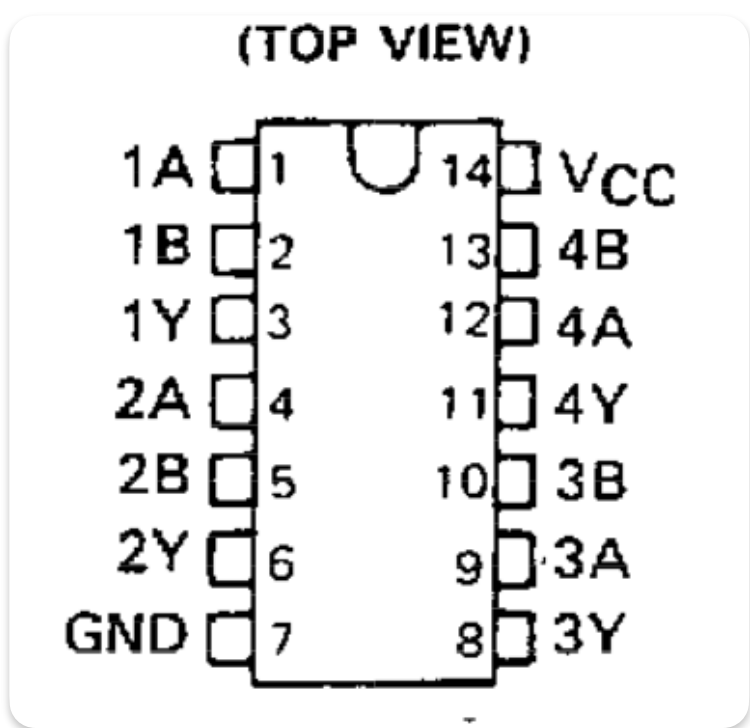
Quad 2-Input AND Gates



Recommended Operating Conditions								
Symbol	Parameter	DM54LS08			DM74LS08			Units
		Min	Nom	Max	Min	Nom	Max	
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.7			0.8	V
I _{OH}	High Level Output Current			–0.4			–0.4	mA
I _{OL}	Low Level Output Current			4			8	mA
T _A	Free Air Operating Temperature	–55		125	0		70	°C

SN74LS32,

QUADRUPLE 2-INPUT POSITIVE-OR GATES

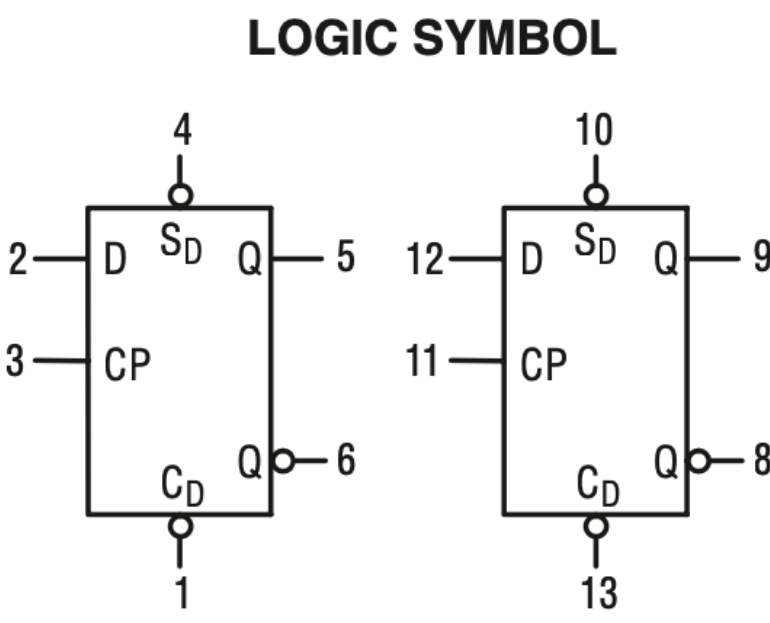
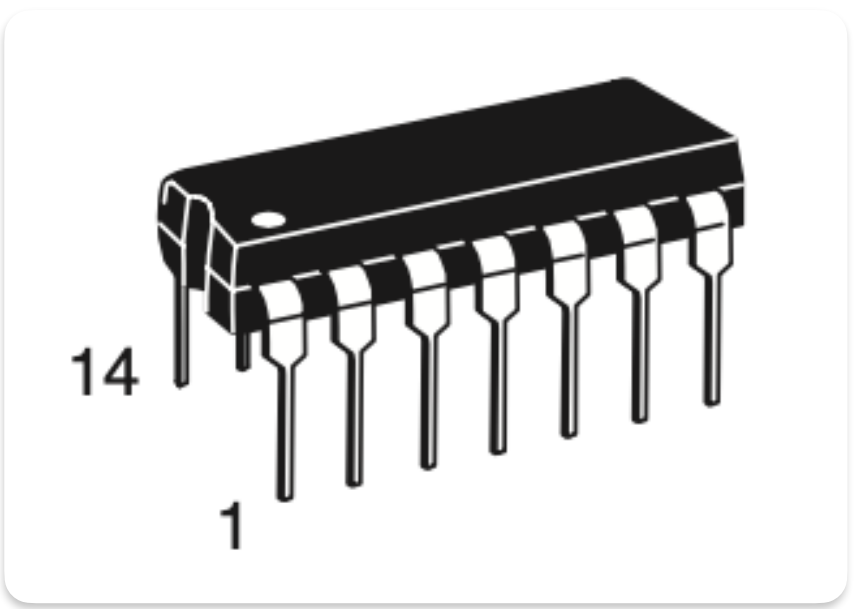


recommended operating conditions

	SN5432			SN7432			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
V _{CC}	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	2			2			V
V _{IL}			0.8			0.8	V
I _{OH}			– 0.8			– 0.8	mA
I _{OL}			16			16	mA
T _A	– 55		125	0		70	°C

SN74LS74A

Dual D-Type Positive Edge-Triggered Flip-Flop



V_{CC} = PIN 14
GND = PIN 7

GUARANTEED OPERATING RANGES

Symbol	Parameter	Min	Typ	Max	Unit
V _{CC}	Supply Voltage	4.75	5.0	5.25	V
T _A	Operating Ambient Temperature Range	0	25	70	°C
I _{OH}	Output Current – High			–0.4	mA
I _{OL}	Output Current – Low			8.0	mA