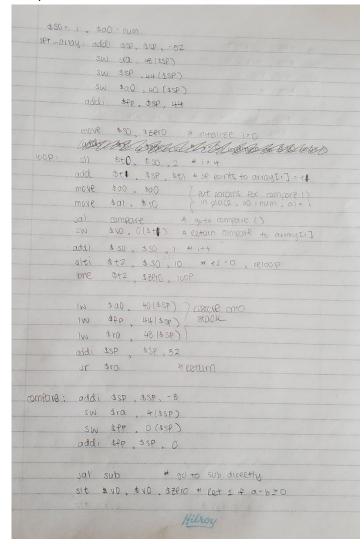
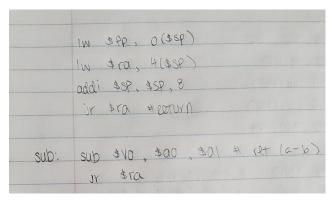
Computer Organization: Homework 2

- 2.4) MIPS doesn't have a subtract immediate instruction because it already has an add immediate instruction (addi). One could simply add an negative number and that would suffice, making a subiinstruction unnecessary.
- 2.6) To extract an arbitrary field from a 32-bit register from \$t3\$ to \$t0, we can use the following instructions:

```
sll $t0, $t3, 9
srl $t0, $t0, 15
```

2.15)





```
2.29)
                                                    Ainitialize to = 0
                              $t0, $zero, $zero
                              $a1, $zero, finish
                                                    # exit loop when al = 0
   100p:
                        beq
                              $t0, $t0, $a0
                        add
                                                    # to += ao; (ao x a)
                        sub
                              $a1, $a1, 1
                                                    # al --; -1 for each iteration
                              1000
                        .i
                                                    # relocp
   finish:
                              $t0, $t0, 100
                                                    # add 100 to to (a6 x al +100)
                        addi
                              $v0, $t0, $zero
                        add
                                                     # veturn to
    using the provided parameters, this program calculates and returns
    (ac x al) + 100.
2.30)
                       $a2, $a2, 2 # i = 2500 × 4
               511
                      $a3, $a3, 2 # j = 2500 x 4
                      $v0, $zero, $zero # v0 = 0
               add
                      $t0, $zero, $zero = +0 = 0 (1)
```

```
$t4, $a0, $t0 # t4 = address of a0 [i]
              add
     outer:
                     $t4, 0($t4) $ ty = a [1]
              1 w
                     $t1, $zero, $zero # 1 = 0 ()
              add
                     $t3, $a1, $t1 # t3 = address of a | [j]
              add
      inner:
                     $t3, 0($t3) # t3 = a'[j]
                     $t3, $t4, skip+1f a1[j] != ac[i], goto skip
              bne
              addi
                     $v0. $v0. 1 # v0 += 1
       skip:
              addi$
                     t1, $t1, 4 # j++
                      $t1, $a3, inner # reloop if ti != a3 ()!= 2500 × 4)
              bne
              addi
                      $t0, $t0, 4 \ i++
                      $t0, $a2, outer # relcop 4 to != a2 (1!= 2500 x 4)
This code snippet compares 2 arrays (a0 & a1), and returns the
 number of matching elements in $40.
```

2.32) b = 25 | a -> ori \$t1 \$t0 25

2.34)

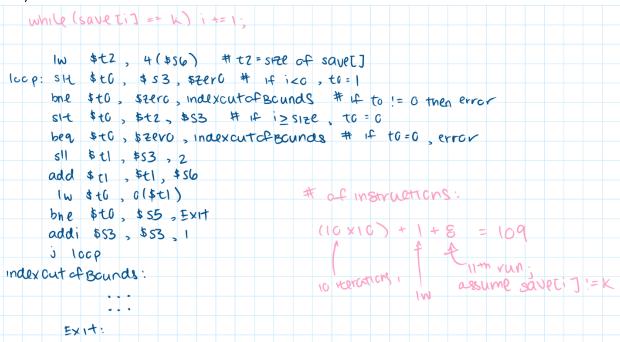
```
addi $v0, $zero, % # Initialize count
loop: lw $v1, 0($a0) # Read next word from source
sw $v1, 0($a1) # Write to destination
addi $a0, $a0, 4 # Advance pointer to next source
addi $a1, $a1, 4 # Advance pointer to next destination

bne
beg $v1, $zero, loop # Loop if word copied != zero

addi $v0, $v0, \ ** mcrement count
```

Pseudoinstructions	Purpose	MIPS Instructions
move \$t1, t2	\$t1 = \$t2	add \$t1, \$t2, \$zero
clear \$t0	\$t0 = 0	add \$t0, \$zero, \$zero
beq \$t1, small, L	if $($t1 = small)$ go to L	li \$at, small
		beq \$t1, \$at, L
beq \$t2, big, L	if $($t2 = big)$ go to L	li \$at, big
		beq \$t2, \$at, L
li \$t1, small	\$t1 = small	addi \$t1, \$zero,small
li \$t2, big	\$t2 = big	addi \$t1, \$zero, big
ble \$t3, \$t5, L	if (\$t3 <= \$t5) go to L	slt \$at, \$t5, \$t3
		bne \$at, \$zero, L
bgt \$t4, \$t5, L	if (\$t4 > \$t5) go to L	slt \$at, \$t5, \$t4
		bne \$at, \$zero, L
bge \$t5, \$t3, L	if (\$t5 >= \$t3) go to L	slt \$at, \$t5, \$t3
		beq \$at, \$zero, L
addi \$t0, \$t2,	\$t0 = \$t2 + big	li \$at, big
big		add \$t0, \$t2, \$at
lw \$t5, big(\$t2)	\$t5 = memory[\$t2 + big]	li \$at, big
		add \$at, \$at, \$t2
		add \$t5, \$t2, \$at

2.46)



for (i=0	a0 = A s0 = C ; i2= 100; i++) a[i] = b[i] + c; a1 = B t0 = i
	addi \$t0, \$t0, \$zero # i=0
100 P	: add \$t1, \$00, \$ t0 # t1 = address of a[i]
	add \$t2, \$a1, \$t0 # t2= address of btij
	sw &t2, 0(\$t1) # ati] = bti]
	add 0(\$t1), \$50,57er0 # ati] += C
	addi \$t0, \$t0, 4 # i++
	bne \$t3, \$tero, LOCP # If i < IC1, relocp.
	1+ (100 × 6) + 6 = 607 mstruettong
	1 + (100 × G) + G = 607 instructions addi revations + 1 > 100
	# of cota ceterences = 101