Anna Tang 3180300155 Edition 3:

Computer Organization: Assignment 3

3.1) 4096, ->	2-BH BINATY	
2) 4096		
2)2048 RO		
2)1024 RO	. 4096 = 1 0000 0000 0000z	
2) 512 RO	32 - bit = 0000 0000 0000 000 000 000 000	3 .
2)256 RO		′ 2
2)128 RC		
2) 64 RO		
2) 32 RO		
2 16 RC		
2 1 8 RG		
2) 4 RC		
2) 2 RC		
2 1 RC		
CRI		

```
3.2) -20472 -> 32 bit binary
 2 ) 2047
 2 11023 R1
                  2047,0 = 111 1111 111/2
 2) 5.1,1 R1
                 -2047,c = coc cocc coc 1,
 2)255 RI
                  32- 617 = 1111 1111 1111 1111 1000 cocc coc 12
 2)127 RI
 2) 63 RI
 2)31
          RI
 2) 15
          RI
       7 RI
 2)
          RI
           RI
           RI
```

- 3.4) 1111 1111 1111 1111 1111 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111

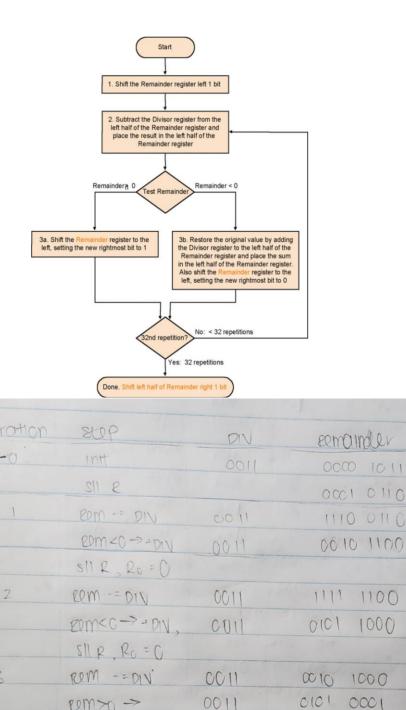
- 3.7) abs \$t2,\$t3

 addu \$t2,\$t3,\$5zero # add contents of t3 into t2

 bge \$t3,\$zero, goto # oneck if $t3 \ge 0$ sub \$t2,\$zero,\$t3 # if $t3 \ge 0$, then 0-(-t3)=t3
- 3.9) If A_lower is sign-extended, then A_upper_adjusted is adjusted as A_upper + 1.
- 3.10) overflow for addition occurs when we add 2
 eositive numbers and get a negative regult
 addu \$t2, \$t3, \$t4

 Situ \$t2, \$t2, \$t3 # t2=1 if t2<t3 (overflow)

3.29) For this question, we can use the algorithm presented in the PowerPoint of chapter 3. We will refer to the third version of division algorithm.



Quotient = 0011, Remainder = 0010

shift left half right

com>0 > 311 R, Ro=1

cem - = DIN

com>0 -> 511 R, Ro=1

FIN

0011

0011

0011

0001

0001

1100

0011

010

0100

0010

BABBBB POSSONS

3.35)
$$2.85 \times 10^{3} + 9.84 \times 10^{4}$$

Alignment — 0.285×10^{4} 9.84 × 10⁴

0.285 × 10⁴

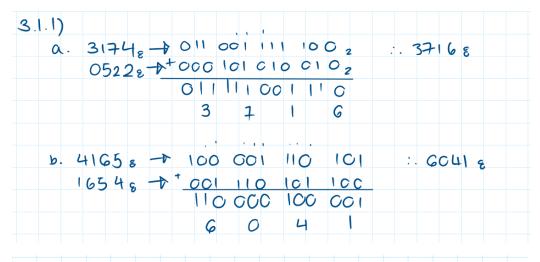
10.125 × 10⁴

10.125 × 10⁴

1.0125 × 10⁵

.: guard & round — 1.01 × 10⁵

Edition 4:



3.1.2)

a. $3174_8 \rightarrow 011 001 111 100_2 :. 3716_8$ $0522_8 \rightarrow 000 101 010 010_2$ 0111110011103 7 1 6

b. $4165_8 \rightarrow 011 110 001 011$ $1654_8 \rightarrow 001 110 101 100 :. 1467_8$ [1]01 100 110 111

3.1.3)

a. $31748 \rightarrow 011 \ 001 \ 111 \ 1002$ $2^{2} + 2^{3} + 2^{4} + 2^{5} + 2^{6} + 2^{7} + 2^{16} = 1660 \text{ (signed & unsigned)}$ b. $41658 \rightarrow 100 \ 001 \ 110 \ 1012$ unsigned: $2^{6} + 2^{7} + 2^{7} + 2^{7} + 2^{17} = 2165 \text{ (signed)} : -1(2^{6} + 2^{7} + 2^{7} + 2^{5} + 2^{6}) = -117$

```
3.2.4)
    a. C352 " + 1100 0011 0101 0010 2 , 36AE " + 0011 0110 1010 11102
      3GAE 16 + 1100 1001 CICI 00102
               1000 1100 1010 01002 BCAUL
                   CA
    b. 5ED416 + 0101 1110 1101 0100, 07A416 + 0000 0111 1010 0100,
     07A4 16 + 1111 1000 0101 1 1002
              0101 0111 0011 00002
                                    : 5730 L
3.2.5)
    a. C35216 - 435216 - 435216 - 36AE16 - A-B = - (A+B)
      0100 0011 0101 00102
    + 0011 0110 1010 1110z
      0111 1010 0000 0000
                                 : FAOO
    ₹ 1111 1010 0000 0000
            A 0
   6. 5ED416 + 0101 1110 1101 01062, 07A416 + 0000 0111 1010 01062
     07A4 16 + 1111 1000 0101 1 1002
              0101 0111 0011 00002 : 5730 16
               5
3.2.6
    a. C352,6 + 1100 0011 0101 0010 =
    b. 550416 - 0101 1110 1101 01062
    For each digit in a nex number, 4 bits are used. We can represent a
    byte by using 2 nex digits, which is 8 bits.
                                   0120012
3.3.1)
                             b. 185 - 101/1001
    a. 216 - 1101 10002
      255-1111 11112
                               122 - 0111 1010
                                      0611 11 11
        : underflow, -39
                                   : Neither, 63
3.3.2)
                             b. -185 - 1011 1001
     a. -216 + 1101 1000 2
       -255-0<sup>+</sup> 1111 1111 <sub>2</sub>
                              122-0-0111 1010
                                        01110011
          : OVERFION , -215
                                      : Neither, 63
```

```
3.3.3)
     a. 1101 1000
                           b. 1011 1001
        - 111 1111
                             -0111 1010
                            (10)0011 1111
                              : overflow , - 179
        : NONE , - 39
3.3.4)
    a. 15,c 7 0000 11112 - 15,c
       139 10 - 1000 10112 + 117 10
                1001 10102
    b. 151,0 - 10010111 2 - 105,0
      214 10 - 1101011012 - 42 10
1011011012 - 14710
                 : saturated arithmetic: - 128,0
3.3.5)
      a. 6006 1111 2 7 1510
                                   b. 1001 0111 2 -105 10
       + 0111 01012 + +117 10
                                      - 110101102
                                                     + 42 10
         1000 0100
                                                      -63 10
                                       1100 0001
                      132 16
       : paturated arithmetic: 128,6
3.3.6)
                                   b. 1001 0111, 151, 6

1101 0110 2 214 16
                  15 16
    a. 0000 11112
   + 1000 1011, 139,0
1661 1010, 154,0
                                    10110 1101, 365 16
    saturated arithmetic 128 ..
```

: saturated Arithmetic : 255 10

erotion	8tep	HUHIPILET	Huttipliand	product	1 1/2	
C	init vals	001 010	000 000 110 010	CCC 01	00 000	000
1	MPILEY 0 = 0" SIT HOUND, STI MPI		000 001 100 100	000 0	00 00	000
2	zii wond sori upi		000 011 001 000	000 00	1 100	100
3	MPILEY 0 = 0 SII MOUND , STI MPI		000 110 016 000) 10C	100
4	MPILLY 0 = 1 , HOME SIL MCORD , SYLMP		001 100 100 000	000 11	110	160
	Mpher 0 = 0 sil mond, sri mp		011 001 000 000	000 11	1 110	100
	MPILEY 0 = 0 SII MCAND, SY I MPIL		110 010 000 000	OCC 11	1 110	100

	356 × 266 +		uumarand.	Product
				000 000 000 000
1	MPILLEYO = 0	001011	000 000 111 010	000 000 000 000
	sil wond, sri my	NOY		
2	MPILEY 0 = 1, WOORD	L+6 000 101	000 001 110 100	000 000 111 010
	sil wond, sri w	Pier	21 222	10) 10
3			600 011 101 000	000 010 101 110
į,	all uppnd, arl i		200 111 210 600	666 619 161 110
4				000 010 101 110
5	MPLUYOF 1		cc 110 100 000	001 001 111 110
	silvant, srivi			
6	MPILETO = 0	000 000	011 101 000 000	cc1 cc1 111 110

3.5.1)

Hardware-

If the LSB of the multiplier is equal to 0, the product register in the hardware implementation stores the sum of the product and the multiplicand register. After the addition process, the multiplicand and multiplier are simultaneously shifted left and right respectively.

For A bits, we would have A iterations. Thus, A iterations * 3 operations * B TU = 3AB time units.

Software-

Software implementation is somewhat like the hardware implementation, but instead it performs the multiplicand and multiplier shifts separately.

Again, A bits will equal to A iterations. Thus, A iterations * 5 operations * B TU = 5AB time units.

- a. 3(8)(4tu) = 96 time units 5(8)(4tu) = 160 time units
- b. 3(64)(8tu) = 1536 time units 5(64)(8tu) = 2560 time units

3.5.2)

Each bit in the adder is checked before the multiplication to let the hardware know whether to add the multiplicand or not. There are A bits, thus A iterations.

The total time taken would be A iterations * B TU = AB time units.

- a. (7)(4tu) = 28 time units
- b. (64)(8tu) = 504 time units

	28 = C7 = + 1[11 010] - 0[00 111]2 + -2610 = 710
	protter step suctiont plv som
	a mituals and con page 111 occided and accident
	1 800 - DIN 000 000 000 111 001 011 01
	\$110,000 000 000 000 000 000 000 000 000
	SIT DIV 000 000 000 011 100 000 000 000 011 010
2	80M - 21 000 000 000 011 100 000 111 100 111 010
1	10 m < 0 -0 -010 000 000 000 011 100 000 000 0
	311 0, 00 0
-	SY PIN 000 000 000 001 110 000 000 000 011 010
3	00
-	70m < 0 +> + 11 000 000 000 001 110 000 000 000 011 010
-	SIL 012 000 000 000 000 111 000 000 011 010
	5.10.11
4	
-	em < 0 ++ 01/1 000 000 000 000 111 000 000 000 011 010
	SIL 0.000 000 000 000 011 100 000 000 011 010
5	80M PIN COO CCO CCC CCC CN ICC 111 111 111 CIC
	80M < 0+ + DIV 000 000 000 000 011 100 000 000 011 010
	SH 0-00-0
	STI PIN 000 000 000 000 001 110 000 000 011 010
6	20m- DIN - 000-600 000 000 000 110 000 000 000 100
-	ERM 20 - 000 001 000 000 001 110 000 000 001 100
10.00	SII Q, Qo=1
	SY1 DIV 000 001 000 000 000 111 000 000 001 100
7	cem = DIN 000 001 000 000 000 111 000 000 000 101
	20m20 000 011 000 000 000 111 000 000 000 101
	SII 6 , Qu=0
	STI DIV 000 011 000 000 000 011 000 000 000 10
	QUOTIENT : 100 011; 7-310

Quotient: 100 011 = -3 base10, Remainder: 100 000 000 101 = -5 base10

75	28 = 07 = + 1[11 010] + 0[00 111] + -2610 = +10
	motion at a p suction of piv com
	committees and the contract of
	1 80m - DIV 000 000 000 111 001 011 011
	\$11 Q , G = 0
100	SIT DIV 000 000 000 011 100 000 000 000 011 010
2	11 100 111 010 000 111 100 111 100 111 010
4 16	10 m < 0 -1 - 511 000 000 000 000 011 100 000 000 000
-	311 0, 00=0
10	SY1 PIN 000 000 000 001 110 000 000 000 011 010
3	
	2011/20 -7 -11/1/ 000 000 000 000 110 000 000 000 011 010
-	SH Q . Q & C OCC OCC OCC OCC ON OCC OCC ON OLO
4	
-	Perm <0 7 - 01 000 000 000 000 000 111 000 000 00
	STI DIN GOO COO COO GOO DII 100 CCO CCO CTI CTO
5	80M PIN COO CCO CCC CO ON ICO 111 111 111 010
1	EM <0++ DIV 000 000 000 000 011 100 000 000 011 010
	SII 0 - Q0 - C
F	STI PIN 000 000 000 000 001 110 000 000 011 010
6	ERM DIN - 000-600 000 000 000 110 000 000 000 100
	20m20 - 000 001 000 000 001110 000 000 001100
	SII Q, Qo=1
	STI DIN 000 001 000 000 000 111 000 000 CC1 100
7	eem = DIN 000 001 000 000 000 111 000 000 000 101
1	80m20 000 011 000 000 000 111 000 000 000 101
	SII 6 , Qc=0
	STI DIN 000 011 000 000 000 011 000 000 000 10
-	QUOTIENT = 100 011, 7-310

Quotient: 100100 = -4 base10, Remainder = 100 000 000 001 = -1 base10

+			WILL 1012] =	1Tco 111-7
separation of				eemainder
0			000 111	000 000 011 101
		sii gen		ccc ccc 111 C10
3 -				III OCI OII ICI
		(pm) < 0	000 111	000 001 110 100
2			000 111	111 010 110 100
	60	m<0	000 111	000 011 101 000
3		-= DIN	000 111	111 100 101 000
	ter	0<0	occ 111	000 111 010 000
. 4	con	Y -= DIV	000 111	000 000 010 000
	ear	120	ccc 111	000 000 100 00 1
5	eer	n -= DIV	000 111	111 001 100 001
The same of	cor	n < 0	000 111	000 001 000 010
6		n -= DIN		111 001100 001
		m < 0.		000 010 000 100
7) -= DIV	000 111	111 111 000 100
1000		m<0	000 111	000 100 001 000
FIN	srl	eem left in		900 001 00 0 1000
				20 100 z = -410 = 428
	-		eemainaur = 1	ccocl2 = -110 = 418
3.7.				
			+ 1 THOIC	2] = 0[00 1112] + -2610 = 710
storit-		2100	VIG	pomoundly
- BALLI L			111 000	000 000 011 010
		11 000		000 000 110 100
		211 K81		111 261 116 1661
1				111 001 110 100
				000 000 110 10 0
2				111 001 110 100
		com < 0	_ CCC 111	000 001 101 000
3			N COC 111	111 010 110 100
				600 011 010 000
LI				111 010 110 106
-1				000 110 100 000
5				111 111 100 000
				001 10 1 000 000
	6		DIV 000 111	
, 5		Rom > 0	000 111	00110000001
,	7			000 101 000 001
				00101000001+
F	10	STIR	cert raif	000 101 000 011
4			auctunt =	100 011 2 \$ 43E \$ -310
4			: eemoinder	= 100 101 2 + 458 + -510

3.10.1)

3.10.3)

3.10.4)

310.4)	
2.	03 25,0 → 111111.01 × 2° → 1.111101 × 25 exp=5 → 127+5=132
,	2131 R1 0.5 · 2 = 1 0 = 0 1000 0100 1111 1010 0000 000
	21 3 21 · 0100 0010 0111 1101 0000 0000 0000
2	1 000 11111 000 1010 1110 11010 · 2 2 2 2 17 2 17 144
3	0000 0000 0000 0000 0000 0000 0000 000

3.10.5)

	5) 63.25 to + 111 111 . 61 . 2° + 1 11 11 11 1 . 25 990 2 30 0 10 28 05 - 1016
S/	= 0 100 0000 0100 1111 1610 0000 0000 00
4	b exp = 17 + 1623 = 1040
•	0000 0000 0000 0000 0000 0000 0000
	Hilrov