

ELECTRONICS (ELTG)
SEM-4
INTERNAL EXAMINATION
FULL MARKS 10

Answer any 5 question. Each question contains 2 marks.

1. What do you mean by OP CODE?
2. What is Flag Register?
3. Explain LHLD 9000H.
4. Explain LDAX B.
5. Explain ADC A.
6. What is meant by instruction cycle?
7. What is meant by machine cycle?

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SEM-4
THEORY EXAMINATION
FULL MARKS 25

Answer any 5 question. Each question contains 5 marks.

1. Explain why the address capability of 8085 microprocessor is 64 KB.
2. What is subroutine? Explain with example.
3. Explain all the addressing modes of the 8085 with the help of examples?
4. Briefly explain the steps involved in a fetch cycle?
5. What is a stack pointer register, describe briefly?
6. Write an assembly language program to find first 5 numbers of Fibonacci Series using 8085.
7. Write an assembly language program to find square root of 4 numbers using lookup table.

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SEM-4
PRACTICAL EXAMINATION
FULL MARKS 15

Write any 3 of the following assembly language programs. Each program carries 5 marks.

1. Subtract the following given numbers and store the result in another memory location using 8085 assembly language programming.

ADDRESS	DATA (HEX)
8000	9F
8001	3E
8002	OUTPUT

2. Add following 16 bit numbers and store the result in another memory location using 8085 assembly language programming. Keep provision for a carry.

ADDRESS	DATA (HEX)
8000	24
8001	AB
8002	4E
8003	7C

3. Multiply the given 8 bit numbers by repeated addition algorithm and store the result in a memory location using 8085 assembly language programming.

ADDRESS	DATA (HEX)
8000	7
8001	B
8002	OUTPUT

4. The integers 1, 2, 3.....9 are stored in consecutive memory location of your choice. Make the 8085 microprocessor fetch the numbers one by one and add them. Store the result in a memory location.

5. Add the following given numbers and store the result in another memory location using 8085 assembly language programming. Keep provision for a carry. (Use command ADD r, where r is any general purpose register)

ADDRESS	DATA (HEX)
8000	92
8001	F7
8002	OUTPUT