UNIVERSITY OF CALCUTTA

1. **Answer any four questions :**   4 X 5 = 20
   a) What does a scanner do ?
   b) Distinguish between Primary memory and Secondary memory.
   c) Full form : EEPROM, VDU
   d) Draw a truth table of two input NAND gate.
   e) What is the function of SP?
   f) What is the function of cache memory?
   g) Define Process.
   h) What is Virtual Memory?

2. **Answer any four questions :**   4 X 20 = 80
   a) Draw a block diagram of a computer system.
   b) Distinguish between software and hardware.
   c) Why NAND and NOR gate are called universal gate?
   d) State and prove De-Morgan's Law.
   e) Explain about Vonneuman architecture.
   f) Explain about Zero address and one address instruction.
   g) Distinguish between preemptive and non-preemptive algorithm.
   h) Explain the process state diagram.