UNIVERSITY OF CALCUTTA

FAKIR CHAND COLLEGE

COMPUTER SCIENCE

B.Sc. SEM-III (General) Examination-2021

Paper: CC-3/GE-3

Internal Assessment

Full Marks: 30   Time: 1hr

Answer Q.No.1 and any four from the rest.

1. Answer any four questions: 1.5X4=6
   a) What is the need of cache memory?
   b) What is the memory structure of Vonneuman Architecture?
   c) What is one address of instruction format?
   d) What is the function of SP??
   e) What is hit ratio of Cache memory?
   f) What is micro-instruction?
   g) What are the differences between PROM and EPROM?

2. Explain about instruction cycle.   6
3. Explain about Addressing Mode Techniques.   6
4. Explain about DMA procedure.   6
5. Explain about Cache Mapping Techniques.   6
6. Explain the role of Hardwired Control Unit.   6
7. What are the differences between Hardwired and Vertical micro instruction?   6
8. Explain the role of tristate buffer.   6
9. What is the differences between memory mapped I/O and I/O mapped I/O?   6