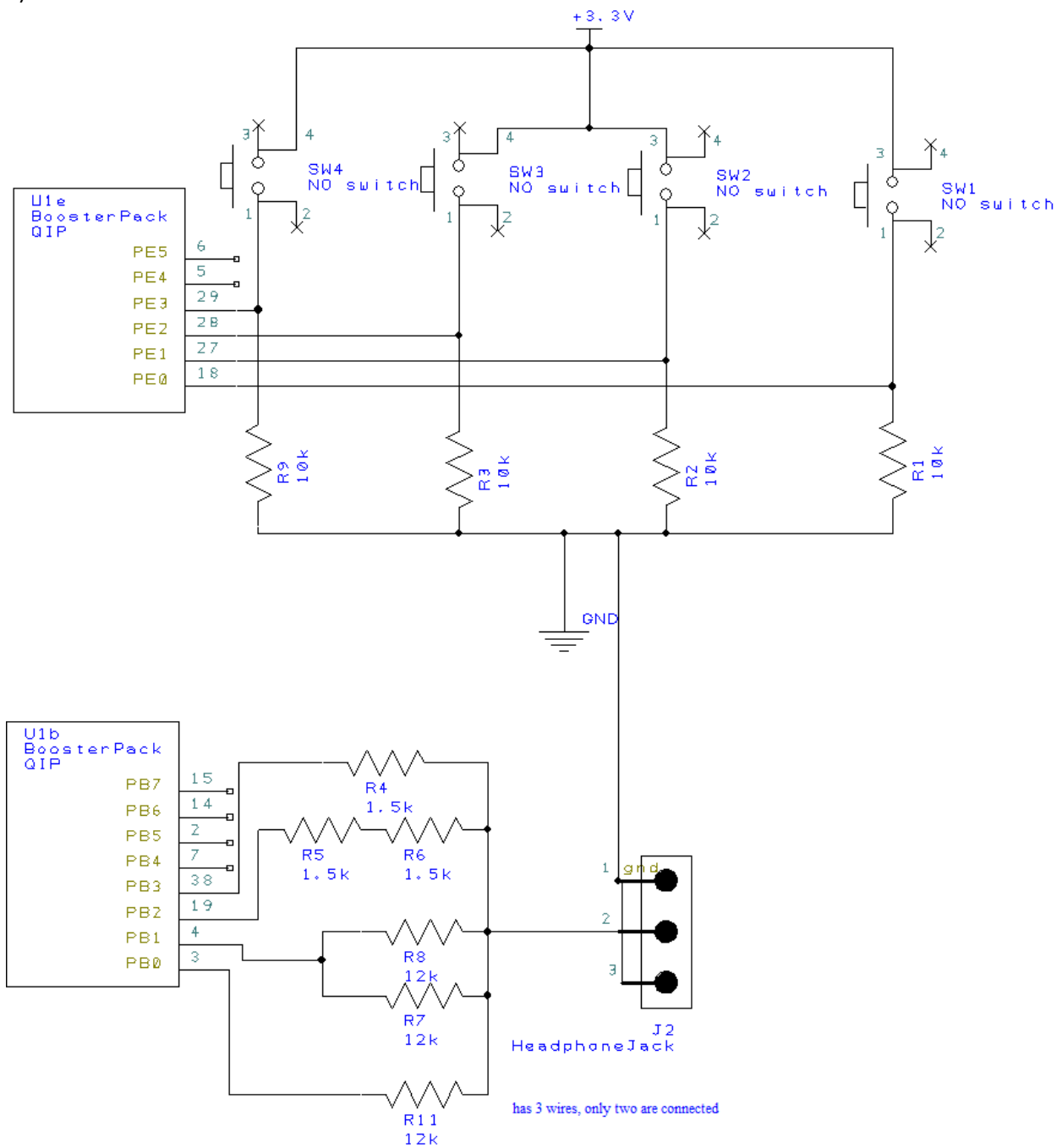
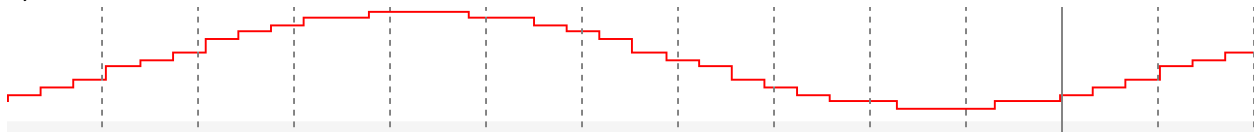


2)



3)

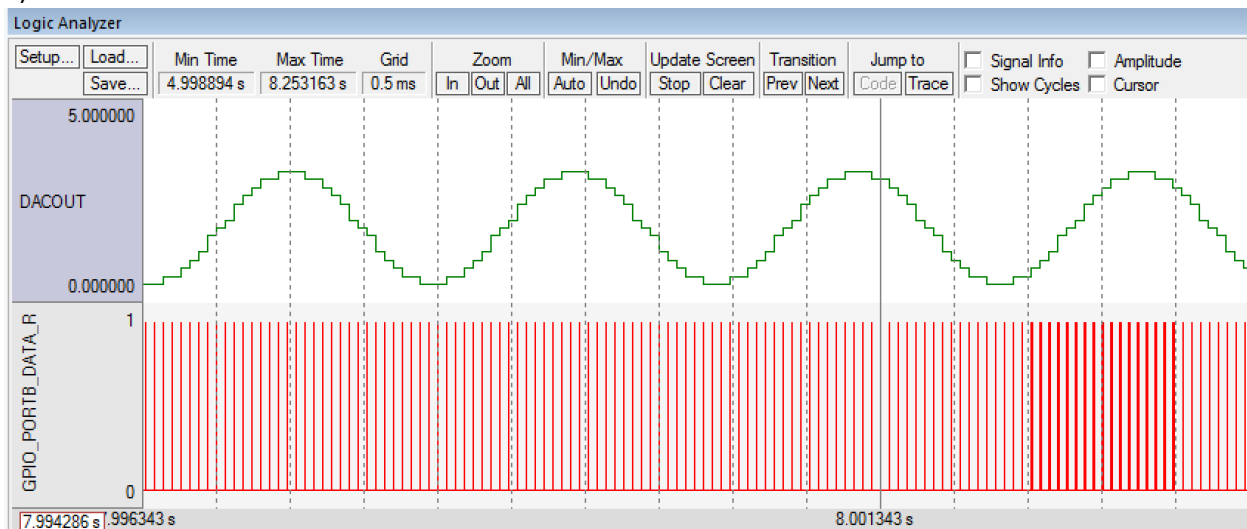


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EE319K – Tiwari

Lab 6 Deliverables

4)



5)

n	Actual (V)	Theory (V)	Error		Resolution (V)
			(V)	Error /3.3V	
0	0.001		0.000	0.001	0.03%
1	1.038		0.220	0.818	24.79%
2	2.040		0.440	1.600	48.48%
3	3.080		0.660	2.420	73.33%
4	0.066		0.880	0.814	24.67%
5	1.103		1.100	0.003	0.09%
6	2.100		1.320	0.780	23.64%
7	3.150		1.540	1.610	48.79%
8	0.132		1.760	1.628	49.33%
9	1.169		1.980	0.811	24.58%
10	2.170		2.200	0.030	0.91%
11	3.210		2.420	0.790	23.94%
12	0.198		2.640	2.442	74.00%
13	1.234		2.860	1.626	49.27%
14	2.230		3.080	0.850	25.76%

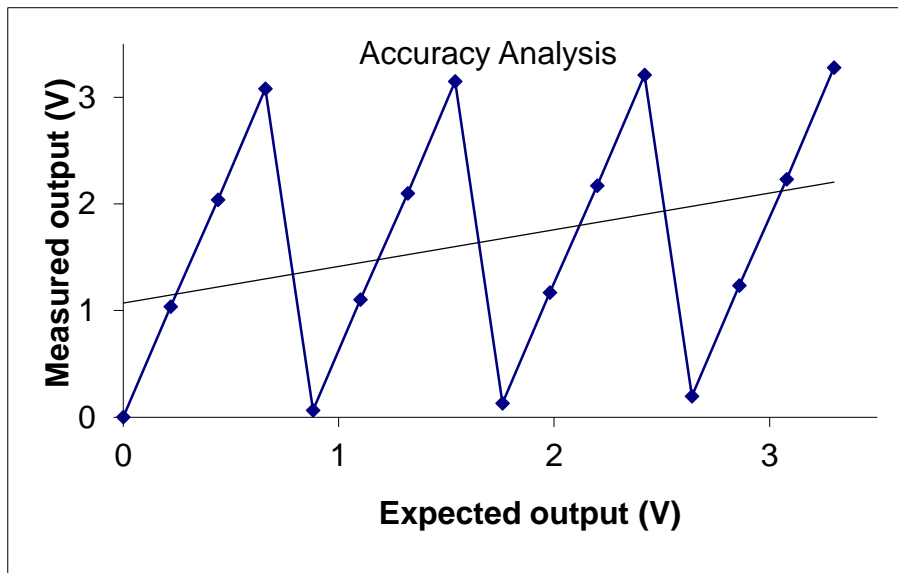
15 3.280 3.300 0.020 0.61% 1.050

Average accuracy of full
 scale(V)= 1.015 30.76%

Average resolution (V)= 0.219

Range= 3.3V

Average precision (V)= (4 bits)



6)

- The interrupt trigger occurs whenever the SysTick current register reaches 0.
- The interrupt vector is in startup.s
- The program stops, checks the vector table for the location of the handler, and jumps to the handler.
- LR holds the address in memory prior to the interrupt being fired. By setting the PC to LR, the program returns to its prior place.