Bound Flasher Verification Checklist

Version 01, Apr.2, 2024

<Revision history>

Date	Content of revision	Version	Creator
Mar. 26, 2024	New creation	01	Group 5 Mar. 26, 2024

<Reference document>

No.	Title name	Versi
1	design_spec.pdf	0.00

BOUND FLASHER VALIDATION CHECK

Created by: Group 5 - L02

	Target	BOUND FLASHER							
	1st category	2nd category		3rd category	Judgment	TM Name	Judged by	Confirmation Method	Remark
No.		No. (Test case name)	No.	(Test case explanation)	Judgment	TWINAITE	Judged by	Commination Method	Kemark
1	Combination block check	1 next_state_generator_tb		ext states generated when the LED state reaches the lue, and when kickback occurs.	Correctness	next_state_generator_tb.v	Nguyễn Hữu Thông	Simulate and compare the correctness of output transactions based on the input stimuli generated by the testbench .	
		2 next_counter_generator_tb	2 Check the n state	ext counter in count-up and count-down and counter-load	Correctness	next_counter_generator_tb.v	Nguyễn Hữu Thông	Simulate and compare the correctness of output transactions based on the input stimuli generated by the testbench .	
		3 kickback_match_generator_tb	3 Check all kid	ckback case	Correctness	kickback_match_generator_tb.v	Nguyễn Hữu Thông	Simulate and compare the correctness of output transactions based on the input stimuli generated by the testbench .	
2	Sequential block check	4 bound_flasher_fsm_tb	4 Check loade	ed value of FFs in the reset and normal case	Correctness	bound_flasher_fsm_tb.v	Võ Thị Hoàng Yến	Simulate and compare the correctness of output transactions based on the input stimuli generated by the testbench .	
2		5 counter_tb	5 Check loade	ed value of FFs in the reset and normal case	Correctness	counter_tb.v	Võ Thị Hoàng Yến	Simulate and compare the correctness of output transactions based on the input stimuli generated by the testbench .	
	System check	system_verification_0_tb		eset signal in the system. The reset signal in the system is phous signal and active low	Correctness	system_verification_0_tb.v	Trần Anh Tài	Simulate and compare the correctness of output transactions based on the input stimuli generated by the testbench .	
2		system_verification_1_tb 7	7	ates of 'flick" signal equal to 0 (except INIT_STATE)	Correctness	system_verification_1_tb.v	Trần Anh Tài	Simulate and compare the correctness of output transactions based on the input stimuli generated by the testbench .	Fixed state transition bug (wrong behavior). In the ONLED5_10, ONLED0_5 state, the system only turns on LED[9] in ONLED5_10 and LED[4] in ONLED0_5, and then transitions to next state.
3		system_verification_2_tb 8	Check kickb	pack case in OFFLED15_5 state	Correctness	system_verification_2_tb.v	Trần Anh Tài	Simulate and compare the correctness of output transactions based on the input stimuli generated by the testbench .	
		system_verification_3_tb	Check kickb	eack case in OFFLED10_0 state (at kickback LED[5])	Correctness	system_verification_3_tb.v	Trần Anh Tài	Simulate and compare the correctness of output transactions based on the input stimuli generated by the testbench .	
		system_verification_4_tb	10 Check kickb	eack case in OFFLED10_0 state (at kickback LED[0])	Correctness	system_verification_4_tb.v	Trần Anh Tài	Simulate and compare the correctness of output transactions based on the input stimuli generated by the testbench .	