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## WeAct Black Pill V2.0

STM32F411CEU6

#### Board

Name	WeAct Black Pill V2.0
Part	Unknown
Brand	WeAct Studio
Origin	China

#### Microcontroller

Part	STM32F411CEU6
Manufacturer	ST-Microelectronics
Core	Arm Cortex-M4
Max. Clock Speed	100MHz
Package	<u>UFQFPN</u> 48 pins

#### **Internal memories**

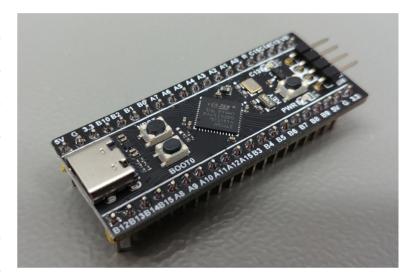
FLASH	512KiB
SRAM	128KiB

#### **小 Oscillators**

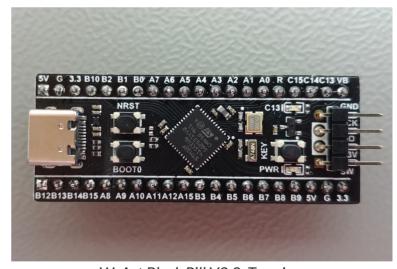
HSI	16MHz
LSI	32kHz
HSE	25MHz
LSE	32.768kHz

#### **7** Power

#### Pictures



WeAct Black Pill V2.0: Perspective view



WeAct Black Pill V2.0: Top view



Sources	Any +5V pin (+5V) USB connector (+5V)
$V_{\mathrm{DDA}}$ pin	No
V <sub>SSA</sub> pin	No
V <sub>REF-</sub> pin	No
V <sub>REF+</sub> pin	No
Backup battery	Pin

#### Regulator

Manufacturer	Diodes Incorporated
Part	АР7343 (6т)
Package	X2-DFN1010-4 4 pins
Input	+3.52V to +5.25V
Output	+3.3V @ 300mA
Datasheet	AP7343.pdf

#### **PCB**

Color	Black
Size (w x I)	20.78mm x 52.81mm
Mounting	Breadboard

#### **Remarks**

• Warning: The +5V pins on this board are directly connected to the +5V pin of the USB connector. There is no protection in place. Do not power this board through USB and an external power supply at the same time.

#### **Inputs**

**C** Reset button

#### **Outputs**











#### **Connectors**



WeAct Black Pill V2.0: Bottom view

Resources

Perspective view

Original schematic

Source files on Github

Original dimensions drawing

Top view

Bottom view

**⇄** Header 1

**⇄** Header 2

• USB connector

\* SWD header

Generic EEPROM

#### inputs & outputs

#### C Reset button

Name	NRST
Reference	-
Туре	Button
Connected to	NRST
Mode	Active low

#### **少 Power LED**

Name	PWR
Reference	-
Туре	LED
Connected to	+3.3V rail
Mode	N.A.

#### **\*** BOOT0 button

Name	ВООТО
Reference	-
Туре	Button
Connected to	BOOT0
Mode	Active high

#### User LED

Name	PC13
Reference	-
Туре	LED
Connected to	PC13
Mode	Sink

#### User button

Name	KEY
Reference	-
Туре	Button
Connected to	PAO
Mode	Active low

# **Connectors & headers**

### **≠** Header 1 properties

Name	Unknown
Reference	None
Туре	pin header (2.54mm, 20x1, male)

# **≠** Header 1 pins

#	Name	Function	Connected to
1	5V	-	+5V rail
2	G	-	Ground plane
3	3.3	-	+3.3V rail
4	B10	-	PB10
5	B2	-	PB2

6	B1	-	PB1
7	В0	-	PB0
8	A7	-	PA7
9	A6	-	PA6
10	A5	-	PA5
11	A4	-	PA4
12	A3	-	PA3
13	A2	-	PA2
14	A1	-	PA1
15	Α0	-	PAO
16	R	-	NRST
17	C15	-	PC15
18	C14	-	PC14
19	C13	-	PC13
20	VB	-	$V_{BAT}$

# 

# NameUnknownReferenceNoneTypepin header (2.54mm, 20x1, male)

# **≠** Header 2 pins

#	Name	Function	Connected to
1	B12	-	PB12
2	B13	-	PB13
3	B14	-	PB14
4	B15	-	PB15
5	A8	-	PA8
6	A9	-	PA9
7	A10	-	PA10
8	A11	-	PA11
9	A12	-	PA12
10	A15	-	PA15
11	В3	-	PB3
12	B4	-	PB4
13	B5	-	PB5
14	В6	-	PB6
15	В7	-	PB7

16	В8	-	PB8
17	В9	-	PB9
18	5V	-	+5V rail
19	G	-	Ground plane
20	3.3	-	+3.3V rail

# ◆ USB connector properties◆ USB connector pins

Name	Unknown
Reference	None
Туре	USB C

#	Name	Function	Connected to
A1/B12	-	GND	Ground plane
A4/B9	-	VBUS	+5V rail
B8	-	SBU2	Ground plane via $5.1$ k $\Omega$ (R8)
A5	-	CC1	Ground plane via $5.1$ k $\Omega$ (R8)
В7	-	D-	PA11 via 10Ω (R9)
A6	-	D+	PA12 via 10Ω (R7)
A7	-	D-	PA11 via 10Ω (R9)
B6	-	D+	PA12 via 10Ω (R7)
A8	-	SBU1	Ground plane via $5.1$ k $\Omega$ (R8)
B5	-	CC2	Ground plane via $5.1$ k $\Omega$ (R8)
B4/A9	-	VBUS	+5V rail
B1/A12	-	GND	Ground plane

# **★** SWD header properties

Name	SW
Reference	None
Туре	pin header (2.54mm, 4x1, male)

# ★ SWD header pins

# Name Function Connected to	)
1 3.3V VCC +3.3V rail	
2 SWDIO SWDIO PA13	
3 SWCLK SWCLK PA14	
4 GND GND Ground plane	!

### **Devices**



# footprint

Generic EEPROM pins
footprint

Name	Unknown
Reference	U3
Manufacturer	Unknown
Part	Generic EEPROM
Marking	Unknown
Datasheet	Unavailable
Package	SOP 8 pins
Description	Generic I2C EEPROM

#	Name	Function	Connected to
1	-	/CS	PA4
2	-	DO	PB4
3	-	/WP	+3.3V rail
4	-	GND	Ground plane
5	-	DI	PA7
6	-	CLK	PA5
7	-	/HOLD	+3.3V rail
8	-	VCC	+3.3V rail

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