Mithril - FMCW Radar

by

Tomas Esson, Ajay Thakkar, Juan Jimenez tesson@stevens.edu, athakka5@stevens.edu, jjimene6@stevens.edu May 2, 2024

© Tomas Esson, Ajay Thakkar, Juan Jimenez tesson@stevens.edu, athakka5@stevens.edu, jjimene6@stevens.edu ALL RIGHTS RESERVED

Table of Contents

1	Introduction	1
2	Project Description 2.1	2 2
3	Resources	3
4	Radar Theory	4
5	Part Selection	5
6	PCB	6
7	Digital Processing	7
8	Networking	8
9	Results	9
10	Issues	10

List of Tables

List of Figures

2.1	Flowchart of the Mithril system.	 	 	 	2

Introduction

The following includes small biographies on all the authors as well as their research interests and projects.

Authors' Biographies

Ajay Thakkar

Ajay Thakkar Ajay Thakkar is a junior majoring in Computer Engineering. He is interested in signal processing and lower level coding. Below you can find his GitHub: https://github.com/athakkar2.

Tomas Esson

Tomas Esson is an aspiring Computer Engineering at Stevens Institute of technology. He is an avid surfer and enjoys elegant math proofs. Currently pursuing interests in computer chip design, digital systems implementation, mathematical optimization of computer chips, and electrical engineering.

Juan Jimenez

Juan Jimenez is a Junior Computer Engineering student at the Stevens Institute of technology. Interested in the intersection between Artificial Intelligence, embedded electronics, and software engineering. To see more projects visit the following GitHub link: https://github.com/jjimene1

Project Description

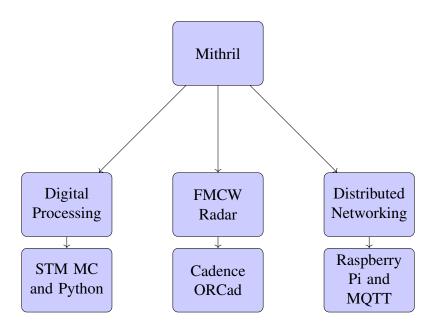


Figure 2.1: Flowchart of the Mithril system

Mithril is a nodal FMCW radar system that incorporates traditional FMCW radar, digital processing, edge computing, and distributed networking. The initial idea of this

As can be seen in Figure 2.1, the radar was designed as a standalone PCB in ORCad, digital processing was handled by STM microcontrollers, and distributed networking is done via Raspberry Pi's and the MQTT protocol. All of these components were designed, engineered, and interfaced from scratch with a limited budget of 2000 dollars.

2.1

The heart of the project is a standalone PCB capable of FMCW radar.

Resources

Chapter 4 Radar Theory

Part Selection

PCB

Digital Processing

Networking

Results

Issues

Index

Chapter	Introduction, 1		
Digital Processing, 7	Issues, 10		
Introduction, 1			
Issues, 10	Networking, 8		
Networking, 8	C.		
Part Selection, 5	Part Selection, 5		
PCB, 6	PCB, 6 Project Description, 2		
Project Description, 2			
Radar Theory, 4			
Resources, 3	Radar Theory, 4		
Results, 9	Resources, 3		
Digital Processing, 7	Results, 9		