Chimera-2016-I Emulator Assignment

Practical 6 - Rotate

CANS Tech INC

Implementing the RCLA Instruction

Once again inside the Group_1 function switch add

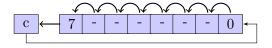
case 0x98: // RCLA
CODE HERE
break;

RCLA		Addressing	Opcode	
Rotate left through		A	0x98	
carry Memory or				
Accumulator				
Flags:	T T T			
notes				

RCLA instructions rotate the Register A one bit towards left through the carry bit.

If Register A contains 0x7A (01111010) and the carry bit is 1, after ROLA instruction is executed, Register A contains 0xF5 (11110101) and the carry bit is reset to 0.

If you look at the RCLA op-code in detail you will see what it does...



```
Firstly, save the current flags...
saved flags = Flags;
Next, set the carry based on the MSB of Register A...
if ((Registers[REGISTER A] & 0x80) == 0x80) {
    Flags = Flags \mid FLAG \mid C:
else{
    Flags = Flags & (0xFF - FLAG C):
```

```
Next, do the shift...
```

 ${\it Registers}[{\it REGISTER_A}] = ({\it Registers}[{\it REGISTER_A}] \ \ {\it \mbox{$\$$}} \ \ {\it \mbox{$\$$}} \ \ {\it \mbox{$\$$}} \ \ \ {\it \mbox{$\$$}} \ \ \ \mbox{$\$$} \ \ \ \mbox{$\$$} \ \mbox{$\$$} \ \ \mbox{$\$$} \mbox{$\$$} \ \mbox{$\$$} \mbox{$\$$} \ \mbox{$\$$} \ \mbox{$\$$} \ \mbox{$\$$} \ \mbox{$\$$} \mbox{$\$$} \ \mbox{$\$$} \mbox{$

Remember the old carry goes into the RCLA of Register A...

Don't forget the other flags!

Compile and run your code to see how many marks you have!

Implementing the SALA Instruction

Once again inside the Group_1 function switch add

case 0x99: // SALA CODE HERE break;

SALA		Addressing	Opcode	
Arithmetic shift left		A	0x99	
Memory or				
Accumulator				
Flags:	T T T			
notes				

If you look at the SALA op-code in detail you will see what it does...

It is the same as RCLA except we don't set the LSB to 1 if the carry was set prior to the op-code being execute...

...Good Luck

Implementing the SHRA Instruction

Once again inside the Group_1 function switch add

case 0x9A: // SHRA CODE HERE break;

SHRA		Addressing	Opcode	
Arithmetic shift right		A	0x9A	
Memory or				
Accumulator				
Flags:	T T T			
notes				

If you look at the SHRA op-code in detail you will see what it does...

```
Firstly, pre-set the Carry

if ((Registers[REGISTER_A] & 0x01) == 0x01) {
    Flags = Flags | FLAG_C;
}
else {
    Flags = Flags & (0xFF - FLAG_C);
}
```

Do the shift...

 $Registers[REGISTER_A] = (Registers[REGISTER_A] \ \$1) \ \& \ 0x7F$

But there is more, remember that SHRA has sign extention...

```
Add... if
((Flags & FLAG_N) == FLAG_N) { Registers
[REGISTER_A] = Registers
[REGISTER_A] | 0x80 }
```

Remember to test any remaining flags!

Compile and run your code to see how many marks you have!

You should now be able to do all remaining Op_codes, good luck!

