## **UTTARA UNIVERSITY**

## SEMESTER RESULT

Program	B.Sc (Engg.) in Co	mputer Scien	ce & Engineering			Main	Batch	38
Student ID	2161081055 Registration UU16100494 Name MD ATIKUR RAHMAN					·		
C Code		Cou	rse Title	Credit	Course Type	GP	LG	Remark
Semester	Spring 16	Section	A					
CSEC111	COMPUTER SYSTEM	COMPUTER SYSTEM & APPLICATION				3.75	Α	
CSEC112	COMPUTER SYSTEM & APPLICATION SESSIONSAL			1.50		4.00	A+	
ENG100	COMMUNICATION	COMMUNICATION SKILL				3.75	Α	
MATHM110	CALCULAS & ORDIN	ARY DIFFERENTI	AL EQUATION	3.00		4.00	A+	
STAT121	STATISTICS & PROBA	ALITY		3.00		4.00	A+	
						SGP	A =	3.89
Semester	Summer 16	Section	Α					
BUS110	BUSINESS COMMUN	NICATION		3.00		3.75	Α	
CSEC121	PROGRAMMING LANGUAGE & APPLICATION -I			3.00		4.00	A+	
CSEC122	PROGRAMMING LANGUAGE & APPLICATION-I SESSIONAL			1.50		4.00	A+	
MATHM123	DISCRETE MATHEM	ATICS		3.00		4.00	A+	
PHY110	MODERN PHYSICS			3.00		4.00	A+	
PHY111	MODERN PHYSICS SESSIONAL			1.50		4.00	A+	
						SGP		3.95
Semester	Fall 16	Section	В					
ACTG121	ACCOUNTING			3.00		4.00	A+	
CSEC211	PROGRAMMING LA	NGUAGE & APPL	ICATION -II	3.00		4.00	A+	
CSEC212	PROGRAMMING LA	NGUAGE & APPL	ICATION-II SESSIONAL	1.50		4.00	A+	
EENG121	ELECTRICAL CIRCUIT			3.00		4.00	A+	
EENG122	ELECTRICAL CIRCUIT			1.50		4.00	A+	
mathm129	COMPLEX VARIABLE		ANSFORMS	3.00		4.00	A+	
	001111 2271 1711171522	- C - C - C - C - C - C - C - C - C - C						
				1		SGP		4.00
Semester	Spring 17	Section	R			SGP		4.00
Semester CSEC213	Spring 17  BASIC ELECTRONICS	Section	В				A =	4.00
CSEC213	BASIC ELECTRONICS	<u> </u>	1	3.00		4.00	A =	4.00
CSEC213	BASIC ELECTRONICS ELECTRICAL & ELECT	<u> </u>	1	3.00 1.50		4.00	A = A+ A-	4.00
CSEC213 CSEC214 CSEC215	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE	TRONICS TECHNO	1	3.00 1.50 3.00		4.00 3.50 3.75	A =  A+  A-  A	4.00
CSEC214 CSEC215 CSEC216	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE DATA STRUCTURE S	TRONICS TECHNO	1	3.00 1.50 3.00 1.50		4.00 3.50 3.75 4.00	A =  A+  A-  A  A+	4.00
CSEC213 CSEC214 CSEC215 CSEC216 CSEC217	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE DATA STRUCTURE S DIGITAL LOGIC DESI	TRONICS TECHNO ESSIONAL	1	3.00 1.50 3.00 1.50 3.00		4.00 3.50 3.75 4.00 3.25	A =  A+  A-  A  A+  B+	4.00
CSEC213 CSEC214 CSEC215	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE DATA STRUCTURE S	TRONICS TECHNO ESSIONAL	1	3.00 1.50 3.00 1.50		4.00 3.50 3.75 4.00 3.25 4.00	A =  A+  A-  A +  B+  A+	
CSEC213 CSEC214 CSEC215 CSEC216 CSEC217 CSEC218	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE DATA STRUCTURE S DIGITAL LOGIC DESI	TRONICS TECHNO ESSIONAL GN GN SESSIONAL	DLOGY SESSIONAL	3.00 1.50 3.00 1.50 3.00		4.00 3.50 3.75 4.00 3.25	A =  A+  A-  A +  B+  A+	3.72
CSEC213 CSEC214 CSEC215 CSEC216 CSEC217 CSEC218 Semester	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE DATA STRUCTURE S DIGITAL LOGIC DESI DIGITAL LOGIC DESI Summer 17	ESSIONAL  GN GN SESSIONAL  Section	DLOGY SESSIONAL	3.00 1.50 3.00 1.50 3.00 1.50		4.00 3.50 3.75 4.00 3.25 4.00 SGP	A =  A+  A-  A+  B+  A+  A+	
CSEC213 CSEC214 CSEC215 CSEC216 CSEC217 CSEC218 Semester CSEC225	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE DATA STRUCTURE S DIGITAL LOGIC DESI DIGITAL LOGIC DESI Summer 17 MICROPROCESSR IN	ESSIONAL  GN GN SESSIONAL  Section  ITERFACING & LO	DLOGY SESSIONAL  B  DW LEVEL LANGUAGE	3.00 1.50 3.00 1.50 3.00 1.50		4.00 3.50 3.75 4.00 3.25 4.00 SGP	A =  A+  A-  A +  B+  A+  A+  C+	
CSEC213 CSEC214 CSEC215 CSEC216 CSEC217 CSEC218 Semester CSEC225 CSEC226	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE DATA STRUCTURE S DIGITAL LOGIC DESI DIGITAL LOGIC DESI Summer 17 MICROPROCESSOR	ESSIONAL  GN GN SESSIONAL  Section  ITERFACING & LO  INTERFACING &	DLOGY SESSIONAL	3.00 1.50 3.00 1.50 3.00 1.50		4.00 3.50 3.75 4.00 3.25 4.00 SGP	A =  A+  A-  A +  B+  A+  A+  A+  A-  A+  A+  A+  A-  A+  A+	
CSEC213 CSEC214 CSEC215 CSEC216 CSEC217 CSEC218  Semester CSEC225 CSEC226 CSEC226	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE DATA STRUCTURE S DIGITAL LOGIC DESI DIGITAL LOGIC DESI  Summer 17 MICROPROCESSR IN MICROPROCESSOR COMPUTER ARCHIT	ESSIONAL  GN GN SESSIONAL  Section  ITERFACING & LO  INTERFACING &	DLOGY SESSIONAL  B  DW LEVEL LANGUAGE	3.00 1.50 3.00 1.50 3.00 1.50 3.00 IAL 1.50 3.00		4.00 3.50 3.75 4.00 3.25 4.00 SGP 2.50 3.50 2.75	A =  A+  A-  A +  B+  A+  A+  A+  B+  A-  B-  B-	
CSEC213 CSEC214 CSEC215 CSEC216 CSEC217 CSEC218  Semester CSEC225 CSEC226 CSEC229 ECON229	BASIC ELECTRONICS  ELECTRICAL & ELECT  DATA STRUCTURE  DATA STRUCTURE S  DIGITAL LOGIC DESI  DIGITAL LOGIC DESI  Summer 17  MICROPROCESSOR IN  MICROPROCESSOR IN  COMPUTER ARCHIT  ECONOMICS	ESSIONAL  GN GN SESSIONAL  Section ITERFACING & LO INTERFACING & ECTURE	DLOGY SESSIONAL  B  DW LEVEL LANGUAGE  LOW LEVEL LANGUAGE SESSION	3.00 1.50 3.00 1.50 3.00 1.50		4.00 3.50 3.75 4.00 3.25 4.00 SGP 2.50 3.50 2.75 2.50	A =  A+  A-  A +  B+  A+  A+  A+  A-  A+  A+  A+  A-  A+  A+	
CSEC213 CSEC214 CSEC215 CSEC216 CSEC217 CSEC218  Semester CSEC225 CSEC226 CSEC226	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE DATA STRUCTURE S DIGITAL LOGIC DESI DIGITAL LOGIC DESI  Summer 17 MICROPROCESSR IN MICROPROCESSOR COMPUTER ARCHIT	ESSIONAL  GN GN SESSIONAL  Section ITERFACING & LO INTERFACING & ECTURE	DLOGY SESSIONAL  B  DW LEVEL LANGUAGE  LOW LEVEL LANGUAGE SESSION	3.00 1.50 3.00 1.50 3.00 1.50 3.00 IAL 1.50 3.00		4.00 3.50 3.75 4.00 3.25 4.00 SGP 2.50 3.50 2.75	A =  A+  A-  A +  B+  A+  A+  A+  B+  A-  B-  B-	
CSEC213 CSEC214 CSEC215 CSEC216 CSEC217 CSEC218  Semester CSEC225 CSEC226 CSEC229 ECON229 mathm221	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE DATA STRUCTURE S DIGITAL LOGIC DESI DIGITAL LOGIC DESI DIGITAL LOGIC DESI MICROPROCESSR IN MICROPROCESSOR COMPUTER ARCHIT ECONOMICS LINEAR ALGEBRA &	ESSIONAL  GN GN SESSIONAL  Section ITERFACING & LO INTERFACING & ECTURE  FOURIER TRANS	DLOGY SESSIONAL  B  DW LEVEL LANGUAGE  LOW LEVEL LANGUAGE SESSION	3.00 1.50 3.00 1.50 3.00 1.50 3.00 AL 1.50 3.00 3.00		4.00 3.50 3.75 4.00 3.25 4.00 SGP 2.50 3.50 2.75 2.50	A =  A+  A-  A +  B+  A+  A+  C+  A-  A-  A-  A-  A-  A-  A-  A-  B-  C+  A-	
CSEC213 CSEC214 CSEC215 CSEC216 CSEC217 CSEC218  Semester CSEC225 CSEC226 CSEC229 ECON229 mathm221  Semester	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE DATA STRUCTURE S DIGITAL LOGIC DESI DIGITAL LOGIC DESI Summer 17 MICROPROCESSOR COMPUTER ARCHIT ECONOMICS LINEAR ALGEBRA &	ESSIONAL  GN  GN SESSIONAL  Section  ITERFACING & LO  INTERFACING &  ECTURE  FOURIER TRANS	DLOGY SESSIONAL  B  DW LEVEL LANGUAGE  LOW LEVEL LANGUAGE SESSION	3.00 1.50 3.00 1.50 3.00 1.50 3.00 AL 1.50 3.00 3.00		4.00 3.50 3.75 4.00 3.25 4.00 SGP 2.50 3.50 2.75 2.50 3.50	A =  A+  A-  A +  B+  A+  A+  C+  A-  A-  A-  A-  A-  A-  A-  A-  B-  C+  A-	3.72
CSEC213 CSEC214 CSEC215 CSEC216 CSEC217 CSEC218  Semester CSEC225 CSEC226 CSEC229 ECON229 mathm221  Semester	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE DATA STRUCTURE S DIGITAL LOGIC DESI DIGITAL LOGIC DESI DIGITAL LOGIC DESI MICROPROCESSR IN MICROPROCESSOR COMPUTER ARCHIT ECONOMICS LINEAR ALGEBRA &	ESSIONAL  GN  GN SESSIONAL  Section  ITERFACING & LO  INTERFACING &  ECTURE  FOURIER TRANS	DLOGY SESSIONAL  B  DW LEVEL LANGUAGE  LOW LEVEL LANGUAGE SESSION	3.00 1.50 3.00 1.50 3.00 1.50 3.00 AL 1.50 3.00 3.00		4.00 3.50 3.75 4.00 3.25 4.00 SGP 2.50 3.50 2.75 2.50 3.50	A =  A+  A-  A +  B+  A+  A+  C+  A-  A-  A-  A-  A-  A-  A-  A-  B-  C+  A-	3.72
CSEC213 CSEC214 CSEC215 CSEC216 CSEC217 CSEC218  Semester CSEC225 CSEC226 CSEC229 ECON229 mathm221	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE DATA STRUCTURE S DIGITAL LOGIC DESI DIGITAL LOGIC DESI Summer 17 MICROPROCESSOR COMPUTER ARCHIT ECONOMICS LINEAR ALGEBRA &	ESSIONAL  GN GN SESSIONAL  Section ITERFACING & LO INTERFACING & ECTURE  FOURIER TRANS  Section I DESIGN	B DW LEVEL LANGUAGE LOW LEVEL LANGUAGE SESSION FORM B	3.00 1.50 3.00 1.50 3.00 1.50 3.00 3.00 3.00 3.00 3.00		4.00 3.50 3.75 4.00 3.25 4.00 SGP 2.50 3.50 2.75 2.50 3.50 SGP	A =  A+  A-  A +  B+  A+  A-  A-  A+  A-  A-  A-  A-  A-  A	3.72
CSEC213 CSEC214 CSEC215 CSEC216 CSEC217 CSEC218  Semester CSEC225 CSEC226 CSEC229 ECON229 mathm221  Semester CSEC227	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE DATA STRUCTURE S DIGITAL LOGIC DESI DIGITAL LOGIC DESI  Summer 17 MICROPROCESSR IN MICROPROCESSOR ICOMPUTER ARCHIT ECONOMICS LINEAR ALGEBRA &  Fall 17 OPERATING SYSTEM	ESSIONAL  GN GN SESSIONAL  Section ITERFACING & LO INTERFACING & ECTURE  FOURIER TRANS  Section I DESIGN I DESIGN SESSIO	B DW LEVEL LANGUAGE LOW LEVEL LANGUAGE SESSION FORM B	3.00 1.50 3.00 1.50 3.00 1.50 3.00 3.00 3.00 3.00 3.00		4.00 3.50 3.75 4.00 3.25 4.00 SGP 2.50 3.50 2.75 2.50 3.50 SGP	A =  A+  A-  A +  B+  A+  A-  C+  A-  B-  C+  A-  C+	3.72
CSEC213 CSEC214 CSEC215 CSEC216 CSEC217 CSEC218  Semester CSEC225 CSEC226 CSEC229 ECON229 mathm221  Semester CSEC227 CSEC227	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE DATA STRUCTURE S DIGITAL LOGIC DESI DIGITAL LOGIC DESI DIGITAL LOGIC DESI MICROPROCESSR IN MICROPROCESSOR COMPUTER ARCHIT ECONOMICS LINEAR ALGEBRA & Fall 17 OPERATING SYSTEM	ESSIONAL  GN GN SESSIONAL  Section ITERFACING & LO INTERFACING & ECTURE  FOURIER TRANS  Section I DESIGN I DESIGN PROGRAMMING	B DW LEVEL LANGUAGE LOW LEVEL LANGUAGE SESSION FORM B	3.00 1.50 3.00 1.50 3.00 1.50 3.00 3.00 3.00 3.00 3.00 3.00		4.00 3.50 3.75 4.00 3.25 4.00 SGP 2.50 3.50 2.75 2.50 3.50 SGP	A =  A+  A-  A +  B+  A+  A-  C+  A-  B-  C+  A-  B-  C+  B-  C-  C-  B-  C-  C-  B-  C-  C-  B-  C-  C	3.72
CSEC213 CSEC214 CSEC215 CSEC216 CSEC217 CSEC218  Semester CSEC225 CSEC226 CSEC229 ECON229 mathm221  Semester CSEC227 CSEC227 CSEC228 CSEC228	BASIC ELECTRONICS ELECTRICAL & ELECT DATA STRUCTURE DATA STRUCTURE S DIGITAL LOGIC DESI DIGITAL LOGIC DESI DIGITAL LOGIC DESI  Summer 17 MICROPROCESSR IN MICROPROCESSOR ICOMPUTER ARCHIT ECONOMICS LINEAR ALGEBRA & Fall 17 OPERATING SYSTEM OBJECT ORIENTED FOR THE PROPERTY OF THE PROPERTY	ESSIONAL  GN GN SESSIONAL  Section  ITERFACING & LO INTERFACING & LO INTER	B DW LEVEL LANGUAGE LOW LEVEL LANGUAGE SESSION FORM B	3.00 1.50 3.00 1.50 3.00 1.50 3.00 3.00 3.00 3.00 3.00 3.00 3.00 3.00		4.00 3.50 3.75 4.00 3.25 4.00 SGP 2.50 3.50 2.75 2.50 3.50 SGP	A =  A+  A-  A +  B+  A+  A-  A-  A-  A-  B-  C+  A-  B-  C+  A-  A-  A-  A-  A-  A-  A-  A-  A-  A	3.72

## **UTTARA UNIVERSITY**

## **SEMESTER RESULT**

Program	B.Sc (Engg.) in Computer Science & Engineering			Main Batch			38		
Student ID	2161081055 Registration UU16100494 Name				MD ATIKUR RAHMAN				
C Code		Cor	urse Title		Credit	Course Type	GP	LG	Remark
Semester	Spring 18	Section	А						
CSEC311	DESIGN & ANALYSI	S OF ALGORITHN	1		3.00		4.00	A+	
CSEC312	DESIGN & ANALYSI	S OF ALGORITHN	/ SESSIONAL		1.50		3.50	A-	
CSEC325	COMPLIER CONSTR	RUCTION			3.00		3.00	В	
CSEC326	COMPLIER CONSTR	RUCTION SESSION	NAL		1.50		3.00	В	
CSEC428	COMPUTER GRAPH	IICS			3.00		3.75	Α	
CSEC429	COMPUTER GRAPH	IICS SESSIONAL			1.50		2.75	B-	
							SGP	A =	3.42
Semester	Summer 18	Section	А						•
CSEC316	SYSTEM ANALYSIS	& DESIGN			3.00		3.75	Α	
CSEC317	SYSTEM ANALYSIS & DESIGN SESSIONAL				1.50		2.50	C+	
CSEC321	DATABASE MANAGEMENT SYSTEM				3.00		3.50	A-	
CSEC322	DATABASE MANAGEMENT SYSTEM SESSIONAL				1.50		3.50	A-	
CSEC323	DATA COMMUNICATION				3.00		3.50	A-	
CSEC324	MEASUREMENT &	INSTRUMENTAT	ION		3.00		4.00	A+	
							SGPA =		3.55
Semester	Fall 18	Section	A						<b>'</b>
CSEC430	SOFTWARE ENGINE	EERING			3.00		2.75	B-	
CSEC431	SOFTWARE ENGINE	EERING SESSION	AL		1.50		3.75	Α	
CSEC432	COMPUTER COMMUNICATION & NETWORKS				3.00		3.75	Α	
CSEC433	COMPUTER COMMUNICATION & NETWORKS SESSIONAL				1.50		4.00	A+	
MATHM462	NUMERICAL ANALY	SIS &COORDINA	TE GEOMETRY		3.00		4.00	A+	
MNGT228	MANAGEMENT & ORGANAZATIONAL BEHAVIOR				3.00		4.00	A+	
							SGP	A =	3.68
Semester	Spring 19	Section	Α						
CSEC399	PROJECT I				3.00		4.00	A+	
CSEC416	COMPUTER SECURITY				3.00		4.00	A+	
CSEC424	ARTIFICIAL INTELLIGENCE AND EXPERT SYSTEMS & SESSIONAL			AL	4.50		3.25	B+	
CSEC467	COMPUTER VISSION AND IMAGE PROCESSING & SESSIONAL				4.50		4.00	A+	
							SGP	A =	3.78
Semester	Summer 19	Section	A						
CSEC418	MACHINE LEARNIN	G	•		3.00		3.75	Α	
CSEC422	INTERNET PROGRA	MMING & SESSI	ONAL		4.50		4.00	A+	
							SGP	A =	3.90
Semester	Fall 19	Section	A						
CSEC499	PROJECT II				3.00		4.00	A+	
							SGP	۸ –	4.00
							301	~ -	7.00

Date of Result Publication	Controller of Examinations
	Uttara University

কোন সেমিষ্টার/কোর্সের ফলাফল না পেলে বা ফলাফলে কোন অসঙ্গতি পরিলক্ষিত হলে পরীক্ষা দপ্তরের নির্ধারিত রেজিষ্টারে লিপিবদ্ধ করার অনুরোধ করা যাচ্ছে। যোগাযোগ: +880 18 7260 7354

কোন শিক্ষার্থী প্রোগ্রাম কার্যক্রম শেষ হওয়ার পর যদি রিটেক করতে আগ্রহী হয় তবে চুড়ান্ত ফলাফল প্রকাশের আগেই পরীক্ষা দপ্তরে আবেদন করে ফলাফল প্রকাশ স্থগিত রাখার ব্যবস্থা করতে হবে। যোগাযোগ: +880 18 7260 7354