Start

08:00

09:00

10:00

11:00

12:00

Winter Semester 2024-25

* Only Registered Semester

20BCE2497 (STUDENT) -

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***** • Students are required to generate the invoice and then proceed to pay the required course fee. Registration is confirmed only if status of the course is 'Registered and Approved' (in regular cases) or 'Registered, Invoice Generated, Fees Paid and Approved' (when there is a requirement for course fee payment).

Class Group	Course	LT PJ Category C	Course Option	Class Id	Slot/ Venue	Faculty Details	Registered / Updated Date & Time	Attendance Date/ Type	Status & Ref. No.
General (Semester)	BCLE213E - Remote Sensing and GIS (Embedded Theory)	2 0 0 0 University Elective	Regular (Under Course University Elective Equivalence) (CLE2023)		G2 - CDMM105	PORCHELVAN P -	13-Dec-2024 18:21	14-Dec-2024 - Manual	Registered and Approved
General (Semester)	BCLE213E - Remote Sensing and GIS (Embedded Lab)	0 0 2 0 University Elective	Regular (Under Course Equivalence)	VL2024250502203	L29+L30 -	PORCHELVAN P -	13-Dec-2024 18:21	14-Dec-2024 - Manual	Registered and Approved
General (Semester)	BCSE325L - Introduction to Bitcoin (Theory Only)	3 0 0 0 University Elective	(CLE2023) Regular (Under Course Equivalence)	VL2024250501913	GDN153 B1+TB1 -	SCE KATHIRAVAN S -	03-Jan-2025 15:43	04-Jan-2025 - Manual	Registered and Approved
	(Mediy diliy)	3.0			SJT602	SCOPE		- Manual	
General (Semester)	BCSE332L - Deep Learning (Theory Only)	3 0 0 0 University Elective 3.0	Regular (Under Course Equivalence) (CSE4088)	VL2024250502028	E2+TE2 - SJT627	SUKANTA GHOSH - SCOPE	13-Dec-2024 10:51	14-Dec-2024 - Manual	Registered and Approved
General (Semester)	BCSE332P - Deep Learning Lab (Lab Only)	0 0 2 0 University Elective	Regular (Under Course Equivalence)	VL2024250505752	L27+L28 -	SUKANTA GHOSH -	13-Dec-2024 10:54	14-Dec-2024 - Manual	Registered and Approved
		3.0	(CSE4088) Regular (Under Course	e	SJT317 C1+TC1 -	SCOPE BHARANI DHARAN N -			
General (Semester)	BITE202L - Digital Logic and Microprocessors (Theory Only)	0 0 University Elective	Equivalence) (ITE1001)	VL2024250506492	SJТ824	SCORE	13-Dec-2024 12:18	14-Dec-2024 - Manual	Registered and Approved
General (Semester)	BITE202P - Digital Logic and Microprocessors Lab (Lab Only)	0 0 2 0 University Elective 1.0	Regular (Under Course Equivalence) (ITE1001)	VL2024250506493	L41+L42 - SJT121	BHARANI DHARAN N - SCORE	13-Dec-2024 12:18	14-Dec-2024 - Manual	Registered and Approved
General (Semester)	CSE1901 - Technical Answers for Real World Problems (TARP) (Embedded Theory)	1 0 0 0 University Core 1.0	Regular	VL2024250507366	TCC1 - SJT303	BALAKRISHNAN P -	10-Jan-2025 16:12	11-Jan-2025 - Manual	Registered and Approved
General (Semester)	CSE1901 - Technical Answers for Real World Problems (TARP) (Embedded Project)	0 0 0 4 University Core 1.0	Regular	VL2024250507367	TCC1 -	BALAKRISHNAN P - SCOPE	10-Jan-2025 16:12	11-Jan-2025 - Manual	Registered and Approved
Weekend Intra (Semester)	CSE3501 - Information Security Analysis and Audit (Embedded Theory)	2 0 0 0 Programme Elective 2.0	Reregistered (CSE3501)	VL2024250507903	Y11+Y12 - SJT825	AGILANDEESWARI L - SCORE	12-Jan-2025 11:18	13-Jan-2025 - Manual	Registered, Invoice Generated, Fees Paid and Approved - VL2500027821
Weekend Intra (Semester)	CSE3501 - Information Security Analysis and Audit (Embedded Lab)	0 0 2 0 Programme Elective 1.0	Reregistered (CSE3501)	VL2024250507904	L77+L78+L91+L92 - SJT119	AGILANDEESWARI L - SCORE	12-Jan-2025 11:18	13-Jan-2025 - Manual	Registered, Invoice Generated, Fees Paid and Approved - VL2500027821
Weekend Intra (Semester)	CSE3501 - Information Security Analysis and Audit (Embedded Project)	0 0 0 4 Programme Elective 1.0	Reregistered (CSE3501)	VL2024250507905	L77+L78+L91+L92 - NIL	AGILANDEESWARI L - SCORE	12-Jan-2025 11:18	13-Jan-2025 - Manual	Registered, Invoice Generated, Fees Paid and Approved - VL2500027821
Weekend Intra (Semester)	JAP1001 - Japanese for Beginners (Theory Only)	2 0 0 0 2.0 University Core (LANGUAGE Basket)	Reregistered (JAP1001)	VL2024250507581	X11+X12 - SJT501	KHANJAN -	12-Jan-2025 11:07	13-Jan-2025 - Manual	Registered, Invoice Generated, Fees Paid and Approved - VL2500027821
					Total Number Of Credits	: 22.0			

	End	08:50	09:50	10:50	11:50	12:50	-	Lunch	14:50	15:50	16:50	17:50	18:50	19:00	19:50
LAB -	Start	08:00	08:51	09:51	10:41	11:40	12:31	Lunch	14:00	14:51	15:51	16:41	17:40	18:31	-
	End	08:50	09:40	10:40	11:30	12:30	13:20	Lunch	14:50	15:40	16:40	17:30	18:30	19:20	-
MON	THEORY	A1	F1	D1	TB1-BCSE325L-TH-SJT602-ALL	TG1	-	Lunch	A2	F2	D2	TB2	TG2	-	V3
	LAB	L1	L2	L3	L4	L5	L6	Lunch	L31	L32	L33	L34	L35	L36	-
TUE	THEORY	B1-BCSE325L-TH-SJT602-ALL	G1	E1	TC1-BITE202L-TH-SJT824-ALL	TAA1	-	Lunch	B2	G2-BCLE213E-ETH-CDMM105-ALL	E2-BCSE332L-TH-SJT627-ALL	TC2	TAA2	-	V4
	LAB	L7	L8	L9	L10	L11	L12	Lunch	L37	L38	L39	L40	L41-BITE202P-LO-SJT121-ALL	L42-BITE202P-LO-SJT121-ALL	-
WED	THEORY	C1-BITE202L-TH-SJT824-ALL	A1	F1	V1	V2	-	Lunch	C2	A2	F2	TD2	TBB2	-	V5
WLD	LAB	L13	L14	L15	L16	L17	L18	Lunch	L43	L44	L45	L46	L47	L48	-
THU -	THEORY	D1	B1-BCSE325L-TH-SJT602-ALL	G1	TE1	TCC1-CSE1901-ETH-SJT303-ALL	-	Lunch	D2	B2	G2-BCLE213E-ETH-CDMM105-ALL	TE2-BCSE332L-TH-SJT627-ALL	TCC2	-	V6
	LAB	L19	L20	L21	L22	L23	L24	Lunch	L49	L50	L51	L52	L53	L54	-

Lunch

14:00

15:00

16:00

17:00

18:00

18:51

19:01