

ASMA Ver.	0.2.1	PERZAD -	- Quick P	ER Zero-Addres	s Dete	ction test	18 Feb 2022 17:40:42 Page 2
LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
				19 *		Low Cor	**************************************
00000000		00000000 00000000	00000955	22 PERZAD 23	START	0 i PERZAD,R0	
00000000 0000008C	0000000	00000000 00000080	0000008C	25 26 PGMCODE 27 PGM_PER_ 28		PERZAD+X'8C' F'0' EQU X'80'	Program interrupt code Program interrupt code PER Event program interrupt code
00000090 00000096 00000098	0000 00000000 00000000	00000090	00000096	30 31 PERCODE 32 PERADDR		PERZAD+X'96' XL2'00' AD(0)	PER interrupt fields PER interrupt code PER interrupt address
		00000150	00000000	34 PGMOPSW	EQU	PERZAD+X'150'	z Program Old PSW
000000A0 000001A0 000001A8	00000001 80000000 00000000 00000200	000000A0	000001A0	36 37 38	ORG DC DC	PERZAD+X'1A0' X'0000000180000000' AD(GO)	z Restart New PSW
	00000001 80000000 00000000 000002CC	000001B0	000001D0	40 41 42	ORG DC DC	PERZAD+X'1D0' X'0000000180000000' AD(PGMRUPT)	z Program New PSW
000001E0		000001E0	00000200	44	ORG	PERZAD+X'200'	Start of actual program

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
				46 ****	******	*******	************	
				47 *		Begin t	tests	
				48 *****	*******	·**************	*************	
0000200	EB9B 0928 002F		00000928	50 GO	LCTLG	R9,R11,PERCTL	Init CR9-CR11 PER Control Registers	
0000206	8000 0940		00000940	51		ENPER	Enable Program Event Recording	
000020A	D201 0000 0948	00000000	00000948	53	MVC	0(2,R0),=XL2'070	00' (just go on to next instruction)	
0000210	D203 0002 0944	00000002	00000944	54 55	MVC	2(4,R0),=XL4'47F	F00000' (to be fixed by tests before usé)	
		00000216	00000001	56 BEGRA	NGE EQU	*	Begin of PER Range	

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LOC	OBJECT CODE	ADDR1 ADDR2	2 STMT			
			59 *	********* Instructi ******	ons that should NE	**************************************
00000216	EB0F 0888 0004	000008	388 62	LMG	R0,R15,ZEROREGS	Initialize all registers to zero
0000021C 00000220 00000222 00000224	4110 0000 1811 1E11 1F11	00000	000 64 65 66 67	LA LR ALR SLR	R1,0 R1,R1 R1,R1 R1,R1	
00000226	1511		68	CLR	R1,R1	
00000228	1211		69	LTR	R1,R1	
0000022A 0000022E 00000234	4120 0002 D201 2002 094A 47F0 1000	000000 0000002 000000 000000 00000238	94A 72 900 73	LA MVC B EQU	R2,2 2(2,R2),=S(B) 0(,R1)	R2> branch instruction in low core
00000238 0000023E	D201 2002 094C 07F1	00000002 000009	77	MVC BR	2(2,R2),=S(BR) R1	
00000240 00000246 0000024A	D201 2002 094E 4130 0003 0631	00000240 000000 0000000 000000 000000	94E 80 903 81 82	EQU MVC LA BCTR EQU	* 2(2,R2),=S(BCTR) R3,3 R3,R1 *	
0000024C 00000252 00000254	D201 2002 0950 1830 4430 1000	00000002 000009 00000258 000000	86 900 87	MVC LR EX EQU	2(2,R2),=S(EX) R3,R0 R3,0(,R1)	

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
				91 * In:	structi	ons that should ALI	**************************************
00000258	EB0F 0888 0004		00000888	94	LMG	R0,R15,ZEROREGS	Reset all registers back to zero
0000025E	5800 1000		00000000	96 ZAD01	L	R0,0(,R1)	
00000262	5000 1000		00000000	98 ZAD02	ST	R0,0(,R1)	
00000266	9180 1000		00000000	100 ZAD03	TM	0(R1),X'80'	
0000026A	D200 1000 2000	00000000	00000000	102 ZAD04	MVC	0(1,R1),0(R2)	
00000270	D500 1000 2000	00000000	00000000	104 ZAD05	CLC	0(1,R1),0(R2)	
00000276	5500 1000		00000000	106 ZAD06	CL	R0,0(,R1)	
0000027A	9500 1000		00000000	108 ZAD07	CLI	0(R1),X'00'	
0000027E	BF0F 1000		00000000	110 ZAD08	ICM	R0,15,0(R1)	
00000282	4300 1000		00000000	112 ZAD09	IC	R0,0(,R1)	
00000286	980F 1000		00000000	114 ZAD10	LM	R0,R15,0(R1)	
0000028A	4800 1000		00000000	116 ZAD11	LH	R0,0(,R1)	
0000028E	9680 1000		00000000	118 ZAD12	OI	0(R1),X'80'	
00000296	4130 0001 4150 00FF 8950 0018		00000001 000000FF 00000018	120 121 122	LA LA SLL	R3,1 R5,X'FF' R5,24	Destination length must be non-zero Pad char to make src len reg non-zero Move into high-order byte position
			0000018			-	Hove Theo high-order byte position
0000029E				124 ZAD13	MVCL	R2,R4	
000002A0	0F24			126 ZAD14	CLCL	R2,R4	

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
				128 ******* 129 * 130 *****	Ver	ify we've seen ALL expect	**************************************
		000002A2	00000001	132 ENDRANGE	EQU	*	End of PER Range
000002A2 000002A6	4110 0800 4120 000E		00000800 0000000E	134 135	LA LA	R1,ZADOKTAB R2,NUMZADS	R1> table R2 <== number of table entries
000002AA 000002AE			00000001 000002BE	137 DONELOOP 138	CLI JNE	1(R1),X'FF' DONEFAIL	Have we seen this event? No?! ** FAIL!! **
000002B2 000002B6			00000008 000002AA	140 141	LA BCT	R1,L'ZADOKTAB(,R1) R2,DONELOOP	Bump to next table entry Loooop through all entries
000002BA	A7F4 02E3		00000880	143	J	SUCCESS	Done! Successful Test!
				146 *	FA	AIL! Missing PER Zero-Add	**************************************
000002BE 000002C4 000002C8	D201 0922 0952 9203 0927 A7F4 02DE	00000922	00000952 00000927 00000884	149 DONEFAIL 150 151	MVC MVI J	BADPSW+8+2(2),=XL2'0BAD' BADPSW+16-1,BADNOZAD FAILURE	<pre>Indicate test failure Indicate failure code ** FAIL!! **</pre>

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
				154	*		Program Interrupt Hand	**************************************
000002CC 000002D0	9180 008F A784 02D0		0000008F 00000870	157 158	PGMRUPT	TM JZ	PGMCODE+3,PGM_PER_EVENT ABORT	Expected interrupt? No?! ** ABORT!! **
000002D4 000002D8	9504 0096 4780 02EA		00000096 000002EA	160 161		CLI BE	PERCODE, ZADEVENT ZADCHECK	Zero-Address Detection event? Yes, go check event address
000002DC 000002E2 000002E6	D201 0922 0954 9201 0927 A7F4 02CF	00000922	00000954 00000927 00000884	163 164 165		MVC MVI J	BADPSW+8+2(2),=XL2'DEAD' BADPSW+16-1,BADPER FAILURE	<pre>Indicate PGMRUPT failure Indicate failure code ** FAIL!! **</pre>
000002EA 000002F0 000002F4	EB0F 0328 0024 4110 0800 4120 000E		00000328 00000800 0000000E	167 168 169	ZADCHECK	STMG LA LA	R0,R15,PGMREGS R1,ZADOKTAB R2,NUMZADS	Save caller's registers R1> table R2 <== number of table entries
000002F8 000002FC 00000300	5830 1004 5530 009C A774 0009		00000004 0000009C 00000312	171 172 173	ZADLOOP	L CL JNE	R3,4(,R1) R3,PERADDR+4 ZADNEXT	R3 <== Expected Event Address Expected Event Address? No, try next entry
00000304 00000308 0000030E	92FF 1001 EB0F 0328 0004 B2B2 0150		00000001 00000328	175 176 177		MVI LMG	1(R1),X'FF' R0,R15,PGMREGS PGMOPSW	Yes, flag as having been seen Restore caller's registers Return to caller
0000030E 00000312 00000316	4110 1008 4620 02F8		00000150 00000008 000002F8		ZADNEXT	LA BCT	R1,L'ZADOKTAB(,R1) R2,ZADLOOP	Bump to next table entry Loooop to try next entry
0000031A 00000320 00000324	D201 0922 0952 9202 0927 A7F4 02B0	00000922	00000952 00000927 00000884	182 183 184		MVC MVI J	BADPSW+8+2(2),=XL2'0BAD' BADPSW+16-1,BADZAD FAILURE	<pre>Indicate test failure Indicate failure code ** FAIL!! **</pre>
00000328	00000000 00000000			186	PGMREGS	DC	16D'0'	Saved GR registers 0 - 15
				189	*		Test FAILURE codes.	*********** • • ************
		00000001 00000002 00000003	00000001	193	BADPER BADZAD BADNOZAD	EQU	X'01' Unexpected PER X'02' Unexpected PER X'03' Missing PER ZAD	ZAD Event

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
				196 ******* 197 *			**************************************
				198 ******	****	***************	**************************************
		00000010		200	000	DED74D VI000I	
000003A8 00000800		000003A8	00000800	200 201 ZADOKTAB	ORG	PERZAD+X'800' 0D'0'	Fixed table location PER ZAD Addresses Table
00000000				ZOI ZADOKTAD	, DC	0D 0	FER ZAD Addresses rable
				203 *		(nn)	= test# ("ZADnn" label#)
				204 * 205 *		X'FF'	<pre>= event was detected A(xxxx) = PER event address</pre>
				206 *			`
				207 *			
				208 *		VV VV	VVVV
00000800	01000000 0000025E			210	DC	AL1(01),X'00',XL2	1'00',A(ZAD01)
00000808	02000000 00000262			211	DC	AL1(02),X'00',XL2	.'00',A(ZAD02)
00000810	03000000 00000266			212	DC	AL1(03),X'00',XL2	
00000818	04000000 0000026A 05000000 00000270			213 214	DC DC	AL1(04),X'00',XL2 AL1(05),X'00',XL2	1'00',A(ZAD04)
00000828	06000000 00000276			215	DC	AL1(05),X'00',XL2	1'00',A(ZAD05)
00000830	07000000 0000027A			216	DC	AL1(07),X'00',XL2	'00',A(ZAD07)
00000838	08000000 0000027E			217	DC	AL1(08),X'00',XL2	
00000840 00000848	09000000 00000282 0A000000 00000286			218 219	DC DC	AL1(09),X'00',XL2 AL1(10),X'00',XL2	
00000850	0B000000 0000028A			220	DC	AL1(11),X'00',XL2	'00',A(ZAD11)
00000858	0C000000 0000028E			221	DC	AL1(12),X'00',XL2	1'00',A(ZAD12)
00000860 00000868	0D000000 0000029E 0E000000 000002A0			222 223	DC DC	AL1(13),X'00',XL2 AL1(14),X'00',XL2	1'00',A(ZAD13)
	01000000 000001110	0000000E	00000001	224 NUMZADS	EQU	(*-ZADOKTAB)/8	Number of table entries

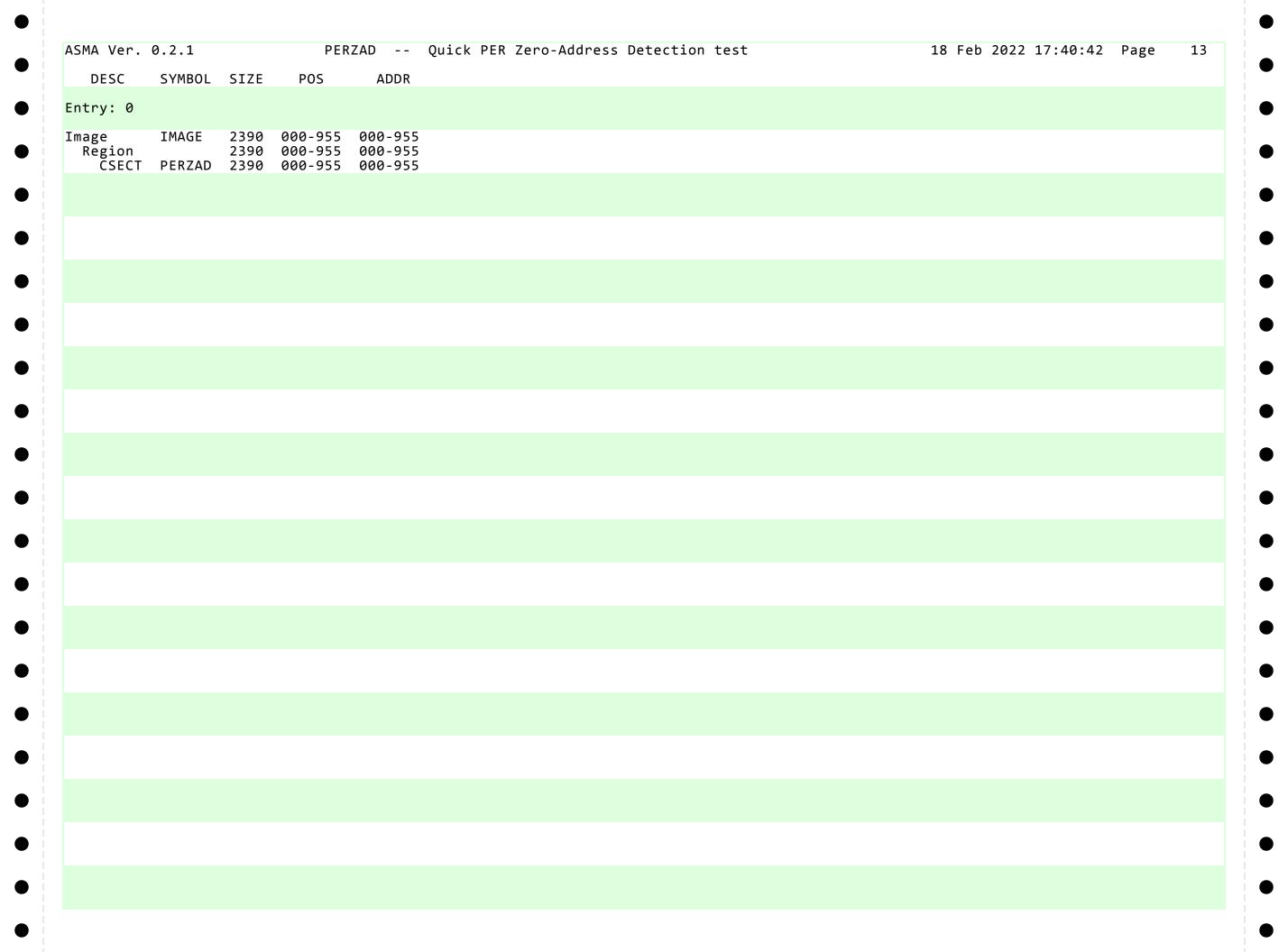
				228 ******	ADU *****	***************	unexpected program interrupt
				_			
	D201 0922 0954 D203 0924 008C	00000922		230 ABORT	MVC		2'DEAD'
	A7F4 0004	00000924	0000008C 00000884	231 232	J	FAILURE	DE(L'PGMCODE),PGMCODE
				_	-		

				235 * 236 ******	Suc ****	ccessful completior	/ Abnormal termination ************************************
00000000	D2D2 0000		0000000				
	B2B2 0908 B2B2 0918		00000908 00000918	238 SUCCESS 239 FAILURE			Load test completed successfully PSW Load the test FAILED somewhere!! PSW
3000004	5252 V)10		30000710	233 I AILUNE	LIJWL	- DADI JW	LOGG CHE CESE LATELD SOMEWHELE: 1 3W

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
				242	*		WORKING STORAGE	********* ********
00000888	00000000 00000000			245	ZEROREGS	DC	16D'0'	ZEROED GR registers 0 - 15
	00020001 80000000 00000000 00000000			247 248	GOODPSW	DC DC	XL8'0002000180000000' XL4'00000000',A(X'0000000	ð 0 ')
	00020001 80000000 0000DEAD 000000FF			250 251	BADPSW	DC DC	XL8'0002000180000000' XL4'0000DEAD',A(X'000000F	FF') (FF = Reason for Failure)
		04000000 00000004			CR9_ZEROAZADEVENT		EQU X'04000000' X'04'	Zero-address Detection Zero-address Detection event
00000930	00000000 04000000 00000000 00000216 00000000 000002A2			256 257 258	PERCTL	DC DC DC	AD(CR9_ZEROADDR) AD(BEGRANGE) AD(ENDRANGE)	PER events CR10 = Range begining address CR11 = Range ending address
00000940	40			260	ENPER	DC	B'01000000'	Enable PER bit in PSW
00000944 00000944 00000948	47F00000 0700			262 263 264		LTORG	, =XL4'47F00000' =XL2'0700'	Literals Pool
0000094A	0238 0240			265 266			=S(B) =S(BR)	
00000950 00000952	024C 0258 0BAD DEAD			267 268 269 270			=S(BCTR) =S(EX) =XL2'0BAD' =XL2'DEAD'	
		00000000	00000001	272	RØ	EQU	0	Register equates
		00000001 00000002 00000003		273 274 275	R1 R2	EQU EQU EQU	1 2 3	
		00000004 00000005 00000006	00000001 00000001 00000001	276 277 278	R5 R6	EQU EQU EQU	4 5 6	
		00000007 00000008 00000009	00000001 00000001 00000001	279 280 281	R8 R9	EQU EQU EQU	7 8 9	
		0000000A 0000000B 0000000C	00000001 00000001 00000001	282 283 284	R11 R12	EQU EQU	10 11 12	
		0000000D 0000000E 0000000F	00000001 00000001 00000001	285 286 287	R14	EQU EQU EQU	13 14 15	
				289		END		

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SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFE	RENCE	S														
ABORT	I	0000870	6	230	158																
			6																		
B	U	0000238	1	74	72																
BADNOZAD	U	0000003	1	194	150																
BADPER	U	0000001	1	192	164																
BADPSW	X	0000918	8	250	149	150	163	164	182	183	230	231	239								
BADZAD	U	0000002	1	193	183																
BCTR	U	000024C	1	83	80																
BEGRANGE	U	0000216	1	56	257																
BR	U	0000240	1	78	76																
CR9 ZEROADDR	U	4000000	1	253	256																
DONEFAIL	Ť	00002BE	6	149	138																
DONELOOP	T	00002AA	4	137	141																
ENDRANGE	ιī	00002AA	1	132	258																
ENPER	В	0000242	1	260	51																
	D II		1																		
EX	Ų	0000258	1	88	85	165	104	222													
FAILURE	_ T	0000884	4	239	151	165	184	232													
GO	Ţ	0000200	6	50	38																
GOODPSW	X	0000908	8	247	238																
IMAGE	1	0000000	2390	0																	
NUMZADS	U	000000E	1	224	135	169															
PERADDR	Α	0000098	8	32	172																
PERCODE	Χ	0000096	2	31	160																
PERCTL	Α	0000928	8	256	50																
PERZAD	i i	0000000	2390	22	25	30	34	36	40	44	200	23									
PGMCODE	F	000008C	4	26	157	231	34	30	70	7-7	200	23									
PGMOPSW	ii	0000000	ø	34	177	231															
PGMREGS	D	0000130	8	186	167	176															
	Т					1/0															
PGMRUPT	1	00002CC	4	157	42																
PGM_PER_EVENT	U	0000080	1	27	157				0.5	0.4	0.5	0.0	105	440	110		446	467	476		
RØ	U	0000000	1	272	23	53	54	62	86	94	96	98	106	110	112	114	116	167	176		
R1	U	0000001	1	273	64	65	66	67	68	69	73	77	82	87	96	98	100	102	104	106	
					108	110	112	114	116	118	134	137	140	168	171	175	179				
R10	U	A00000	1	282																	
R11	U	000000B	1	283	50																
R12	U	000000C	1	284																	
R13	U	00000D	1	285																	
R14	Ū	000000E	1	286																	
R15	Ű	000000F	1	287	62	94	114	167	176												
R2	II	0000001	1	274	71	72	76	80	85	102	101	12/	126	135	141	169	180				
R3	11	0000002	1	274	81	82	86	87	120	171	172	144	120	100	-	100	100				
R4	11	0000003	1	275	124	126	80	0/	170	т/т	1/2										
	U		_																		
R5	U	0000005	1	277	121	122															
R6	U	0000006	1	278																	
R7	U	0000007	1	279																	
R8	U	0000008	1	280																	
R9	U	0000009	1	281	50																
SUCCESS	I	0000880	4	238	143																
ZAD01	I	000025E	4	96	210																
ZAD02	I	0000262	4	98	211																
ZAD03	I	0000266	4	100	212																
ZAD04	I	000026A	6	102	213																
ZAD05	Ī	0000270	6	104	214																
2,1505	-	3000270	O	104	217																

SMA Ver. 0.2.1 ACRO DEFN REFERENCES	PERZAD -	- Quick PER Zero-Address Detection test	18 Feb 2022 17:40:42 Page	12
o defined macros				



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STMT	FILE NAME		
c:\Users\Fish\Doo	cuments\Visual Studio 2008\Projects\MyProjects\ASMA-0\PERZAD\PERZAD.asm	n	
** NO ERRORS FOUND **			