ASMA Ver.	0.2.1 bfp-022-mu	ltsub: Test	IEEE Mult	iply And	Subtract 17 Aug 2022 12:26:56 Page 1
LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				2 *: 3 *	********************
				4 *	Testcase IEEE MULTIPLY AND SUBTRACT  Test case capability includes IEEE exceptions trappable and otherwise. Test results, FPCR flags, the Condition code, and any DXC are saved for all tests.
				8 * 9 *	
				10 * 11 * 12 *	instructions. Standard Multiply and Multiply to longer precision
				13 * 14 *	********
				15 * 16 *	** IMPORTANT! **
				17 * 18 *	
				19 * 20 * 21 *	to display messages and thus your .tst runtest script MUST contain a "DIAG8CMD ENABLE" statement within it!
				22 * 23 *	*********************
				26 *	
				27 * 28 *	bfp-022-multsub.asm
				29 * 30 * 31 * 32 *	Hercules Binary Floating Point Validation Package by Stephen R. Orso
				33 * 34 * 35 *	Copyright 2016 by Stephen R Orso. Runtest *Compare dependency removed by Fish on 2022-08-16 PADCSECT macro/usage removed by Fish on 2022-08-16
				38 *	Redistribution and use in source and binary forms, with or without modification, are permitted provided that the following conditions
				40 *	are met:
				42 *	
				43 * 44 *	2. Redistributions in binary form must reproduce the above copyright
				45 * 46 * 47 *	the documentation and/or other materials provided with the
				50 * 51 *	permission.
				52 * 53 * 54 * 55 *	

LOC	0.2.1 bfp-022-mu:				Subtract 17 Aug 2022 12:26:56 Page
UC	ORIECI CODE	ADDR1	ADDR2	STMT	
					HOLDER BE LIABLE FOR ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL,
					EXEMPLARY, OR CONSEQUENTIAL DAMAGES (INCLUDING, BUT NOT LIMITED TO, PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES; LOSS OF USE, DATA, OR
					PROFITS; OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND ON ANY THEORY
					OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT
					(INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE USE
					OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.
				64 *	***********************
				03	
					******************
				68 *	
				69 * 70 *	Tests the following three conversion instructions MULTIPLY AND SUBTRACT (short BFP, RRE)
				70 *	
				72 *	· · · · · · · · · · · · · · · · · · ·
				73 *	MULTIPLY AND SUBTRACT (long BFP, RXE)
				74 *	
				75 * 76 *	Test data is compiled into this program. The program itself verifies
					the resulting status of registers and condition codes via a series of
				78 *	simple CLC comparisons.
				79 *	
					Test Case Order 1) Short BFP basic tests, including traps and NaN propagation
					2) Short BFP finite number tests, including traps and scaling
				83 *	
					4) Long BFP basic tests, including traps and NaN propagation
					5) Long BFP finite number tests, including traps and scaling
				87 *	6) Long BFP FPC-controlled rounding mode exhaustive tests
					Three input test sets are provided each for short and long BFP
				89 *	inputs. Test values are the same for each precision for most
				90 *	
				91 * 92 *	
					Review of Softfloat code for multiply and add shows that the
				94 *	multiplication and addition are performed in precision-independent
					format. Overflow, underflow, inexact, and incremented are detected
					upon conversion from precision-independent format to the target format. As a result, it should not matter whether overflow etc is
					caused by the multiplication or the addition. We will include
					a few test cases where this differs in the finite testing section,
				100 *	but that's all.
				101 *	
				102 *	Also tests the following floating point support instructions  LOAD (Short)
				104 *	
				105 *	LFPC (Load Floating Point Control Register)
				106 *	·
				107 * 108 *	
				108 *	· · · · · · · · · · · · · · · · · · ·
				110 *	
				111 *	********************

C	OBJECT CODE	ADDR1	ADDR2	STMT				
				113 *	<b>,</b>			
				114 *		for c	ompatibilitv	with the z/CMS test rig, do not change
				115 *				5. Everything else is fair game.
				116 *		•	•	, ,
		0000000			BFPMULS		0	
		0000000			TRTLABL		*	
		00000000		119 F		EQU	0	Work register for cc extraction
		00000001		120 F		EQU	1	Holds sount of tost input values
		00000002 00000003		121 F 122 F		EQU	2	Holds count of test input values
		00000003		122 F		EQU EQU	4	Points to next test input value(s) Rounding tests inner loop control
		00000004		124 F		EQU	5	Rounding tests outer loop control
		00000005		125 F		EQU	6	Rounding tests top of inner loop
		00000007		126 F		EQU	7	Pointer to next result value(s)
		80000008		127 F		EQU	8	Pointer to next FPCR result
		00000009	00000001	128 F	R9	EQU	9	Rounding tests top of outer loop
		A000000A		129 F		EQU	10	Pointer to test address list
		0000000B		130 F		EQU	11	**Reserved for z/CMS test rig
		0000000C		131 F		EQU	12	Holds number of test cases in set
		000000D		132 F		EQU	13	Mainline return address
		0000000E		133 F		EQU	14	**Return address for z/CMS test rig
		0000000F	0000001	134 F 135 *		EQU	15	**Base register on z/CMS or Hyperion
						ng Poi	nt Register	equates to keep the cross reference clean
				137 *		.6 .01	ne kegister	equates to keep the cross reference execut
		00000000	00000001	138 F		EQU	0	
		00000001		139 F		EQU	1	
		00000002	00000001	140 F	PR2	EQU	2	
		0000003		141 F		EQU	3	
		00000004		142 F		EQU	4	
		00000005		143 F		EQU	5	
			00000001	144 F		EQU	6	
		00000007		145 F		EQU	/ 0	
		00000008 00000009	00000001 00000001	146 F 147 F		EQU EQU	8	
		00000003	00000001	147 F		EQU	10	
		0000000A	00000001	149 F		EQU	11	
		0000000C	00000001	150 F		EQU	12	
		000000D	00000001	151 F		EQU	13	
		0000000E	00000001	152 F		EQU	14	
		0000000F	00000001	153 F	PR15	EQU	15	

DC

A(LBFPRMOF)

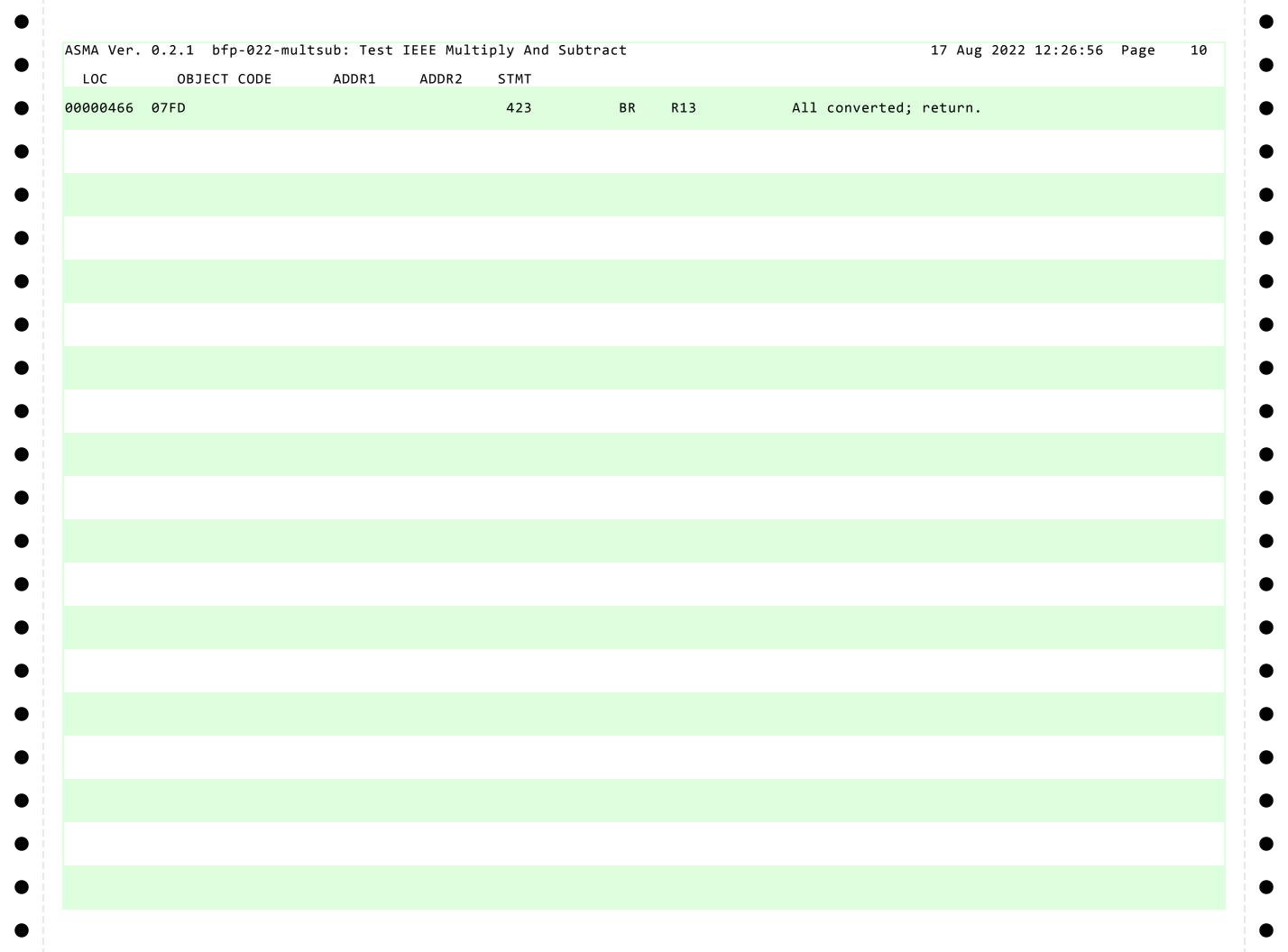
286

287 \*

00000358 0000CA00

000035C         9823 A000         0000000         312 BYR         LM R8,R9,8(R10) Get count and addr of multiplicand values get address of result area and flag area.           0000364 1222         314 BYR         LTR R2,R2 ANY test cases?         Any test cases?        No, return to caller           0000366 078D         315 SEFPNFLP DS         H         Top of outer loop - Multiplicand         Get count and start of multiplicand           0000368 0000368 000000         9845 A000         00000000         318 SEFPNFLP DS         H         Top of outer loop - Multiplicand           0000360 000000         0DC0         320 SET NR, S, O(R10)         Set top of middle loop        which are the same as the multiplicands           000036E 000036E 000000         9867 A000         0000000         322 SET NR, S, O(R10)         Set top of middle loop - multiplier           0000372 0010         325 SET NR, S, O(R10)         Set top of inner loop - subtrahend        which are the same as the multiplicands           0000372 0010         325 SET NR, S, O(R10)         Set top of inner loop - subtrahend           0000372 0010         326 *         Multiply and Add: R1 = R3 x R2 + R1           0000373 000         00000000         329 EE FPR4, O(R10)         Set top of inner loop - subtrahend           0000374 000         00000000         320 EFPN SET NR         Set exceptions non-trappable	ASMA Ver.	0.2.1 bfp-022-mu	ltsub: Test IEEE Mu	ltiply And	Subtract		17 Aug 2022 12:26:56 Page	7
290	LOC	OBJECT CODE	ADDR1 ADDR2	STMT				
291   Perform Multiply And Subtract using provided short BFP Inputs. This 292   set of tests checks NaM propagation, operations on values that are 293   not finite numbers, and other basic tests. This set generate 295   Perform Multiplicand State   Perform State					*****	******	*************	
293   not finite numbers, and other basic tests. This set generates   294   results that can be validated against Figure 19-24 on page 19-39 of   295   297   297   298   297   298   299   298   299   298   299   298   298   299   298   298   299   298   29				291 *	Perform Mult:	iply And Subtr	ract using provided short BFP inputs. This	
294				292 * 293 *	set of tests not finite n	checks NaN pr umbers, and ot	ropagation, operations on values that are ther basic tests. This set generates	
## Four results are generated for each input: one RRE with all exceptions trappable, a second RRE with all exceptions trappable, a fourth RXE with all exceptions non-trappable, a second RRE with all exceptions non-trappable, a fourth RXE with all exceptions non-trappable, a second RXE with all exceptions non-trappable, a fourth RXE with all exceptions non-trappable, and the product alf exceptions non-trappable and page 80 exceptions non-trappable and page 80 exceptions non-trappable and page 80 exceptions				294 * 295 *	results that	can be valida		
299 * a third RXE with all exceptions non-trappable, a fourth RXE with all and a sexeptions non-trappable, a fourth RXE with all and a sexeptions non-trappable, a fourth RXE with all and a sexeptions received as a sexeptions trappable.  300 * Exceptions trappable. 301 * Because this is a three-operand instruction, validation against and a sexeption of the sex o				297 *				
301				299 *	a third RXE w	with all excep		
303 * Figure 19-24, effectively an 8 x 8 x 8 table, will generate a 304 * phenomonal set of results. Namely 512 results of 16 bytes each 305 * plus 512 FPCR contents of 16 bytes each 306 * plus 512 FPCR contents of 16 bytes each 307 * The product and FPCR are stored for each result. 308 ** 309 ************************************				301 *	•	• •		
304								
307 * The product and FPCR are stored for each result.   388 *   389   ***********************************				304 * 305 *	phenomonal s	et of results.	Namely 512 results of 16 bytes each	
309				307 *	The product	and FPCR are s	stored for each result.	
000035C 9823 A000 000008 9889 A008 0000008 312 0000000 312 0000000 313 LT R R2,R2 A000 0000000 314 LTR R2,R2 Any test cases?       LTR R2,R2 Any test cases? Any test cases? Any test cases?       Any test cases? Any test cases? Any test cases?         0000366 078D 318 00000000 00000000 00000000 00000000 0000					******	******	************	
0000360 9889 A008 0000008 313 LM R8,R9,8(R10) Get address of result area and flag area. 0000366 078D 314 LTR R2,R2 Any test cases? 0000366 078D 315 SBFPNFLP DS BZR R13No, return to caller 0000368 0000368 9845 A000 0000000 318 LM R4,R5,0(R10) Get count and start of multiplier values set to multiplier values set to possible to possible to possible to multiplier values set to possible to possibl	0000035C			311 SB	FPNF DS	0H	BFP Short non-finite values tests	
Any test cases?								
316 *   317   SBFPNFLP DS				314	LTR	R2,R2	Any test cases?	
0000368         9845 A000         0000000         318 sight         LM R4,R5,0(R10) Get count and start of multiplier valueswhich are the same as the multiplicands           000036C	00000366	078D			BZK	K13	No, return to caller	
319 *which are the same as the multiplicands 320 BASR R12,0 Set top of middle loop 321 * 322 DS OH Top of middle loop - multiplier 322 DS OH R6,R7,0(R10) Get count and start of subtrahend values 324 *which are the same as the multiplicands 325 BASR R1,0 Set top of inner loop - subtrahend values 326 *which are the same as the multiplicands 327 * Multiply and Add: R1 = R3 x R2 + R1 328 *which are the same as the multiplicands 326 *which are the same as the multiplicands 327 * Multiply and Add: R1 = R3 x R2 + R1 328 *which are the same as the multiplicands 328 *which are the same as the multiplicands 329 Et top of inner loop - subtrahend 320 Et FPR1,0(,R3) Get short BFP multiplicand 320 Et FPR1,0(,R5) Get short BFP multiplier 331 *which are the same as the multiplicands 326 *which are the same as the multiplicands 327 * Multiply and Add: R1 = R3 x R2 + R1 328 *which are the same as the multiplicands 329 Et top of inner loop - subtrahend 320 Et FPR1,0(,R3) Get short BFP multiplicand 328 *which are the same as the multiplicands 329 Et top of inner loop - subtrahend 329 Et FPR1,0(,R3) Get short BFP multiplicand 329 Et FPR1,0(,R5) Get short BFP multiplicand 330 Et FPR8,0(,R7) Get short BFP subtrahend 331 Et FPR8,0(,R7) Get short BFP subtrahend 340 MSEBR FPR8,FPR4,FPR1 Multiply FPR4 by FPR1, add FPR8 RRE 331 *which are the same as the multiplicands 329 Et FPR8,0(,R7) Get short BFP subtrahend 340 MSEBR FPR8,FPR4,FPR1 Multiply FPR4 by FPR1, add FPR8 RRE 340 MSEBR FPR8,FPR4,FPR1 Multiply FPR4 by FPR1, add FPR8 RRE 340 MSEBR FPR8,FPR4,FPR1 Multiply FPR4 by FPR1, add FPR8 RRE 340 MSEBR FPR8,FPR4,FPR1 Multiply FPR4 by FPR1, add FPR8 RRE 340 MSEBR FPR8,FFR4,FPR1 Multiply FPR4 by FPR1, add FPR8 RRE 340 MSEBR FPR8,FFR4,FPR1 Multiply FPR4 by FPR1, add FPR8 RRE 340 MSEBR FPR8,FFR4,FPR1 Multiply FPR4 by FPR1, add FPR8 RRE 340 MSEBR FPR8,FFR4,FPR1 Multiply FPR4 by FPR1, add FPR8 RRE 340 MSEBR FPR8,FFR4,FPR1 Multiply FPR4 by FPR1, add FPR8 RRE 340 MSEBR FPR8,FFR4,FPR1 Multiply FPR4 by	00000368	9845 1000	99999					
321 *   322   25			000000	319 *			which are the same as the multiplicands	
Material	0000036C	0DC0			BASR	R12,0	Set top of middle loop	
324 *which are the same as the multiplicands 325 BASR R1,0 Set top of inner loop - subtrahend 326 * 327 * Multiply and Add: R1 = R3 x R2 + R1 328 *  0000374 7840 3000 00000000 329 LE FPR4,0(,R3) Get short BFP multiplicand 0000378 7810 5000 0000000 330 LE FPR1,0(,R5) Get short BFP multiplier 331 *  0000370 B29D F2F4 000002F4 332 LFPC FPCREGNT Set exceptions non-trappable 0000380 7880 7000 0000000 333 LE FPR8,0(,R7) Get short BFP subtrahend 0000384 B30F 8041 334 MSEBR FPR8,FPR4,FFR1 Multiply FPR4 by FPR1, add FPR8 RRE 0000388 7080 8000 0000000 335 STE FPR8,0(,R8) Store short BFP product-difference 0000380 B29C 9000 0000000 336 STFPC 0(R9) Store resulting FPCR flags and DXC 0000390 B29D F2F8 00000000 339 LE FPR8,0(,R7) Get short BFP subtrahend 0000394 7880 7000 0000000 339 LE FPR8,0(,R7) Get short BFP subtrahend 0000394 7880 7000 0000000 339 LE FPR8,0(,R7) Get short BFP subtrahend 0000396 B30F 8041 MSEBR FPR8,FPR4,FPR1 Multiply FPR4 by FPR1, add FPR8 RRE 0000390 R30F 8041 STE FPR8,0(,R7) Get short BFP subtrahend 0000390 R30F 8041 STE FPR8,0(,R7) Get short BFP product-difference 0000390 R30F 8041 STE FPR8,0(,R7) Get short BFP product-difference 0000390 R30F 8041 STE FPR8,4(,R8) Store short BFP product-difference 0000390 R30F 8041 STE FPR8,4(,R8) Store short BFP product-difference 0000390 R30F 8041 STE FPR8,4(,R8) Store short BFP product-difference 0000390 R30F 8041 STE FPR8,4(,R8) Store short BFP product-difference 0000390 R30F 8041 STE FPR8,4(,R8) Store short BFP product-difference 0000390 R30F 8041 STE FPR8,4(,R8) Store short BFP product-difference 0000390 R30F 8040 STEPC 4(R9) Store short BFP product-difference	0000036E			322				
BASR R1,0   Set top of inner loop - subtrahend	3000036E	9867 A000	000000		LM	R6,R7,0(R10)		
327 * Multiply and Add: R1 = R3 x R2 + R1 328 *  0000374 7840 3000 00000000 329 LE FPR4,0(,R3) Get short BFP multiplicand 0000378 7810 5000 00000000 330 LE FPR1,0(,R5) Get short BFP multiplier  000037C B29D F2F4 000002F4 332 LFPC FPCREGNT Set exceptions non-trappable 0000380 7880 7000 0000000 333 LE FPR8,0(,R7) Get short BFP subtrahend 0000384 B30F 8041	30000372	0D10		325	BASR	R1,0		
Deciding				327 *	Multiply and	Add: R1 = R3	x R2 + R1	
331 * 000037C B29D F2F4				00 329				
000037C       B29D       F2F4       000002F4       332       LFPC       FPCREGNT       Set exceptions non-trappable         0000380       7880       7000       00000000       333       LE       FPR8,0(,R7)       Get short BFP subtrahend         0000384       B30F       8041       334       MSEBR FPR8,FPR4,FPR1       Multiply FPR4 by FPR1, add FPR8 RRE         0000388       7080       8000       0000000       335       STE       FPR8,0(,R8)       Store short BFP product-difference         0000390       B29C       9000       0000000       336       STFPC       0(R9)       Store resulting FPCR flags and DXC         0000394       7880       7000       0000000       339       LE       FPR8,0(,R7)       Get short BFP subtrahend         0000390       R300       0000000       336       LE       FPR8,0(,R7)       Get short BFP subtrahend         0000390       R300       0000000       339       LE       FPR8,0(,R7)       Get short BFP subtrahend         0000390       R300       MSEBR FPR8,FPR4,FPR1       Multiply FPR4 by FPR1, add FPR8 RRE         0000390       R300       MSEBR FPR8,FPR4,FPR1       Multiply FPR4 by FPR1, add FPR8 RRE         00000390       R300       MSEBR FPR8,FPR4,FPR1       Mul	000003/8	7810 5000	000000		LE	FPR1,0(,R5)	GET SNORT BEP MUITIPLIER	
0000384       B30F       8041       334       MSEBR FPR8,FPR4,FPR1       Multiply FPR4 by FPR1, add FPR8 RRE         0000388       7080       8000       0000000       335       STE FPR8,0(,R8)       Store short BFP product-difference         000038C       B29C       9000       0000000       336       STFPC 0(R9)       Store resulting FPCR flags and DXC         0000390       B29D       F2F8       000002F8       338       LFPC       FPCREGTR       Set exceptions trappable         0000394       7880       7000       0000000       339       LE       FPR8,0(,R7)       Get short BFP subtrahend         0000398       B30F       8041       MSEBR FPR8,FPR4,FPR1       Multiply FPR4 by FPR1, add FPR8 RRE         0000390       7080       8004       00000004       341       STE       FPR8,4(,R8)       Store short BFP product-difference         00003A0       B29C       9004       00000004       342       STFPC       4(R9)       Store resulting FPCR flags and DXC	0000037C			F4 332				
0000388       7080       00000000       335       STE       FPR8,0(,R8)       Store short BFP product-difference         000038C       B29C       9000       0000000       336       STFPC       0(R9)       Store resulting FPCR flags and DXC         0000390       B29D       F2F8       000002F8       338       LFPC       FPCREGTR       Set exceptions trappable         0000394       7880       7000       0000000       339       LE       FPR8,0(,R7)       Get short BFP subtrahend         0000398       B30F       8041       MSEBR       FPR8,FPR4,FPR1       Multiply       FPR4       by FPR1, add       FPR8         000039C       7080       8004       00000004       341       STE       FPR8,4(,R8)       Store short BFP product-difference         00003A0       B29C       9004       00000004       342       STFPC       4(R9)       Store resulting       FPCR       flags and       DXC	00000380 00000384		000000					
337 *  0000390 B29D F2F8	00000388	7080 8000		00 335	STE	FPR8,0(,R8)	Store short BFP product-difference	
0000390 B29D F2F8 000002F8 338 LFPC FPCREGTR Set exceptions trappable 0000394 7880 7000 0000000 339 LE FPR8,0(,R7) Get short BFP subtrahend 0000398 B30F 8041 340 MSEBR FPR8,FPR4,FPR1 Multiply FPR4 by FPR1, add FPR8 RRE 000039C 7080 8004 0000004 341 STE FPR8,4(,R8) Store short BFP product-difference 00003A0 B29C 9004 00000004 342 STFPC 4(R9) Store resulting FPCR flags and DXC	0000038C	R5AC A000	000000		STFPC	0(R9)	Store resulting FPCR flags and DXC	
340 MSEBR FPR8,FPR4,FPR1 Multiply FPR4 by FPR1, add FPR8 RRE 000039C 7080 8004 00000004 341 STE FPR8,4(,R8) Store short BFP product-difference 00003A0 B29C 9004 00000004 342 STFPC 4(R9) Store resulting FPCR flags and DXC	00000390			F8 338				
00000004 341 STE FPR8,4(,R8) Store short BFP product-difference 00003A0 B29C 9004	00000394 00000398		000000					
บบบปังหับ BZ9C 9004	0000039C	7080 8004		04 341	STE	FPR8,4(,R8)	Store short BFP product-difference	
	000003A0	B29C 9004	000000		STFPC	4(R9)	Store resulting FPCR flags and DXC	

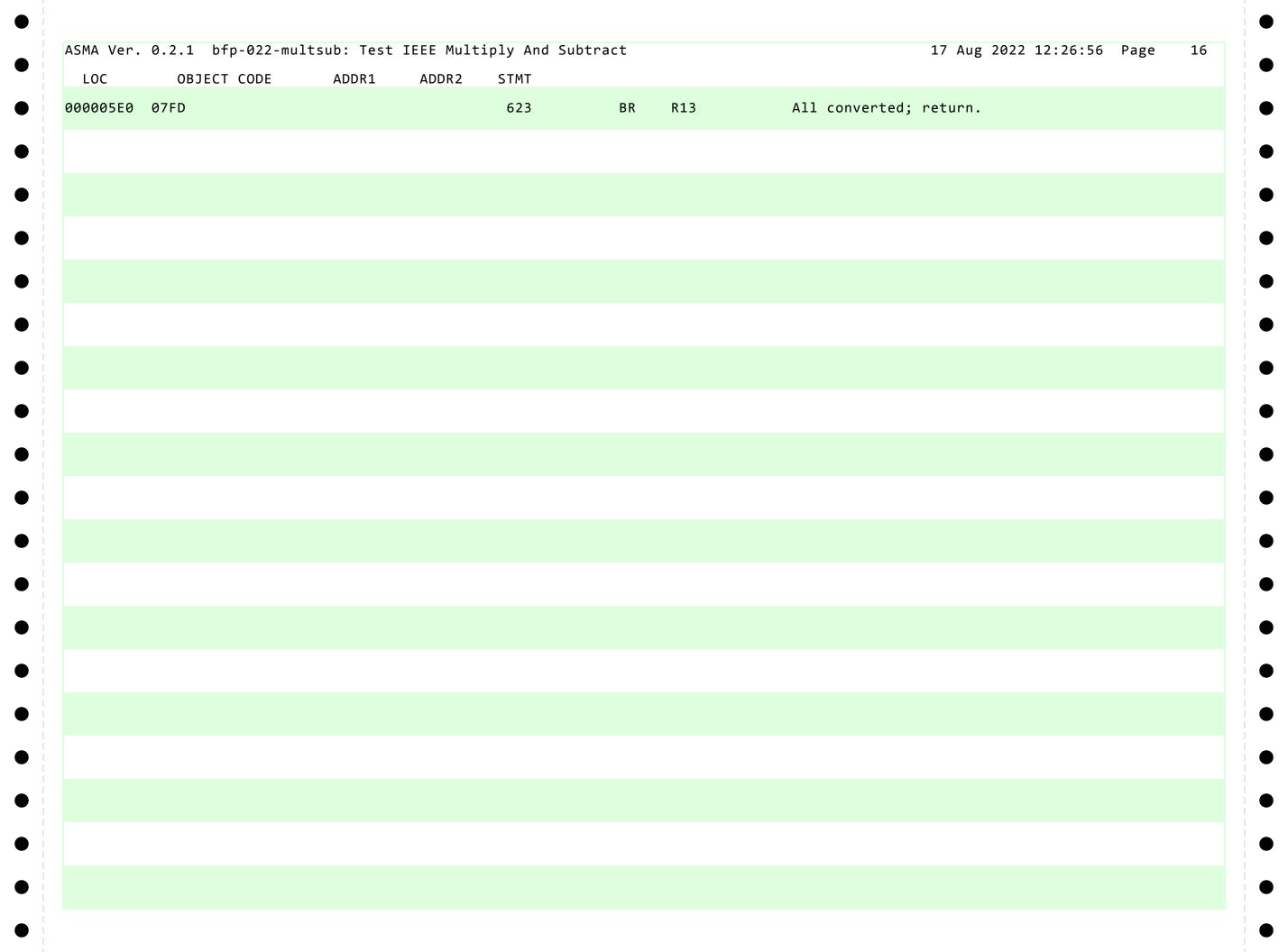
ASMA Ver.	0.2.1 bfp-022-mu	ltsub: Test IEEE	Multiply	And Sub	tract		17 Aug 2022 12:26:56 Page	9
LOC	OBJECT CODE	ADDR1 ADD	R2 STN	1T				
				58 ***** 59 *	******	******	************	
			37 37 37	70 * Per 71 * Thi 72 * Ine	s set of	tests triggers	act using provided short BFP input triples. IEEE exceptions Overflow, Underflow, and trap and non-trap results.	
			37				for each input: one RRE with all a second RRE with all exceptions trappable,	
			37 37	'6 * a t		with all excep	tions non-trappable, a fourth RXE with all	
			37 38	'9 * The 80 *	·		<pre>tored for each result. ************************************</pre>	
			30	<b>Σ</b>				
000003EE	9823 A000 9878 A008		0000 38 0008 38	33 SBFPF	LM LM	R2,R3,0(R10) R7,R8,8(R10)	Get count and address of test input values Get address of result area and flag area.	
000003F2 000003F6 000003F8	1222	0000	38	35	LTR BZR	R2, R2 R13	Any test cases?No, return to caller	
000003FA	0DC0		38 38	37 38 *	BASR	R12,0	Set top of loop	
000003FC			02F4 38			FPCREGNT	Set exceptions non-trappable	
	7840 3000 7810 3004		0000 39 0004 39		LE LE	FPR4,0(,R3) FPR1 1*4( R3)	Get short BFP multiplicand Get short BFP multiplier	
	7880 3008		0008 39		LE		Get short BFP subtrahend	
	B30F 8041		39				1 Multiply FPR4 by FPR1, add FPR8 RRE	
00000410 00000414	7080 7000 B29C 8000		0000 39 0000 39	95	STE STFPC	FPR8,0(,R7) 0(R8)	Store short BFP product-difference Store resulting FPCR flags and DXC	
00000418	B29D F2F8	0000	39 02F8 39	96 * 97	LFPC	FPCREGTR	Set exceptions trappable	
0000041C	7880 3008	0000	0008 39 39	)8 )9 *	LE	FPR8,2*4(,R3)	Reload short BFP subtrahendmultiplier is still in FPR1,	
				90 *			multiplicand is still in FPR4	
00000420	B30F 8041			)1			1 Multiply short FPR8 by FPR1 RRE	
	7080 7004		0004 40		STE		Store short BFP product-difference	
00000428	B29C 8004	0000	0004 40 40	)4 *	SIFPC	4(R8)	Store resulting FPCR flags and DXC	
0000042C 00000430	B29D F2F4 7880 3008		02F4 40 0008 40	96	LFPC LE	FPCREGNT FPR8,2*4(,R3)	Set exceptions non-trappable Reload short BFP subtrahend	
				)7 *			multiplicand is still in FPR4	
00000434	ED40 3004 800F		0004 46		MSEB		R3) Mult. FPR4 by multiplier, add FPR8 RXE	
0000043A 0000043E	7080 7008 B29C 8008		0008 40 0008 41	L <b>0</b>	STE STFPC	FPR8,2*4(,R/) 8(R8)	Store short BFP product Store resulting FPCR flags and DXC	
00000442	B29D F2F8	9999	41 02F8 41	1 *	l FPC	FPCREGTR	Set exceptions trappable	
00000446	7880 3008		0008 41	L3	LE		Reload short BFP subtrahend	
				4 *		, ,,	multiplicand is still in FPR4	
0000044A	ED40 3004 800F		0004 41				R3) Mult. FPR4 by multiplier, add FPR8 RXE	
00000450 00000454	7080 700C B29C 800C		000C 41 000C 41		STE	FPR8,3*4(,R7) 12(R8)	Store short BFP product Store resulting FPCR flags and DXC	
00000434	D27C 000C	9996		L7 L8 *	31770	12(110)	Store resulting from itags alla DAC	
00000458	4130 300C		000C 41	_9	LA	R3,3*4(,R3)	Point to next input value trible	
	4170 7010		0010 42		LA	R7,4*4(,R7)	Point to next product result set	
00000460 00000464	4180 8010	0000	0010 42		LA RCTP	R8,4*4(,R8)	Point to next FPCR result set	
4040000	062C		42		BCTR	R2,R12	Convert next input value.	



ASMA Ver.	0.2.1	bfp-022-mults	ub: Test 1	[EEE Multi;	oly And	Subtract	t		17 Aug 2022 12:26:56 Page 1
LOC	ОВЈІ	ECT CODE	ADDR1	ADDR2	STMT				
					.05	*****	*****	******	***********
					490 *	Danfann	M7 4. 4	مسلمان کیما	and veine provided lane DED impute. This
									act using provided long BFP inputs. This opagation, operations on values that are
									ther basic tests. This set generates
					494 *	results	that	can be valida	ted against Figure 19-24 on page 19-39 of
					496 *	SA22-783			
									for each input: one RRE with all
					499 *	a third	RXE v	vith all excep	a second RRE with all exceptions trappable, tions non-trappable, a fourth RXE with all
					501 *	•		rappable.	
									erand instruction, validation against an 8 x 8 x 8 table, will generate a
									Namely 512 results of 32 bytes each
					505 *				16 bytes each.
					506 *				
					507 * 508 *	The pro	duct a	and FPCR are s	tored for each result.
					508 * 509 **	*****	*****	******	***********
					303				
000004D6					511 LB	FPNF I	DS	0H	BFP long non-finite values tests
000004D6				00000000	512		LM	R2,R3,0(R10)	Get count and addr of multiplicand values
000004DA		308		00000008	513		LM	R8,R9,8(R10)	Get address of result area and flag area.
000004DE 000004E0	1222 078D				514 515		LTR BZR	R2,R2 R13	Any test cases?No, return to caller
00000410	070D				516 *	!	DZI	KIJ	No, return to carrer
000004E2						FPNFLP I	DS	0H	Top of outer loop - Multiplicand
000004E2	9845 A	<b>300</b>		00000000	518	ĺ	LM	R4,R5,0(R10)	
00000456	0000				519 *		DACD	D12 0	which are the same as the multiplicands
000004E6	anca				520 521 *	1	BASR	R12,0	Set top of middle loop
000004E8					522	I	DS	0H	Top of middle loop - multiplier
000004E8	9867 A	900		00000000	523		LM	R6,R7,0(R10)	Get count and start of subtrahend values
					524 *				which are the same as the multiplicands
000004EC	0D10				525 526 *		BASR	R1,0	Set top of inner loop - subtrahend
					526 * 527 * 528 *	Multiply	y and	Add: R1 = R3	x R2 + R1
000004EE	7840 30	900		00000000	529		LE	FPR4,0(,R3)	Get long BFP multiplicand
000004F2	7810 50			00000000	530 531 *		LE	FPR1,0(,R5)	Get long BFP multiplier
000004F6	B29D F2			000002F4	532			FPCREGNT	Set exceptions non-trappable
000004FA	6880 70			00000000	533		LD	FPR8,0(,R7)	Get long BFP subtrahend
000004FE 00000502	B31F 80			00000000	534 535		MSDBR STD	FPR8, FPR4, FPR FPR8, 0(,R8)	11 Multiply FPR4 by FPR1, add FPR8 RRE Store long BFP product-difference
00000506	B29C 90			00000000	536 537 *			0(R9)	Store resulting FPCR flags and DXC
0000050A	B29D F2	2F8		000002F8	538		LFPC	FPCREGTR	Set exceptions trappable
0000050E	7880 70	<b>000</b>		00000000	539	!	LE	FPR8,0(,R7)	Get long BFP subtrahend
00000512	B31F 80			00000000	540				Multiply FPR4 by FPR1, add FPR8 RRE
00000516 0000051A	6080 80 B29C 90			00000008 00000004	541 542		STD STEPC	1*4(R9)	Store long BFP product-difference Store resulting FPCR flags and DXC
00000JIA	5250 50	00-		3000004	543 *	•	J 1 1 C	<u> </u>	Store resulting frenchings and bit
					J 13				

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
0000051E 00000522	B29D F2F4 7880 7000		000002F4 00000000	544 545	LFPC LE	FPR8,0(,R7)	Set exceptions non-trappable Get long BFP subtrahend
00000526 0000052C 00000530	ED40 5000 801F 6080 8010 B29C 9008		00000000 00000010 00000008	546 547 548	MSDB STD STFPC		R5) Mult. FPR4 by multiplier, add FPR8 RXE Store long BFP product-difference Store resulting FPCR flags and DXC
00000534 00000538	B29D F2F8 7880 7000		000002F8 00000000	549 * 550 551	LFPC LE	FPCREGTR FPR8,0(,R7)	Set exceptions trappable Get long BFP subtrahend
0000053C 00000542 00000546	ED40 5000 801F 6080 8018 B29C 900C		00000000 00000018 0000000C	552 553 554	MSDB STD STFPC		R5) Mult. FPR4 by multiplier, add FPR8 RXE Store long BFP product-difference Store resulting FPCR flags and DXC
0000054A 0000054E	4180 8020 4190 9010		00000020 00000010	555 * 556 557	LA LA	R8,4*8(,R8) R9,4*4(,R9)	Point to next product-diff. result area Point to next FPCR contents area
00000552 00000556	4170 7008 0661		00000008	558 559 560 *	LA BCTR	R7,8(,R7) R6,R1	Point to next subtrahend value Loop through subtrahend values
00000558 0000055C	4150 5008 064C		00000008	561 562 563 *	LA BCTR	R5,8(,R5) R4,R12	Point to next multiplier Loop through multiplier values
0000055E 00000562 00000566	4130 3008 4620 F4E2 07FD		00000008 000004E2	564 565 566	LA BCT BR	R3,8(,R3) R2,LBFPNFLP R13	Point to next multiplicand Loop through multiplicand values All converted; return.

	·	ltsub: Test IEEE Multi		btract		17 Aug 2022 12:26:56 Page	15
LOC	OBJECT CODE	ADDR1 ADDR2	STMT				
			568 **** 569 *	******	******	*************	
			570 * Pe 571 * Th	is set of	tests triggers	ract using provided long BFP input triples.  S IEEE exceptions Overflow, Underflow, and trap and trap results.	
						d for each input: one RRE with all a second RRE with all exceptions trappable,	
				third RXE ceptions t		otions non-trappable, a fourth RXE with all	
			580 *	·		stored for each result.  ***********************************	
	9823 A000	00000000	583 LBFP		R2,R3,0(R10)		
00000570		00000008	584 585	LM LTR	R7,R8,8(R10) R2,R2	Any test cases?	
00000572			586	BZR	R13	No, return to caller	
00000574	anca		587 588 *	BASK	R12,0	Set top of loop	
00000576	B29D F2F4	000002F4	589		FPCREGNT	Set exceptions non-trappable	
	6840 3000	00000000	590	LD	FPR4,0(,R3)	Get long BFP multiplicand	
0000057E	6810 3008 6880 3010	00000008 00000010	591 592	LD LD	FPR1,8(,R3) FPR8,16(,R3)	Get long BFP multiplier Get long BFP subtrahend	
	B31F 8041	00000010	593			R1 Multiply FPR4 by FPR1, add FPR8 RRE	
0000058A	6080 7000 B29C 8000	00000000 00000000	594 595	STD	FPR8,0(,R7) 0(R8)	Store long BFP product Store resulting FPCR flags and DXC	
0000038L	D29C 8000	99999999	596 *	31170	O(NO)	Store resulting fren flags and bac	
00000592	B29D F2F8 6880 3010	000002F8 00000010	597 598	LFPC LD	<pre>FPCREGTR FPR8,16(,R3)</pre>	Set exceptions trappable Reload long BFP subtrahend	
00000330	0880 3010	00000010	599 *	LU	FPN0,10(,N3)	multiplier is still in FPR1,	
	5345 0044		600 *	46000	5000 5004 500	multiplicand is still in FFR4	
0000059A 0000059E	B31F 8041 6080 7008	0000000	601 602		FPR8, FPR4, FPR		
0000059E	B29C 8004	00000008 00000004	603		FPR8,8(,R7) 1*4(R8)	Store long BFP product-difference Store resulting FPCR flags and DXC	
000005A6	B29D F2F4	000002F4	604 * 605		FPCREGNT	Set exceptions non-trappable	
000005AA	6880 3010	00000010	606 607 *	LD	FPR8,16(,R3)	Reload long BFP subtrahendmultiplicand is still in FFR4	
000005AE	ED40 3008 801F	00000008	608			R3) Mult. FPR4 by multiplier, add FPR8 RXE	
000005B4 000005B8	6080 7010 B29C 8008	00000010 00000008	609 610	STD STFPC	FPR8,2*8(,R7) 2*4(R8)	) Store long BFP product-difference Store resulting FPCR flags and DXC	
000005BC	B29D F2F8	000002F8	611 * 612		FPCREGTR	Set exceptions trappable	
000005C0	6880 3010	00000218	613	LD	FPR8,16(,R3)	Reload long BFP subtrahend	
000005C4	ED40 3008 801F	00000008	614 * 615	MSDB	FPR8,FPR4.8(.	multiplicand is still in FFR4 ,R3) Mult. FPR4 by multiplier, add FPR8 RXE	
000005CA	6080 7018	00000018	616	STD	FPR8,3*8(,R7)	) Store long BFP product-difference	
000005CE	B29C 800C	0000000C	617 618 *	21140	3*4(R8)	Store resulting FPCR flags and DXC	
000005D2		00000018	619	LA	R3,3*8(,R3)	Point to next input value triple	
000005D6 000005DA	4170 7020 4180 8010	00000020 00000010	620 621	LA LA	R7,4*8(,R7)	Point to next product-diff. result set Point to next FPCR result area	
	062C	01000010	622	BCTR	R8,4*4(,R8) R2,R12	Convert next input value.	



ASMA Ver.	0.2.1 bfp-022-mu	ıltsub: Test IE	EE Multip	ply Ar	nd Subtra	ct		17 Aug 2022 12:26:56 Page	17
LOC	OBJECT CODE	ADDR1	ADDR2	STMT					
				626 627 628 629 630 631	* Perform * tests 6 * The row * * All five	m Mult exhaus unding ve FPC	iply using pro tively tests a mode can only rounding mode	************************************  vided long BFP input pairs. This set of ll rounding modes available for Multiply. be specified in the FPC.  s are tested because the preceeding tests, do not often create results that require	
				634 635 636 637	* Two res * and one * * The pro	sults a	Traps are di	for each input and rounding mode: one RRE sabled for all rounding mode tests.	
						*****	******	*************	
000005E2 000005E6 000005EA	9878 A008		0000000	642 643 644	LBFPRM	LM LM LTR	R2,R3,0(R10) R7,R8,8(R10) R2,R2	Get count and address of test input values Get address of result area and flag area. Any test cases?	
000005EC 000005EE 000005F0	078D 1711			645 646 647		BZR XR	R13 R1,R1 R12,0	No, return to caller Zero register 1 for use in IC/STC/indexing Set top of test case loop	
000005F2 000005F6	4150 0005 0D90	0	0000005	648 649 650 651	*	LA BASR	R5,FPCMCT R9,0	Get count of FPC modes to be tested Set top of rounding mode loop	
000005F8	4315 F64B	0	000064B	652 653		IC	R1,FPCMODES-L	'FPCMODES(R5) Get next FPC mode	
000005FC 00000600 00000604 00000608 0000060C	B29D F2F4 B2B8 1000 6840 3000 6810 3008 6880 3010	0 0 0	00002F4 0000000 0000000 0000008 0000010	654 655 656 657 658		SRNMB LD LD	FPCREGNT 0(R1) FPR4,0(,R3) FPR1,8(,R3) FPR8,16(,R3)	Set exceptions non-trappable, clear flags Set FPC Rounding Mode Get long BFP multiplicand Get long BFP multiplier Get long BFP subtrahend	
00000610 00000614 00000618	B31F 8041 6080 7000 B29C 8000	0	0000000	659 660 661 662	*	MSDBR STD STFPC	FPR8, FPR4, FPR FPR8, 0(,R7) 0(R8)	1 Multiply FPR4 by FPR1, add FPR8 RRE Store long BFP product-difference Store resulting FPCR flags and DXC	
0000061C 00000620 00000624 00000628 0000062E	B29D F2F4 B2B8 1000 6880 3010 ED40 3008 801F 6080 7008	0 0 0	00002F4 00000000 0000010 0000008	663 664 665 666 667		SRNMB LD MSDB STD	FPR8,8(,R7)	R3) Multiply long FPR8 by multiplier RXE	
00000632 00000636 0000063A	B29C 8004 4170 7010	0	0000004	668 669 670 671			4(R8) R7,2*8(,R7) R8,2*4(,R8)	Store resulting FPCR flags and DXC  Point to next product result set  Point to next FPCR result area	
0000063E	0659			676	* * End of * skip e	FPC me	ytes of FPCR r	Iterate to next FPC mode  ted. Advance to next test case. We will esult area so that each set of five result at a memory address ending in zero for the	
					* convent		of memory dump		

oma ver.	0.2.1 bfp-022-mu	10000		, ,			17 Aug 2022 12:26:56 Page 18
LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
	4130 3018 4180 8008 062C		00000018 00000008	680 681 682	LA LA BCTR	R3,3*8(,R3) R8,8(,R8) R2,R12	Point to next input value triple Skip to start of next FPCR result area Multiply next input value lots of times
0064A	07FD			683 * 684	BR	R13	All converted; return.
						5	

ASMA Ver.	0.2.1 bfp-022-mu	ıltsub: Test	IEEE Multi	iply And Subtract 17 Aug 2022 12:26:56 Page 19
LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				686 ***********************************
				688 * Table of FPC rounding modes to test product rounding modes. 689 *
				690 * The Set BFP Rounding Mode does allow specification of the FPC 691 * rounding mode as an address, so we shall index into a table of
				692 * BFP rounding modes without bothering with Execute. 693 *
				694 ************************************
				696 * 697 * Rounding modes that may be set in the FPCR. The FPCR controls 698 * rounding of the product. 699 *
				700 * These are indexed directly by the loop counter, which counts down. 701 * So the modes are listed in reverse order here.
0000064C				702 * 703 FPCMODES DS 0C
0000064C				704 DC AL1(7) RFS, Round for shorter precision
0000064E	03 02 01			705 DC AL1(3) RM, Round to -infinity 706 DC AL1(2) RP, Round to +infinity 707 DC AL1(1) RZ, Round to zero
00000650	00	00000005	00000001	708 DC AL1(0) RNTE, Round to Nearest, ties to even
				, 10

ASMA Ver.	0.2.1 bfp-022-m	ultsub: Test I	IEEE Multi	ply And Sub	tract		17 Aug 2022 12:26:56 Page	20
LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
				712 ***** 713 *	*******	*******	*************	
					rt BFP te	est data sets for	Multiply And Subtract testing.	
				717 * NaN	l propagat	tion, and results	sed for tests of basic functionality, from operations involving other than to the top of	
				719 * mu]		d, multiplier, an	d subtrahend, resulting in 8 x 8 x 8 or	
							used for testing boundary conditions	
				724 * and 725 * thi		result (normal,	lues. Each possible condition code scaled, etc) is created by members of	
							sed for exhaustive testing of final ding modes available for the Multiply	
				729 * ins 730 *	struction.			
				732 * mu] 733 * tha	ltiplicand at by 1/16	d with some one-b 5 (0.0625). In B	e rounding mode testing is to use a pits in the low-order byte and multiply sFP, this will have the effect of shifting	
				735 * int	o the hig	gh-order portion	e target precision representation and of the bits that control rounding. The determined by the rounding desired.	
					******	***********	**************	
				740 ***** 741 *	*******	*******	*************	
				743 * zer	o inputs.	. Member values	o test operations using non-finite or chosen to validate Figure 19-24 on page	
				745 * mu]		d, multiplier, an	value in this table is used as the day subtrahend. Eight entries menas 512	
				747 *			*************	
00000654				750 SBFPN	JETN DC	0F	Inputs for short BFP non-finite tests	
00000654 00000658	FF800000 C0000000			750 36FFN 751 752	DC DC	X'FF800000' X'C0000000'	-inf -2.0	
0000065C 00000660	80000000 00000000			753 754	DC DC	X'80000000' X'00000000'	-0 +0	
00000664 00000668 0000066C	7F800000			755 756 757	DC DC DC	X'40000000' X'7F800000' X'FFCB0000'	+2.0 +inf -QNaN	
00000670		00000008	00000001	757 758 759 SBFPN	DC	X'7F8A0000' (*-SBFPNFIN)/4	+SNaN Count of short BFP in list	
						, , , , , , , , , , , , , , , , , , ,		
				761 ***** 762 *	*******	*******	*************	
				-	ond input	test data set.	These are finite triples intended to	

	·						
LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
				872 *			
000006E0	2590000			873	DC	VIDEGGGGG	Multiplicand 11 000001420511474600275
000006E4	3F80000C 3F880000				DC	X'3F80000C'	Multiplicand +1.000001430511474609375
				874 875	DC	X'3F880000'	Multiplier 1.0625 (1/16) Subtrahend -0.5
000006E8	BF000000				DC	X'BF000000'	
000006EC				876	DC	X'BF80000C'	Multiplicand -1.000001430511474609375
000006F0	3F880000			877	DC	X'3F880000'	Multiplier 1.0625 (1/16)
000006F4	31000000			878	DC	X'3F000000'	Subtrahend +0.5
				8/9 <sup>↑</sup> nea	rest is	away trom zero	, incremented.
00000650	3500000			880 *	D.C	V.I.3.E.0.0.0.0.1	Multipliand of 000000476027450202425
000006F8	3F800008			881	DC	X'3F800008'	Multiplicand +1.000000476837158203125
000006FC				882	DC	X'3F880000'	Multiplier 1.0625 (1/16)
00000700	BF000000			883	DC	X'BF000000'	Subtrahend -0.5
00000704	BF800008			884	DC	X'BF800008'	Multiplicand -1.000000476837158203125
00000708	3F880000			885	DC	X'3F880000'	Multiplier 1.0625 (1/16)
0000070C	3F000000			886	DC .	X'3F000000'	Subtrahend +0.5
					rest is	a tie, nearest	even has lower magnitude
	2=22224			888 *		VI. 2 2 2 2 2 4 2 1	
00000710	3F800018			889	DC	X'3F800018'	Multiplicand +1.000002384185791015625
00000714				890	DC	X'3F880000'	Multiplier 1.0625 (1/16)
00000718	BF000000			891	DC	X'BF000000'	Subtrahend -0.5
0000071C	BF800018			892	DC	X'BF800018'	Multiplicand -1.000002384185791015625
00000720	3F880000			893	DC	X'3F880000'	Multiplier 1.0625 (1/16)
00000724	3F000000			894	DC	X'3F000000'	Subtrahend +0.5
				895 *nea 896 *	rest is	a tie, nearest	even has greater magnitude
		00000008	00000001		ICT EQU	(*-SBFPINRM)/	4/3 Count of short BFP rounding tests

	·		IEEE Multi	ply And Subtract 17 Aug 2022 12:26:56 Page 2
LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				899 ***********************************
				901 * Long BFP test data sets for Multiply And Subtract testing.
				902 * 903 * The first test data set is used for tests of basic functionality,
				904 * NaN propagation, and results from operations involving other than 905 * finite numbers. 906 *
				907 * The second test data set is used for testing boundary conditions 908 * using two finite non-zero values. Each possible condition code 909 * and type of result (normal, scaled, etc) is created by members of
				910 * this test data set. 911 *
				912 * The third test data set is used for exhaustive testing of final
				913 * results across the five rounding modes available for the Add 914 * instruction. 915 *
				916 * See the Short BFP test cases header for a discussion of test case 917 * selection for rounding mode test case values. 918 *
				918 <sup></sup> 919 ***********************************
				921 ************************************
				923 * First input test data set, to test operations using non-finite or 924 * zero inputs. Member values chosen to validate Figure 19-24 on page 925 * 19-39 of SA22-7832-10. Each value in this table is used as the 926 * multiplicand, multiplier, and subtrahend. Eight entries menas 512
				927 * result sets. 928 *
				929 *********************
0000728 0000728 0000730	FFF00000 00000000 C0000000 00000000			931 LBFPNFIN DS
0000738	8000000 00000000			934 DC X'8000000000000000000' -0
0000740 0000748				935 DC X'0000000000000000000' +0 936 DC X'400000000000000' +2.0
0000750	7FF00000 00000000			937 DC X'7FF0000000000000000' +inf
	FFF8B000 00000000 7FF0A000 00000000			938 DC X'FFF8B0000000000' -QNaN 939 DC X'7FF0A0000000000' +SNaN
		00000008	00000001	940 LBFPNFCT EQU (*-LBFPNFIN)/8 Count of long BFP in list
				942 ********************
				943 * 944 * Second input test data set. These are finite triples intended to
				945 * trigger overflow, underflow, and inexact exceptions. Each triples is 946 * added twice, once non-trappable and once trappable. Trappable 947 * overflow or underflow yields a scaled result. Trappable inexact
				948 * will show whether the Incremented DXC code is returned. 949 *

```
ASMA Ver. 0.2.1 bfp-022-multsub: Test IEEE Multiply And Subtract
                                                                                                                                25
                                                                                                   17 Aug 2022 12:26:56 Page
  LOC
            OBJECT CODE
                                        ADDR2
                              ADDR1
                                                 STMT
                                                  951 * 1. Overflow
                                                  952 * 2. Underflow - normal inputs
                                                  953 * 3. Underflow - subnormal inputs
                                                  954 * 4. Normal - from subnormal inputs
955 * 5. Inexact - incremented
                                                  956 * 6. Inexact - truncated
                                                  957 *
                                                  958 ************************
00000768
                                                                                       Inputs for long BFP finite tests
                                                  960 LBFPIN
                                                               DS
                                                  961 *
                                                  962 * Overflow on multiplication two ways. Once on the muliplication step,
                                                  963 * and then a second time on the addition step.
                                                  964 *
00000768 7FEFFFFF FFFFFFF
                                                  965
                                                               DC
                                                                     X'7FEFFFFFFFFFFFF
                                                                                          +Nmax
00000770 FFEFFFFF FFFFFFF
                                                  966
                                                               DC
                                                                     X'FFEFFFFFFFFFFF'
                                                                                          -Nmax
00000778 3FF00000 00000000
                                                  967
                                                               DC
                                                                     X'3FF00000000000000'
                                                                                          +1.0
                                                  968 *
00000780 7FDFFFFF FFFFFFF
                                                  969
                                                               DC
                                                                     X'7FDFFFFFFFFFFF'
                                                                                          +Nmax / 2
                                                  970
                                                               DC
                                                                                          -2.0
00000788 C0000000 00000000
                                                                     X'C00000000000000000'
00000790 7FEFFFFF FFFFFFF
                                                  971
                                                               DC
                                                                     X'7FEFFFFFFFFFFF'
                                                                                          +Nmax
                                                  972 *
                                                  973 * Underflow from product of normals. We will multiply two small
                                                  974 * normals to generate a subnormal, and then subtract a large subnormal.
                                                  975 *
00000798
         00100000 00000000
                                                  976
                                                               DC
                                                                     X'00100000000000000'
                                                                                          +Nmin
000007A0 00100000 00000000
                                                  977
                                                               DC
                                                                     X'00100000000000000'
                                                                                          +Nmin
                                                                     X'0008000000000001'
000007A8 00080000 00000001
                                                  978
                                                               DC
                                                                                          A very large subnormal
                                                  979 *
                                                  980 * Underflow from the product of a subnormal and a normal.
                                                  981 *
                                                  982
                                                                     X'3FE00000000000000'
000007B0 3FE00000 00000000
                                                               DC
                                                                                          +0.5
000007B8
                                                  983
                                                               DC
                                                                     X'000FFFFFFFFFFF'
         000FFFFF FFFFFFF
                                                                                          +Dmax subnormal
                                                                                         +Dmin, will appear in result
000007C0 00000000 00000001
                                                  984
                                                               DC
                                                                     X'00000000000000001'
                                                  985 *
                                                  986 * We cannot generate a normal result from product of subnormals
                                                  987 * because the result will be smaller than both the multiplicand and the
                                                  988 * multiplier. So we'll try multiplying +Dmax by 2. The result should
                                                  989 * be +Nmin
                                                  990 *
991
                                                               DC
                                                                     X'000FFFFFFFFFFF
                                                                                          +Dmax
000007D0 40000000 00000000
                                                  992
                                                               DC
                                                                                          +2.0, result should be normal
                                                                     X'40000000000000000'
000007D8 00080000 00000000
                                                  993
                                                               DC
                                                                     X'0008000000000000' A large subnormal
                                                  994 *
                                                  995 * Multiply a value from 1.0 such that the added digits are to the right
                                                  996 * of the right-most bit in the stored significand. The result will be
                                                  997 * inexact, and incremented will be determined by the value of the
                                                  998 * bits in the multiplier.
                                                  999 *
000007E0 3FF00000 0000000C
                                                 1000
                                                               DC
                                                                     X'3FF0000000000000C'
                                                                                          Multiplicand +1, aka 1.0b0
000007E8 3FF10000 00000000
                                                 1001
                                                               DC
                                                                     X'3FF10000000000000'
                                                                                          Multiplier 1.0625 (1/16)
000007F0 BFE00000 00000000
                                                               DC
                                                                     X'BFE00000000000000'
                                                                                          -0.5
                                                 1002
                                                 1003 *..nearest is away from zero, incremented.
                                                 1004 *
000007F8 3FF00000 00000007
                                                 1005
                                                               DC
                                                                     X'3FF0000000000007' Multiplicand +1, aka 1.0b0
```

```
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                                                                                               17 Aug 2022 12:26:56 Page
                                                                                                                            26
 LOC
            OBJECT CODE
                             ADDR1
                                       ADDR2
                                               STMT
00000800 3FF10000 00000000
                                               1006
                                                             DC
                                                                  X'3FF10000000000000'
                                                                                      Multiplier 1.0625 (1/16)
00000808 BFE00000 00000000
                                               1007
                                                             DC
                                                                  X'BFE000000000000' -0.5
                                               1008 *..nearest is toward zero, truncated.
                                               1009 *
                            00000007 00000001 1010 LBFPCT
                                                            EQU
                                                                  (*-LBFPIN)/8/3 Count of long BFP triples in list
                                               1013 *
                                               1014 * Third input test data set. These are finite triples intended to
                                               1015 * test all combinations of rounding mode for the product and the
                                               1016 * remainder. Values are chosen to create a requirement to round
                                               1017 * to the target precision after the computation and to generate
                                               1018 * varying results depending on the rounding mode in the FPCR.
                                               1019 *
                                               1020 * The result set will have cases that represent each of the following
                                               1021 *
                                               1022 * 1. Positive, nearest magnitude is toward zero.
                                               1023 * 2. Negative, nearest magnitude is toward zero.
                                               1024 * 3. Positive, nearest magnitude is away from zero.
                                               1025 * 4. Negative, nearest magnitude is away from zero.
                                               1026 * 5. Positive, tie, nearest even has greater magnitude
                                               1027 * 6. Negative, tie, nearest even has greater magnitude
                                               1028 * 7. Positive, tie, nearest even has lower magnitude
                                               1029 * 8. Negative, tie, nearest even has lower magnitude
                                               1030 *
                                               1031 * Round For Shorter precision correctness can be determined from the
                                               1032 * above test cases.
                                               1033 *
                                               00000810
                                               1036 LBFPINRM DS
                                               1037 *
                                               1038 * Multiply a value from 1.0 such that the added digits are to the right
                                               1039 * of the right-most bit in the stored significand. The result will be
                                               1040 * inexact, and incremented will be determined by the value of the
                                               1041 * bits in the multiplier.
                                               1042 *
00000810 3FF00000 00000007
                                               1043
                                                             DC
                                                                  X'3FF00000000000007'
                                                                                      Multiplicand
00000818 3FF10000 00000000
                                                             DC
                                                                                      Multiplier 1.0625 (1/16)
                                               1044
                                                                  X'3FF10000000000000'
                                                             DC
00000820 BFE00000 00000000
                                               1045
                                                                  X'BFE00000000000000'
                                                                                      -0.5
00000828
         BFF00000 00000007
                                               1046
                                                             DC
                                                                  X'BFF00000000000007'
                                                                                      Multiplicand
00000830 3FF10000 00000000
                                               1047
                                                             DC
                                                                  X'3FF10000000000000'
                                                                                      Multiplier 1.0625 (1/16)
00000838 3FE00000 00000000
                                               1048
                                                             DC
                                                                  X'3FE00000000000000'
                                                                                      +0.5
                                               1049 *..nearest is toward zero, truncated.
                                               1050 *
                                                             DC
00000840 3FF00000 0000000C
                                               1051
                                                                  X'3FF0000000000000C'
                                                                                      Multiplicand
                                                                                      Multiplier 1.0625 (1/16)
00000848 3FF10000 00000000
                                               1052
                                                             DC
                                                                  X'3FF10000000000000'
00000850 BFE00000 00000000
                                               1053
                                                             DC
                                                                  X'BFE00000000000000'
                                                                                      -0.5
         BFF00000 0000000C
                                                             DC
                                                                  X'BFF000000000000C'
00000858
                                               1054
                                                                                      Multiplicand
00000860 3FF10000 00000000
                                                             DC
                                               1055
                                                                  X'3FF10000000000000'
                                                                                      Multiplier 1.0625 (1/16)
00000868 3FE00000 00000000
                                               1056
                                                             DC
                                                                  X'3FE00000000000000'
                                                                                      +0.5
                                               1057 *..nearest is away from zero, incremented.
```

Description		•					
1078 * ACTUAL results saved here	LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
1880 *   1881 *   1882 *   1883 *   1884 *   1884 *   1885 *   1885 *   1885 *   1885 *   1885 *   1885 *   1886 *   1							
1082 *   1083   1083   1083   1083   1083   1083   1083   1084   1083   1085   1084   1085   1085   1084   1085					1080 *		
00001000						LOCACIONS FOR ACT	UAL results
00003000   00000001   1085   SBFPRMOF   EQU   STRTLABL+X'5000'   Short BFP   finite results   1090			00001000	00000001	1083 SBFPNFOT EQU	STRTLABL+X'1000'	
1089 *  room for 16 tests, 7 used   1090   FPCR flags and DXC from short BFP   1091 *  room for 16 tests, 7 used   1092 *  room for 16 tests, 7 used   1092 *  room for 16 tests, 7 used   1093   SBFPRMO   EQU   STRTLABL+X'5200'   Short BFP rounding mode test results  Room for 16, 8 used.  room for 512 tests, 512 used  room for 16 tests, 7 used  roo			00003000	00000001	1086 *	STRTLABL+X'3000'	
1091 *			00005000	00000001		STRTLABL+X'5000'	
1092 *   1093   SBFPRMO   EQU   STRTLABL+X'5200'   Short BFP rounding mode test results   1094 *   Room for 16, 8 used.   Short BFP rounding mode FPCR results   1095   SBFPRMOF   EQU   STRTLABL+X'5500'   Short BFP rounding mode FPCR results   1096 *   Room for 16, 8 used.   Ro			00005100	00000001		STRTLABL+X'5100'	FPCR flags and DXC from short BFP
1094 *Room for 16, 8 used.  1095 SBFPRMOF EQU STRTLABL+X'5500' Short BFP rounding mode FPCR resultsRoom for 16, 8 usedRoom for 16, 8 usednext location starts at X'5800'next location starts at X'5800'room for 512 tests, 512 used  1100 *room for 512 tests, 512 used					1092 *		·
00005500 0000001 1095 SBFPRMOF EQU STRTLABL+X'5500' Short BFP rounding mode FPCR resultsRoom for 16, 8 used. 1097 *next location starts at X'5800' 1098 *  00006000 0000001 1099 LBFPNFOT EQU STRTLABL+X'6000' Long non-finite BFP resultsroom for 512 tests, 512 used  0000A000 0000001 1101 LBFPNFFL EQU STRTLABL+X'A000' FPCR flags and DXC from long BFProom for 512 tests, 512 used  1103 *room for 512 tests, 512 used  1103 *room for 512 tests, 512 used  0000C000 00000001 1104 LBFPOUT EQU STRTLABL+X'C000' Long BFP finite resultsroom for 16 tests, 7 used  0000C200 00000001 1106 LBFPFLGS EQU STRTLABL+X'C200' FPCR flags and DXC from long BFProom for 16 tests, 7 used			00005200	00000001		STRTLABL+X'5200'	
1097 *next location starts at X'5800' 1098 *  00006000 0000001 1099 LBFPNFOT EQU STRTLABL+X'6000' Long non-finite BFP results 1100 *room for 512 tests, 512 used  0000A000 0000001 1101 LBFPNFFL EQU STRTLABL+X'A000' FPCR flags and DXC from long BFP 1102 *room for 512 tests, 512 used  1103 *  0000C000 0000001 1104 LBFPOUT EQU STRTLABL+X'C000' Long BFP finite results 1105 *room for 16 tests, 7 used  0000C200 0000001 1106 LBFPFLGS EQU STRTLABL+X'C200' FPCR flags and DXC from long BFP 1107 *room for 16 tests, 7 used			00005500	00000001	1095 SBFPRMOF EQU	STRTLABL+X'5500'	Short BFP rounding mode FPCR results
1100 *room for 512 tests, 512 used  0000A000 0000001 1101 LBFPNFFL EQU STRTLABL+X'A000' FPCR flags and DXC from long BFP 1102 *room for 512 tests, 512 used 1103 *  0000C000 00000001 1104 LBFPOUT EQU STRTLABL+X'C000' Long BFP finite results 1105 *room for 16 tests, 7 used  0000C200 00000001 1106 LBFPFLGS EQU STRTLABL+X'C200' FPCR flags and DXC from long BFP 1107 *room for 16 tests, 7 used							next location starts at X'5800'
0000A000 0000001 1101 LBFPNFFL EQU STRTLABL+X'A000' FPCR flags and DXC from long BFP 1102 *room for 512 tests, 512 used 1103 * 0000C000 00000001 1104 LBFPOUT EQU STRTLABL+X'C000' Long BFP finite results 1105 *room for 16 tests, 7 used 0000C200 00000001 1106 LBFPFLGS EQU STRTLABL+X'C200' FPCR flags and DXC from long BFP 1107 *room for 16 tests, 7 used			00006000	00000001		STRTLABL+X'6000'	
1103 * 0000C000 0000001 1104 LBFPOUT EQU STRTLABL+X'C000' Long BFP finite results 1105 *room for 16 tests, 7 used 0000C200 00000001 1106 LBFPFLGS EQU STRTLABL+X'C200' FPCR flags and DXC from long BFP 1107 *room for 16 tests, 7 used			000A000	00000001	1101 LBFPNFFL EQU	STRTLABL+X'A000'	FPCR flags and DXC from long BFP
1105 *room for 16 tests, 7 used 0000C200 00000001 1106 LBFPFLGS EQU STRTLABL+X'C200' FPCR flags and DXC from long BFP 1107 *room for 16 tests, 7 used					1103 *		·
1107 *room för 16 tests, 7 used			0000C000		1105 *		room for 16 tests, 7 used
1108 *			0000C200	00000001		STRTLABL+X'C200'	
0000C500 00000001 1109 LBFPRMO EQU STRTLABL+X'C500' Long BFP rounding mode test results Room for 16, 8 used.			0000C500		1110 *		Room for 16, 8 used.
0000CA00 00000001 1111 LBFPRMOF EQU STRTLABL+X'CA00' Long BFP rounding mode FPCR results			0000CA00	00000001		STRTLABL+X'CA00'	
1112 *Room for 16, 8 used. 1113 *next location starts at X'CD00'							

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LOC	OBJECT CODE	ADDR1	ADDR2	TMT				
	7FCA0000 40000000			2011				
	4B4B4B40 60D8D581				DC CL48'QNaN/+SNaN/+inf'			
	7FCA0000 7F800000				DC XL16'7FCA00007F8000007FCA00007F800000'			
	4B4B4B40 60D8D581				DC CL48'QNaN/+SNaN/-QNaN'			
	7FCA0000 FFCB0000				DC XL16'7FCA0000FFCB00007FCA0000FFCB0000'			
	4B4B4B40 60D8D581 7FCA0000 7F8A0000			2016 2017				
	4B4B4B40 4EE2D581			2018				
	7FCA0000 FF800000				DC XL16'7FCA0000FF8000007FCA0000FF800000'			
	4B4B4B40 4EE2D581				DC CL48' +SNaN/-inf/-2.0'			
	7FCA0000 C0000000				DC XL16'7FCA0000C00000007FCA0000C0000000'			
00017080	4B4B4B40 4EE2D581			2022	DC CL48' +SNaN/-inf/-0'			
	7FCA0000 80000000			2023				
	4B4B4B40 4EE2D581			2024				
	7FCA0000 00000000				DC XL16'7FCA0000000000007FCA000000000000'			
	4B4B4B40 4EE2D581				DC CL48' +SNaN/-inf/+2.0'			
	7FCA0000 40000000 4B4B4B40 4EE2D581			2027	DC XL16'7FCA000040000007FCA000040000000'			
	7FCA0000 7F800000			2028 2029	<pre>DC CL48' +SNaN/-inf/+inf' DC XL16'7FCA00007F8000007FCA00007F800000'</pre>			
	4B4B4B40 4EE2D581			2030				
	7FCA0000 FFCB0000				DC XL16'7FCA0000FFCB00007FCA0000FFCB0000'			
	4B4B4B40 4EE2D581				DC CL48' +SNaN/-inf/+SNaN'			
	7FCA0000 7F8A0000				DC XL16'7FCA00007F8A00007FCA00007F8A0000'			
	4B4B4B40 4EE2D581			2034				
00017230	7FCA0000 FF800000			2035				
	4B4B4B40 4EE2D581			2036	·			
	7FCA0000 C0000000			2037				
	4B4B4B40 4EE2D581			2038				
	7FCA0000 80000000			2039	DC XL16'7FCA000080000007FCA000080000000'			
	4B4B4B40 4EE2D581 7FCA0000 00000000			2040	·			
	4B4B4B40 4EE2D581				DC XL16'7FCA0000000000007FCA000000000000' DC CL48' +SNaN/-2.0/+2.0'			
	7FCA0000 40000000				DC XL16'7FCA000040000007FCA000040000000'			
	4B4B4B40 4EE2D581				DC CL48' +SNaN/-2.0/+inf'			
	7FCA0000 7F800000				DC XL16'7FCA00007F8000007FCA00007F800000'			
	4B4B4B40 4EE2D581				DC CL48' +SNaN/-2.0/-QNaN'			
000173B0	7FCA0000 FFCB0000			2047	DC XL16'7FCA0000FFCB00007FCA0000FFCB0000'			
000173C0	4B4B4B40 4EE2D581			.048	DC CL48' +SNaN/-2.0/+SNaN'			
	7FCA0000 7F8A0000				DC XL16'7FCA00007F8A00007FCA00007F8A0000'			
	4B4B4B40 4EE2D581				DC CL48' +SNaN/-0/-inf'			
	7FCA0000 FF800000				DC XL16'7FCA0000FF8000007FCA0000FF800000'			
	4B4B4B40 4EE2D581				DC CL48' +SNaN/-0/-2.0'			
	7FCA0000 C0000000 4B4B4B40 4EE2D581				DC XL16'7FCA0000C00000007FCA0000C0000000' DC CL48' +SNaN/-0/-0'			
	7FCA0000 80000000				DC XL16'7FCA000080000007FCA000080000000'			
	4B4B4B40 4EE2D581				DC CL48' +SNaN/-0/+0'			
	7FCA0000 00000000				DC XL16'7FCA000000000007FCA000000000000'			
	4B4B4B40 4EE2D581				DC CL48' +SNaN/-0/+2.0'			
	7FCA0000 40000000				DC XL16'7FCA0000400000007FCA000040000000'			
	4B4B4B40 4EE2D581				DC CL48' +SNaN/-0/+inf'			
	7FCA0000 7F800000				DC XL16'7FCA00007F8000007FCA00007F800000'			
	4B4B4B40 4EE2D581				DC CL48' +SNaN/-0/-QNaN'			
	7FCA0000 FFCB0000				DC XL16'7FCA0000FFCB00007FCA0000FFCB0000'			
	4B4B4B40 4EE2D581 7FCA0000 7F8A0000			2064 2065	DC CL48' +SNaN/-0/+SNaN'			
000175F0	TELLINGUM TEXTINGUM			Mh	DC XL16'7FCA00007F8A00007FCA00007F8A0000'			

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
0017D30	7FCA0000 40000000			2123 DC XL16'7FCA000040000007FCA000040000000'			
0017D40				2124 DC CL48' +SNaN/-QNaN/+inf'			
0017D70				2125 DC XL16'7FCA00007F8000007FCA00007F800000'			
0017D80				2126 DC CL48' +SNaN/-QNaN/-QNaN'			
	7FCA0000 FFCB0000			2127 DC XL16'7FCA0000FFCB00007FCA0000FFCB0000'			
	4B4B4B40 4EE2D581 7FCA0000 7F8A0000			2128 DC CL48' +SNaN/-QNaN/+SNaN' 2129 DC XL16'7FCA00007F8A00007FCA00007F8A0000'			
	4B4B4B40 4EE2D581			2130 DC CL48' +SNaN/+SNaN/-inf'			
	7FCA0000 FF800000			2131 DC XL16'7FCA0000FF8000007FCA0000FF800000'			
	4B4B4B40 4EE2D581			2132 DC CL48' +SNaN/+SNaN/-2.0'			
	7FCA0000 C0000000			2133 DC XL16'7FCA0000C0000007FCA0000C00000000'			
	4B4B4B40 4EE2D581			2134 DC CL48' +SNaN/+SNaN/-0'			
	7FCA0000 80000000			2135 DC XL16'7FCA000080000007FCA000080000000'			
	4B4B4B40 4EE2D581			2136 DC CL48' +SNaN/+SNaN/+0'			
	7FCA0000 00000000			2137 DC XL16'7FCA000000000007FCA0000000000000			
	4B4B4B40 4EE2D581 7FCA0000 40000000			2138 DC CL48' +SNaN/+SNaN/+2.0' 2139 DC XL16'7FCA000040000007FCA000040000000'			
	4B4B4B40 4EE2D581			2140 DC CL48' +SNaN/+SNaN/+inf'			
	7FCA0000 7F800000			2141 DC XL16'7FCA00007F8000007FCA00007F800000'			
	4B4B4B40 4EE2D581			2142 DC CL48' +SNaN/+SNaN/-QNaN'			
	7FCA0000 FFCB0000			2143 DC XL16'7FCA0000FFCB00007FCA0000FFCB0000'			
	4B4B4B40 4EE2D581			2144 DC CL48' +SNaN/+SNaN/+SNaN'			
0017FF0	7FCA0000 7F8A0000			2145 DC XL16'7FCA00007F8A00007FCA00007F8A0000'			
		00000200	00000001	2146 SBFPNFOT_NUM EQU (*-SBFPNFOT_GOOD)/64			
				2147 *			
		00018000	00000001	2148 * 2149 SBFPNFFL GOOD EQU *	MSEBR/MSEB NF		
00018000	4B4B4B40 60899586	00018000	00000001	2150 DC CL48'inf/-inf/-inf FPCR'	MSEBRYMSEB WI		
0018030				2151 DC XL16'0000000F80000000000000F8000000'			
0018040	4B4B4B40 60899586			2152 DC CL48'inf/-inf/-2.0 FPCR'			
0018070				2153 DC XL16'00000000F800000000000000F8000000'			
	4B4B4B40 60899586			2154 DC CL48'inf/-inf/-0 FPCR'			
	00000000 F8000000			2155 DC XL16'00000000F80000000000000F8000000'			
	4B4B4B40 60899586			2156 DC CL48'inf/-inf/+0 FPCR'			
00180F0				2157 DC XL16'00000000F80000000000000F8000000'			
	4B4B4B40 60899586 00000000 F8000000			2158 DC CL48'inf/-inf/+2.0 FPCR' 2159 DC XL16'00000000F800000000000000F8000000'			
	4B4B4B40 60899586			2160 DC CL48'inf/-inf/+inf FPCR'			
0018170				2161 DC XL16'00800000F800800000800000F8008000'			
	4B4B4B40 60899586			2162 DC CL48'inf/-inf/-QNaN FPCR'			
00181B0				2163 DC XL16'00000000F800000000000000F8000000'			
	4B4B4B40 60899586			2164 DC CL48'inf/-inf/+SNaN FPCR'			
	00800000 F8008000			2165 DC XL16'00800000F800800000800000F8008000'			
	4B4B4B40 60899586			2166 DC CL48'inf/-2.0/-inf FPCR'			
0018230				2167 DC XL16'0000000F80000000000000F8000000'			
	4B4B4B40 60899586			2168 DC CL48'inf/-2.0/-2.0 FPCR'			
0018270 0018280	00000000 F8000000 4B4B4B40 60899586			2169 DC XL16'00000000F800000000000000F8000000' 2170 DC CL48'inf/-2.0/-0 FPCR'			
	00000000 F8000000			2170 DC CL481111/-2.0/-0 FFCN 2171 DC XL16'00000000F800000000000000F8000000'			
	4B4B4B40 60899586			2171 DC XL10 000000001 000000000000000000000000			
00182F0				2173 DC XL16'0000000F80000000000000F8000000'			
				2174 DC CL48'inf/-2.0/+2.0 FPCR'			
0018300				2175 DC XL16'00000000F800000000000000F8000000'			
	00000000 F8000000						
	4B4B4B40 60899586			2176 DC CL48'inf/-2.0/+inf FPCR'			
0018330 0018340 0018370							

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
001FAB0	00800000 F8008000			3131 DC XL16'00800000F800800000800000F8008000'			
001FAC0	4B4B4B40 4EE2D581			3132 DC CL48' +SNaN/+inf/+0 FPCR'			
001FAF0	00800000 F8008000			3133 DC XL16'00800000F800800000800000F8008000'			
001FB00				3134 DC CL48' +SNaN/+inf/+2.0 FPCR'			
001FB30	00800000 F8008000			3135 DC XL16'00800000F800800000800000F8008000'			
				3136 DC CL48' +SNaN/+inf/+inf FPCR'			
001FB70	00800000 F8008000 4B4B4B40 4EE2D581			3137 DC XL16'00800000F800800000800000F8008000' 3138 DC CL48' +SNaN/+inf/-QNaN FPCR'			
001FBB0	00800000 F8008000			3139 DC XL16'00800000F800800000800000F8008000'			
				3140 DC CL48' +SNaN/+inf/+SNaN FPCR'			
001FBF0				3141 DC XL16'00800000F800800000800000F8008000'			
	4B4B4B40 4EE2D581			3142 DC CL48' +SNaN/-QNaN/-inf FPCR'			
001FC30	00800000 F8008000			3143 DC XL16'00800000F800800000800000F8008000'			
	4B4B4B40 4EE2D581			3144 DC CL48' +SNaN/-QNaN/-2.0 FPCR'			
				3145 DC XL16'00800000F800800000800000F8008000'			
				3146 DC CL48' +SNaN/-QNaN/-0 FPCR'			
001FCB0				3147 DC XL16'00800000F800800000800000F8008000'			
	4B4B4B40 4EE2D581 00800000 F8008000			3148 DC CL48' +SNaN/-QNaN/+0 FPCR' 3149 DC XL16'00800000F800800000800000F8008000'			
	4B4B4B40 4EE2D581			3150 DC CL48' +SNaN/-QNaN/+2.0 FPCR'			
001FD30	00800000 F8008000			3151 DC XL16'00800000F800800000800000F8008000'			
001FD40				3152 DC CL48' +SNaN/-QNaN/+inf FPCR'			
001FD70	00800000 F8008000			3153 DC XL16'00800000F800800000800000F8008000'			
				3154 DC CL48' +SNaN/-QNaN/-QNaN FPCR'			
001FDB0	00800000 F8008000			3155 DC XL16'00800000F800800000800000F8008000'			
				3156 DC CL48' +SNaN/-QNaN/+SNaN FPCR'			
001FDF0	00800000 F8008000			3157 DC XL16'00800000F800800000800000F8008000'			
001FE00				3158 DC CL48' +SNaN/+SNaN/-inf FPCR'			
001FE30	00800000 F8008000			3159 DC XL16'00800000F800800000800000F8008000'			
	4B4B4B40 4EE2D581			3160 DC CL48' +SNaN/+SNaN/-2.0 FPCR'			
	00800000 F8008000 4B4B4B40 4EE2D581			3161 DC XL16'00800000F800800000800000F8008000' 3162 DC CL48' +SNaN/+SNaN/-0 FPCR'			
	00800000 F8008000			3163 DC XL16'00800000F800800000800000F8008000'			
	4B4B4B40 4EE2D581			3164 DC CL48' +SNaN/+SNaN/+0 FPCR'			
				3165 DC XL16'00800000F800800000800000F8008000'			
	4B4B4B40 4EE2D581			3166 DC CL48' +SNaN/+SNaN/+2.0 FPCR'			
001FF30	00800000 F8008000			3167 DC XL16'00800000F800800000800000F8008000'			
	4B4B4B40 4EE2D581			3168 DC CL48' +SNaN/+SNaN/+inf FPCR'			
				3169 DC XL16'00800000F800800000800000F8008000'			
	4B4B4B40 4EE2D581			3170 DC CL48' +SNaN/+SNaN/-QNaN FPCR'			
	00800000 F8008000			3171 DC XL16'00800000F800800000800000F8008000'			
	4B4B4B40 4EE2D581 00800000 F8008000			3172 DC CL48' +SNaN/+SNaN/+SNaN FPCR' 3173 DC XL16'00800000F800800000800000F8008000'			
0011110	00800000 18008000	99999999	00000001	3174 SBFPNFFL NUM EQU (*-SBFPNFFL GOOD)/64			
		00000200	0000001	3175 *			
				3176 *			
		00020000	00000001	3177 SBFPOUT_GOOD EQU *			
	D4E2C5C2 D961D4E2			3178 DC CL48'MSEBR/MSEB F Ovfl 1'			
	FF800000 DF7FFFE			3179 DC XL16'FF800000DF7FFFFEFF800000DF7FFFFE'			
	D4E2C5C2 D961D4E2			3180 DC CL48'MSEBR/MSEB F Ovfl 2'			
	FF800000 9FFFFFF			3181 DC XL16'FF8000009FFFFFFFF8000009FFFFFFF'			
	D4E2C5C2 D961D4E2			3182 DC CL48'MSEBR/MSEB F Ufl 1'			
				3183 DC XL16'80400001E000000280400001E0000002'			
	D4E2C5C2 D961D4E2 003FFFFE 5FFFFFA			3184 DC CL48'MSEBR/MSEB F Ufl 2' 3185 DC XL16'003FFFFE5FFFFFA003FFFFE5FFFFA'			
MMINIALIA				DIOD DC VEID AADILLLEDLLLLLLAAADLLLLEDLLLLLLA			

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
0020130	00BFFFFE 00BFFFFE			3187 DC XL16'00BFFFFE00BFFFFE00BFFFFE'			
0020140	D4E2C5C2 D961D4E2			3188 DC CL48'MSEBR/MSEB F Incr'			
0020170	BFC8000D BFC8000D			3189 DC XL16'BFC8000DBFC8000DBFC8000D'			
				3190 DC CL48'MSEBR/MSEB F Trun'			
00201B0	BFC80007 BFC80007	0000007	00000001	3191 DC XL16'BFC80007BFC80007BFC80007'			
		00000007	00000001	3192 SBFPOUT_NUM EQU (*-SBFPOUT_GOOD)/64 3193 *			
				3194 *			
		000201C0	00000001	3195 SBFPFLGS GOOD EQU *			
00201C0	D4E2C5C2 D961D4E2	00020100	0000001	3196 DC CL48'MSEBR/MSEB F Ovfl 1 FPCR'			
00201F0	00280000 F8002800			3197 DC XL16'00280000F800280000280000F8002800'			
	D4E2C5C2 D961D4E2			3198 DC CL48'MSEBR/MSEB F Ovfl 2 FPCR'			
0020230	00280000 F8002000			3199 DC XL16'00280000F800200000280000F8002000'			
				3200 DC CL48'MSEBR/MSEB F Ufl 1 FPCR'			
	00180000 F8001C00			3201 DC XL16'00180000F8001C0000180000F8001C00'			
				3202 DC CL48'MSEBR/MSEB F Ufl 2 FPCR'			
	00180000 F8001000			3203 DC XL16'00180000F800100000180000F8001000'			
				3204 DC CL48'MSEBR/MSEB F Nmin FPCR' 3205 DC XL16'00000000F80000000000000F8000000'			
				3206 DC CL48'MSEBR/MSEB F Incr FPCR'			
				3207 DC XL16'00080000F8000C0000080000F8000C00'			
				3208 DC CL48'MSEBR/MSEB F Trun FPCR'			
0020370	00080000 F8000800			3209 DC XL16'00080000F800080000080000F8000800'			
		00000007	00000001	3210 SBFPFLGS_NUM EQU (*-SBFPFLGS_GOOD)/64			
				3211 *			
				3212 *			
		00020380	00000001	3213 SBFPRMO_GOOD EQU *			
	D4E2C5C2 D961D4E2			3214 DC CL48'MSEBR/MSEB RM +NZ RNTE, RZ'			
00203B0	3FC80007 3FC80007			3215 DC XL16'3FC800073FC800073FC80007'			
	D4E2C5C2 D961D4E2 3FC80008 3FC80008			3216 DC CL48'MSEBR/MSEB RM +NZ RP, RM' 3217 DC XL16'3FC800083FC800083FC800073FC80007'			
	D4E2C5C2 D961D4E2			3218 DC CL48'MSEBR/MSEB RM +NZ RFS'			
	3FC80007 3FC80007			3219 DC XL16'3FC800073FC80007000000000000000000000000000000000			
	D4E2C5C2 D961D4E2			3220 DC CL48'MSEBR/MSEB RM -NZ RNTE, RZ'			
	BFC80007 BFC80007			3221 DC XL16'BFC80007BFC80007BFC80007BFC80007'			
	D4E2C5C2 D961D4E2			3222 DC CL48'MSEBR/MSEB RM -NZ RP, RM'			
	BFC80007 BFC80007			3223 DC XL16'BFC80007BFC80007BFC80008BFC80008'			
	D4E2C5C2 D961D4E2			3224 DC CL48'MSEBR/MSEB RM -NZ RFS'			
				3225 DC XL16'BFC80007BFC80007000000000000000000000000000000000			
	D4E2C5C2 D961D4E2			3226 DC CL48'MSEBR/MSEB RM +NA RNTE, RZ'			
	3FC8000D 3FC8000D D4E2C5C2 D961D4E2			3227 DC XL16'3FC8000D3FC8000D3FC8000C3FC8000C' 3228 DC CL48'MSEBR/MSEB RM +NA RP, RM'			
	3FC8000D 3FC8000D			3229 DC XL16'3FC8000D3FC8000D3FC8000C3FC8000C'			
	D4E2C5C2 D961D4E2			3230 DC CL48'MSEBR/MSEB RM +NA RFS'			
	3FC8000D 3FC8000D			3231 DC XL16'3FC8000D3FC8000D000000000000000000000000000000000			
	D4E2C5C2 D961D4E2			3232 DC CL48'MSEBR/MSEB RM -NA RNTE, RZ'			
	BFC8000D BFC8000D			3233 DC XL16'BFC8000DBFC8000DBFC8000CBFC8000C'			
	D4E2C5C2 D961D4E2			3234 DC CL48'MSEBR/MSEB RM -NA RP, RM'			
	BFC8000C BFC8000C			3235 DC XL16'BFC8000CBFC8000CBFC8000DBFC8000D'			
	D4E2C5C2 D961D4E2			3236 DC CL48'MSEBR/MSEB RM -NA RFS'			
				3237 DC XL16'BFC8000DBFC8000D000000000000000000000000000000000			
	D4E2C5C2 D961D4E2			3238 DC CL48'MSEBR/MSEB RM +TZ RNTE, RZ'			
	3FC80008 3FC80008			3239 DC XL16'3FC800083FC800083FC80008'			
שטמשעששו	D4E2C5C2 D961D4E2			3240 DC CL48'MSEBR/MSEB RM +TZ RP, RM' 3241 DC XL16'3FC800093FC800093FC800083FC80008'			
	3FC80009 3FC80009			3 // I III XI IP 3 FL XNNNA 3 FL			

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00020730	3FC80009 3FC80009			3243 DC XL16'3FC800093FC800090000000000000000000000			
00020740				3244 DC CL48'MSEBR/MSEB RM -TZ RNTE, RZ'			
00020770				3245 DC XL16'BFC80008BFC80008BFC80008'			
	D4E2C5C2 D961D4E2			3246 DC CL48'MSEBR/MSEB RM -TZ RP, RM'			
000207B0	BFC80008 BFC80008 D4E2C5C2 D961D4E2			3247 DC XL16'BFC80008BFC80008BFC80009BFC80009' 3248 DC CL48'MSEBR/MSEB RM -TZ RFS'			
000207C0 000207F0				3249 DC XL16'BFC80009BFC80009000000000000000000000000000000000			
	D4E2C5C2 D961D4E2			3250 DC CL48'MSEBR/MSEB RM +TA RNTE, RZ'			
	3FC8001A 3FC8001A			3251 DC XL16'3FC8001A3FC8001A3FC800193FC80019'			
	D4E2C5C2 D961D4E2			3252 DC CL48'MSEBR/MSEB RM +TA RP, RM'			
00020870				3253 DC XL16'3FC8001A3FC8001A3FC800193FC80019'			
	D4E2C5C2 D961D4E2			3254 DC CL48'MSEBR/MSEB RM +TA RFS'			
	3FC80019 3FC80019			3255 DC XL16'3FC800193FC8001900000000000000000000			
	D4E2C5C2 D961D4E2			3256 DC CL48'MSEBR/MSEB RM -TA RNTE, RZ'			
000208F0	BFC8001A BFC8001A D4E2C5C2 D961D4E2			3257 DC XL16'BFC8001ABFC8001ABFC80019BFC80019'			
00020930				3258 DC CL48'MSEBR/MSEB RM -TA RP, RM' 3259 DC XL16'BFC80019BFC80019BFC8001ABFC8001A'			
	D4E2C5C2 D961D4E2			3260 DC CL48'MSEBR/MSEB RM -TA RFS'			
00020970				3261 DC XL16'BFC80019BFC800190000000000000000000000000000000000			
	2. 000022 2. 000022	00000018	00000001	3262 SBFPRMO NUM EQU (*-SBFPRMO GOOD)/64			
				3263 *			
				3264 *			
		00020980	00000001	3265 SBFPRMOF_GOOD EQU *			
00020980	D4E2C5C2 D961D4E2			3266 DC CL48'MSEBR/MSEB RM +NZ RNTE, RZ FPCR'			
000209B0	00080000 00080000			3267 DC XL16'0008000000080000008000100080001'			
000209C0 000209F0	D4E2C5C2 D961D4E2 00080002			3268 DC CL48'MSEBR/MSEB RM +NZ RP, RM FPCR' 3269 DC XL16'00080002000800020008000300080003'			
	D4E2C5C2 D961D4E2			3270 DC CL48'MSEBR/MSEB RM +NZ RFS FPCR'			
00020A30				3271 DC XL16'00080007000800070000000000000000000000			
	D4E2C5C2 D961D4E2			3272 DC CL48'MSEBR/MSEB RM -NZ RNTE, RZ FPCR'			
00020A70				3273 DC XL16'0008000000080000008000100080001'			
	D4E2C5C2 D961D4E2			3274 DC CL48'MSEBR/MSEB RM -NZ RP, RM FPCR'			
	00080002 00080002			3275 DC XL16'00080002000800020008000300080003'			
	D4E2C5C2 D961D4E2			3276 DC CL48'MSEBR/MSEB RM -NZ RFS FPCR'			
	00080007 00080007			3277 DC XL16'00080007000800070000000000000000000000			
	D4E2C5C2 D961D4E2			3278 DC CL48'MSEBR/MSEB RM +NA RNTE, RZ FPCR'			
00020B30	00080000 00080000 D4E2C5C2 D961D4E2			3279 DC XL16'0008000000080000008000100080001' 3280 DC CL48'MSEBR/MSEB RM +NA RP, RM FPCR'			
	00080002 00080002			3281 DC XL16'00080002000800020008000300080003'			
	D4E2C5C2 D961D4E2			3282 DC CL48'MSEBR/MSEB RM +NA RFS FPCR'			
	00080007 00080007			3283 DC XL16'00080007000800070000000000000000000000			
00020BC0	D4E2C5C2 D961D4E2			3284 DC CL48'MSEBR/MSEB RM -NA RNTE, RZ FPCR'			
00020BF0				3285 DC XL16'0008000000080000008000100080001'			
	D4E2C5C2 D961D4E2			3286 DC CL48'MSEBR/MSEB RM -NA RP, RM FPCR'			
	00080002 00080002			3287 DC XL16'00080002000800020008000300080003'			
	D4E2C5C2 D961D4E2			3288 DC CL48'MSEBR/MSEB RM -NA RFS FPCR'			
	00080007 00080007 D4E2C5C2 D961D4E2			3289 DC XL16'00080007000800070000000000000000000000			
00020C80 00020CB0				3291 DC XL16'0008000000080000008000100080001'			
	D4E2C5C2 D961D4E2			3292 DC CL48'MSEBR/MSEB RM +TZ RP, RM FPCR'			
	00080002 00080002			3293 DC XL16'00080002000800020008000300080003'			
	D4E2C5C2 D961D4E2			3294 DC CL48'MSEBR/MSEB RM +TZ RFS FPCR'			
00020D30	00080007 00080007			3295 DC XL16'00080007000800070000000000000000000			
	D4E2C5C2 D961D4E2			3296 DC CL48'MSEBR/MSEB RM -TZ RNTE, RZ FPCR'			
	00080000 00080000			3297 DC XL16'0008000000080000008000100080001'			
00020D80	D4E2C5C2 D961D4E2			3298 DC CL48'MSEBR/MSEB RM -TZ RP, RM FPCR'			

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT						
00020DB0	00080002 00080002			3299						
00020DC0	D4E2C5C2 D961D4E2			3300	•					
00020DF0				3301						
00020E00				3302	·					
00020E30 00020E40				3303 3304						
00020E70				3305						
00020E80	D4E2C5C2 D961D4E2			3306						
00020EB0				3307	·					
00020EC0				3308						
00020EF0	00080000 00080000			3309						
00020F00	D4E2C5C2 D961D4E2			3310	DC CL48'MSEBR/MSEB RM -TA RP, RM FPCR'					
00020F30					DC XL16'00080002000800020008000300080003'					
00020F40					DC CL48'MSEBR/MSEB RM -TA RFS FPCR'					
00020F70	00080007 00080007				DC XL16'000800070008000700000000000000000000					
		00000018	00000001		SBFPRMOF_NUM EQU (*-SBFPRMOF_GOOD)/64					
				3315						
		00020F80	00000001	3316	LBFPNFOT GOOD EQU *					
00020F80	D4E2C4C2 D940D5C6	00020100	00000001		DC CL48'MSDBR NF -inf/-inf/-inf'					
00020FB0				3319	• • •					
00020FC0				3320						
	7FF00000 00000000			3321						
00021000	D4E2C4C2 D940D5C6			3322						
	7FF00000 00000000			3323						
00021040				3324						
	7FF00000 00000000			3325						
00021080				3326						
	7FF00000 00000000			3327						
000210C0	D4E2C4C2 40D5C640 7FF00000 00000000			3328 3329						
00021010				3330						
	7FF00000 00000000				DC XL16'7FF0000000000007FF00000000000000'					
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/-inf/+0'					
	7FF00000 00000000				DC XL16'7FF0000000000007FF000000000000000'					
00021180	D4E2C4C2 D940D5C6			3334	DC CL48'MSDBR NF -inf/-inf/+2.0'					
	7FF00000 00000000				DC XL16'7FF0000000000007FF0000000000000000					
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/-inf/+2.0'					
	7FF00000 00000000				DC XL16'7FF0000000000007FF0000000000000000					
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-inf/+inf'					
	7FF80000 00000000 D4E2C4C2 40D5C640			3339	<pre>DC XL16'7FF80000000000007FF00000000000000000000</pre>					
	7FF80000 00000000				DC XL16'7FF8000000000007FF00000000000000					
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-inf/-QNaN'					
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'					
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/-inf/-QNaN'					
	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B000000000000'					
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-inf/+SNaN'					
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A0000000000000					
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/-inf/+SNaN'					
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A00000000000'					
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-2.0/-inf'					
	7FF00000 00000000				DC XL16'7FF0000000000007FF00000000000000000000					
	D4E2C4C2 40D5C640 7FF00000 00000000				DC CL48'MSDB NF -inf/-2.0/-inf' DC XL16'7FF0000000000007FF00000000000000					
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-2.0/-2.0'					
00021400	D-12C-1C2 DJ-10DJC0			JJJ <del>4</del>	DC CL+0 11300K W 1111/ 2.0/ 2.0					

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
	7FF00000 00000000				DC XL16'7FF0000000000007FF000000000000000'			
00021440	D4E2C4C2 40D5C640				DC CL48 MSDB NF -inf/-2.0/-2.0'			
	7FF00000 00000000				DC XL16'7FF0000000000007FF0000000000000000			
0021480	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-2.0/-0'			
	7FF00000 00000000				DC XL16'7FF0000000000007FF00000000000000000000			
002140	D4E2C4C2 40D5C640 7FF00000 00000000				DC CL48'MSDB NF -inf/-2.0/-0' DC XL16'7FF0000000000007FF0000000000000000			
0021460	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-2.0/+0'			
	7FF00000 00000000				DC XL16'7FF0000000000007FF0000000000000			
0021540	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/-2.0/+0'			
	7FF00000 00000000				DC XL16'7FF0000000000007FF000000000000000'			
00021580	D4E2C4C2 D940D5C6			3366	DC CL48'MSDBR NF -inf/-2.0/+2.0'			
	7FF00000 00000000				DC XL16'7FF00000000000007FF000000000000000'			
000215C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/-2.0/+2.0'			
	7FF00000 00000000				DC XL16'7FF00000000000007FF00000000000000000			
0021600	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-2.0/+inf'			
00021630	7FF80000 00000000 D4E2C4C2 40D5C640				DC XL16'7FF8000000000007FF000000000000000000000			
	7FF80000 00000000				DC XL16'7FF8000000000007FF00000000000000			
0021670	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-2.0/-QNaN'			
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
00216C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/-2.0/-QNaN'			
	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B000000000000'			
0021700	D4E2C4C2 D940D5C6			3378	DC CL48'MSDBR NF -inf/-2.0/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A000000000000'			
00021740	D4E2C4C2 40D5C640				DC CL48 MSDB NF -inf/-2.0/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A0000000000000'			
00021780	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-0/-inf'			
	7FF80000 00000000				DC XL16'7FF800000000000FFF0000000000000'			
000217C0	D4E2C4C2 40D5C640 7FF80000 00000000				DC CL48'MSDB NF -inf/-0/-inf' DC XL16'7FF800000000000FFF000000000000000000000			
00021710	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-0/-2.0'			
	7FF80000 00000000				DC XL16'7FF80000000000000000000000000000000			
0021840					DC CL48'MSDB NF -inf/-0/-2.0'			
	7FF80000 00000000				DC XL16'7FF80000000000000000000000000000000			
00021880	D4E2C4C2 D940D5C6			3390	DC CL48'MSDBR NF -inf/-0/-0'			
000218B0	7FF80000 00000000			3391	DC XL16'7FF8000000000000800000000000000000000			
00218C0					DC CL48'MSDB NF -inf/-0/-0'			
	7FF80000 00000000				DC XL16'7FF800000000000080000000000000000000			
0021900					DC CL48'MSDBR NF -inf/-0/+0'			
	7FF80000 00000000				DC XL16'7FF800000000000000000000000000000000000			
0021940	D4E2C4C2 40D5C640 7FF80000 00000000				DC CL48'MSDB NF -inf/-0/+0' DC XL16'7FF800000000000000000000000000000000000			
0021970					DC CL48'MSDBR NF -inf/-0/+2.0'			
	7FF80000 00000000				DC XL16'7FF8000000000004000000000000000000			
					DC CL48'MSDB NF -inf/-0/+2.0'			
	7FF80000 00000000				DC XL16'7FF80000000000004000000000000000000			
0021A00	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-0/+inf'			
	7FF80000 00000000			3403	DC XL16'7FF8000000000007FF000000000000000'			
0021A40					DC CL48'MSDB NF -inf/-0/+inf'			
	7FF80000 00000000				DC XL16'7FF8000000000007FF000000000000000'			
00021A80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-0/-QNaN'			
	7FF80000 00000000				DC XL16'7FF800000000000FFF8B0000000000'			
00021AC0	D4E2C4C2 40D5C640 7FF80000 00000000				DC CL48'MSDB NF -inf/-0/-QNaN'			
	/			5409	DC XL16'7FF8000000000000FFF8B000000000000'			

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
0021B30	7FF80000 00000000			3411	DC XL16'7FF8000000000007FF0A00000000000'			
0021B40	D4E2C4C2 40D5C640			3412	DC CL48'MSDB NF -inf/-0/+SNaN'			
0021B70	7FF80000 00000000			3413	DC XL16'7FF80000000000007FF0A000000000000'			
0021B80	D4E2C4C2 D940D5C6			3414	DC CL48'MSDBR NF -inf/+0/-inf'			
0021BB0	7FF80000 00000000			3415				
0021BC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/+0/-inf'			
	7FF80000 00000000			3417				
0021C00	D4E2C4C2 D940D5C6			3418				
	7FF80000 00000000			3419				
0021C40	D4E2C4C2 40D5C640			3420	·			
	7FF80000 00000000			3421				
0021C80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/+0/-0'			
	7FF80000 00000000			3423				
00021CC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/+0/-0'			
	7FF80000 00000000			3425				
0021D00	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/+0/+0'			
00021D30 00021D40	7FF80000 00000000 D4E2C4C2 40D5C640			3427 3428				
	7FF80000 00000000				DC XL16'7FF800000000000000000000000000000000000			
0021D70	D4E2C4C2 D940D5C6			3439				
	7FF80000 00000000			3431	· · · · · · · · · · · · · · · · · · ·			
0021DC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/+0/+2.0'			
0021DC0	7FF80000 00000000			3433				
0021E00	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/+0/+inf'			
	7FF80000 00000000				DC XL16'7FF800000000007FF00000000000000			
0021E40	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/+0/+inf'			
	7FF80000 00000000			3437				
0021E80	D4E2C4C2 D940D5C6			3438				
	7FF80000 00000000			3439				
0021EC0	D4E2C4C2 40D5C640			3440				
0021EF0	7FF80000 00000000			3441	DC XL16'7FF800000000000FFF8B000000000000'			
0021F00	D4E2C4C2 D940D5C6			3442	DC CL48'MSDBR NF -inf/+0/+SNaN'			
0021F30	7FF80000 00000000				DC XL16'7FF80000000000007FF0A000000000000'			
0021F40				3444	DC CL48'MSDB NF -inf/+0/+SNaN'			
	7FF80000 00000000				DC XL16'7FF8000000000007FF0A00000000000'			
0021F80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/+2.0/-inf'			
	7FF80000 00000000				DC XL16'7FF8000000000000FFF000000000000000'			
0021FC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/+2.0/-inf'			
	7FF80000 00000000				DC XL16'7FF800000000000FFF000000000000000'			
0022000	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/+2.0/-2.0'			
	FFF00000 00000000				DC XL16'FFF0000000000000FFF00000000000000'			
0022040	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/+2.0/-2.0'			
0022070	FFF00000 00000000				DC XL16'FFF000000000000FFF0000000000000000000			
0022080	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/+2.0/-0'			
00220B0	FFF00000 00000000				DC XL16'FFF000000000000FFF0000000000000000000			
00220C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/+2.0/-0'			
00220F0	FFF00000 00000000				DC XL16'FFF000000000000FFF0000000000000'			
0022100 0022130	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/+2.0/+0' DC XL16'FFF000000000000FFF00000000000000000			
0022130	FFF00000 00000000 D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/+2.0/+0'			
0022140	FFF00000 00000000				DC XL16'FFF000000000000FFF0000000000000000			
0022170	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/+2.0/+2.0'			
	FFF00000 00000000				DC XL16'FFF000000000000FFF0000000000000000			
00221B0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/+2.0/+2.0'			
	FFF00000 00000000				DC XL16'FFF000000000000FFF0000000000000000			
				, <del></del> ( ) )	- P.C. ALTO III OOOOOOOOOOOOOOIII UUUUUUUUUUUUUU			

	0.2.1 bfp-022-mult	sub: Test			d Subtract	17 Aug 2022 12:26:56	Page	72
LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
00022930	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B000000000000'			
00022940	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/-QNaN/+0'			
0022970	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B0000000000000'			
0022980	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-QNaN/+2.0'			
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
00229C0	D4E2C4C2 40D5C640 FFF8B000 00000000				DC CL48'MSDB NF -inf/-QNaN/+2.0' DC XL16'FFF8B0000000000FFF8B00000000000'			
100229F0 10022A00	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-QNaN/+inf'			
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
0022A40	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/-QNaN/+inf'			
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
00022A80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-QNaN/-QNaN'			
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B0000000000000'			
0022AC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/-QNaN/-QNaN'			
	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B000000000000'			
00022B00	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/-QNaN/+SNaN'			
					DC XL16'7FF8A00000000007FF0A00000000000'			
0022B40	D4E2C4C2 40D5C640 7FF8A000 00000000				DC CL48'MSDB NF -inf/-QNaN/+SNaN' DC XL16'7FF8A00000000007FF0A00000000000'			
0022B70	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/+SNaN/-inf'			
	7FF8A000 00000000				DC XL16'7FF8A000000000FFF00000000000000			
0022BC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/+SNaN/-inf'			
	7FF8A000 00000000				DC XL16'7FF8A00000000000FFF000000000000000'			
0022C00	D4E2C4C2 D940D5C6			3546	DC CL48'MSDBR NF -inf/+SNaN/-2.0'			
	7FF8A000 00000000				DC XL16'7FF8A00000000000C00000000000000000000			
0022C40	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/+SNaN/-2.0'			
	7FF8A000 00000000			3549				
00022C80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/+SNaN/-0'			
0022CB0	7FF8A000 00000000 D4E2C4C2 40D5C640				DC XL16'7FF8A00000000000800000000000000000000000			
	7FF8A000 00000000				DC XL16'7FF8A00000000008000000000000000000000000			
0022D00	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/+SNaN/+0'			
	7FF8A000 00000000				DC XL16'7FF8A0000000000000000000000000000000			
0022D40	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/+SNaN/+0'			
	7FF8A000 00000000				DC XL16'7FF8A000000000000000000000000000000000			
0022D80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/+SNaN/+2.0'			
	7FF8A000 00000000				DC XL16'7FF8A0000000000040000000000000000000			
0022DC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -inf/+SNaN/+2.0'			
	7FF8A000 00000000				DC XL16'7FF8A000000000004000000000000000000000000			
0022E00	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/+SNaN/+inf'			
0022E30	7FF8A000 00000000 D4E2C4C2 40D5C640				DC XL16'7FF8A000000000007FF000000000000000000000			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0000000000000000			
0022E70					DC CL48'MSDBR NF -inf/+SNaN/-QNaN'			
	7FF8A000 00000000				DC XL16'7FF8A0000000000FFF8B00000000000'			
0022EC0					DC CL48'MSDB NF -inf/+SNaN/-QNaN'			
0022EF0	7FF8A000 00000000			3569	DC XL16'7FF8A0000000000FFF8B000000000000'			
0022F00	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -inf/+SNaN/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A000000000007FF0A000000000000'			
00022F40					DC CL48'MSDB NF -inf/+SNaN/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A000000000000000			
0022F80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-inf/-inf'			
00022FB0 00022FC0	7FF00000 00000000 D4E2C4C2 40D5C640				DC XL16'7FF00000000000007FF0000000000000000000			
	7FF00000 00000000				DC XL16'7FF0000000000007FF00000000000000000			
0022770	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-inf/-2.0'			

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
0023030	7FF00000 00000000			3579	DC XL16'7FF00000000000007FF000000000000000'			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-inf/-2.0'			
	7FF00000 00000000				DC XL16'7FF0000000000007FF0000000000000000			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-inf/-0'			
	7FF00000 00000000				DC XL16'7FF00000000000007FF00000000000000000			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-inf/-0'			
	7FF00000 00000000 D4E2C4C2 D940D5C6				DC XL16'7FF00000000000007FF0000000000000000000			
	7FF00000 00000000				DC XL16'7FF0000000000007FF0000000000000			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-inf/+0'			
	7FF00000 00000000				DC XL16'7FF0000000000007FF00000000000000			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-inf/+2.0'			
00231B0	7FF00000 00000000			3591	DC XL16'7FF00000000000007FF000000000000000'			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-inf/+2.0'			
	7FF00000 00000000				DC XL16'7FF0000000000007FF0000000000000000			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-inf/+inf'			
	7FF80000 00000000				DC XL16'7FF8000000000007FF00000000000000000			
	D4E2C4C2 40D5C640 7FF80000 00000000				DC CL48'MSDB NF -2.0/-inf/+inf'			
	D4E2C4C2 D940D5C6				DC XL16'7FF80000000000007FF00000000000000000000			
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-inf/-QNaN'			
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-inf/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A000000000007FF0A000000000000'			
	D4E2C4C2 40D5C640			3604	DC CL48'MSDB NF -2.0/-inf/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A00000000000'			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-2.0/-inf'			
	7FF00000 00000000			3607				
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-2.0/-inf'			
	7FF00000 00000000 D4E2C4C2 D940D5C6				DC XL16'7FF00000000000007FF0000000000000000000			
	40180000 00000000				DC XL16'4018000000000004018000000000000'			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-2.0'			
	40180000 00000000				DC XL16'4018000000000004018000000000000'			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-2.0/-0'			
					DC XL16'401000000000000040100000000000000'			
00234C0	D4E2C4C2 40D5C640			3616	DC CL48'MSDB NF -2.0/-2.0/-0'			
	40100000 00000000				DC XL16'401000000000000040100000000000000'			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-2.0/+0'			
					DC XL16'401000000000000040100000000000000'			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-2.0/+0'			
					DC XL16'4010000000000004010000000000000'			
	D4E2C4C2 D940D5C6 4000000 00000000				DC CL48'MSDBR NF -2.0/-2.0/+2.0' DC XL16'4000000000000000400000000000000000000			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-2.0/+2.0'			
					DC XL16'4000000000000004000000000000000000000			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-2.0/+inf'			
	FFF00000 00000000				DC XL16'FFF000000000000FFF000000000000000			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-2.0/+inf'			
0023670	FFF00000 00000000			3629	DC XL16'FFF0000000000000FFF000000000000000'			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-2.0/-QNaN'			
	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B000000000000'			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-2.0/-QNaN'			
	FFF8B000 00000000			フレンブ	DC XL16'FFF8B00000000000FFF8B00000000000'			

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
00023730	7FF8A000 00000000			3635	DC XL16'7FF8A00000000007FF0A000000000000'			
0023740	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-2.0/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A000000000000'			
0023780	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-0/-inf'			
	7FF00000 00000000				DC XL16'7FF00000000000007FF00000000000000000			
00237C0	D4E2C4C2 40D5C640 7FF00000 00000000				DC CL48'MSDB NF -2.0/-0/-inf' DC XL16'7FF0000000000007FF000000000000000000			
100237F0	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-0/-2.0'			
0023830	40000000 00000000				DC XL16'400000000000000400000000000000000000			
0023840	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-0/-2.0'			
0023870	4000000 00000000				DC XL16'400000000000000040000000000000000000			
00023880	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-0/-0'			
000238B0	00000000 00000000				DC XL16'000000000000000000000000000000000000			
000238C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-0/-0'			
000238F0	00000000 00000000				DC XL16'000000000000000000000000000000000000			
0023900	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-0/+0'			
0023930	00000000 00000000				DC XL16'000000000000000000000000000000000000			
00023940 00023970	D4E2C4C2 40D5C640 00000000 00000000				DC CL48'MSDB NF -2.0/-0/+0'			
00023970	D4E2C4C2 D940D5C6				DC XL16'000000000000000000000000000000000000			
00239B0	C0000000 00000000				DC XL16'C000000000000000000000000000000000000			
0023900	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-0/+2.0'			
00239F0	C0000000 00000000				DC XL16'C000000000000000C0000000000000000'			
0023A00	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-0/+inf'			
0023A30	FFF00000 00000000				DC XL16'FFF000000000000FFF0000000000000000'			
0023A40	D4E2C4C2 40D5C640			3660	DC CL48'MSDB NF -2.0/-0/+inf'			
00023A70	FFF00000 00000000				DC XL16'FFF0000000000000FFF000000000000000'			
00023A80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-0/-QNaN'			
00023AB0	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B000000000000'			
0023AC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-0/-QNaN'			
00023AF0	FFF8B000 00000000 D4E2C4C2 D940D5C6				DC XL16'FFF8B00000000000FFF8B00000000000' DC CL48'MSDBR NF -2.0/-0/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A000000000000			
0023B40					DC CL48'MSDB NF -2.0/-0/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A00000000000'			
0023B80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+0/-inf'			
	7FF00000 00000000				DC XL16'7FF0000000000007FF000000000000000'			
00023BC0	D4E2C4C2 40D5C640			3672	DC CL48'MSDB NF -2.0/+0/-inf'			
	7FF00000 00000000				DC XL16'7FF00000000000007FF000000000000000'			
0023C00	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+0/-2.0'			
0023C30					DC XL16'400000000000000040000000000000000000			
0023C40	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+0/-2.0'			
0023C70					DC XL16'4000000000000004000000000000000000000			
0023C80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+0/-0'			
0023CB0 0023CC0	00000000 00000000 D4E2C4C2 40D5C640				DC XL16'000000000000000000000000000000000000			
0023CF0	00000000 00000000				DC XL16'000000000000000000000000000000000000			
0023C10	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+0/+0'			
0023D30					DC XL16'800000000000000800000000000000000000			
0023D40	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+0/+0'			
0023D70	8000000 00000000				DC XL16'8000000000000000800000000000000000000			
00023D80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+0/+2.0'			
00023DB0	C0000000 00000000				DC XL16'C000000000000000000000000000000000000			
0023DC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+0/+2.0'			
0023DF0					DC XL16'C000000000000000000000000000000000000			
0023E00	D4E2C4C2 D940D5C6			3690	DC CL48'MSDBR NF -2.0/+0/+inf'			

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
0023E30	FFF00000 00000000			3691	DC XL16'FFF0000000000000FFF000000000000000'			
0023E40	D4E2C4C2 40D5C640			3692	DC CL48'MSDB NF -2.0/+0/+inf'			
0023E70	FFF00000 00000000			3693				
0023E80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+0/-QNaN'			
0023EB0	FFF8B000 00000000			3695				
0023EC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+0/-QNaN'			
0023EF0	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B000000000000'			
0023F00	D4E2C4C2 D940D5C6			3698				
0023F30	7FF8A000 00000000			3699				
0023F40	D4E2C4C2 40D5C640			3700	• • •			
0023F70	7FF8A000 00000000				DC XL16'7FF8A000000000007FF0A000000000000'			
0023F80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+2.0/-inf'			
0023FB0	7FF00000 00000000				DC XL16'7FF00000000000007FF00000000000000000			
0023FC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+2.0/-inf'			
0023FF0	7FF00000 00000000			3705				
0024000	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+2.0/-2.0'			
0024030	C0000000 00000000			3707				
0024040	D4E2C4C2 40D5C640			3708	· · · · · · · · · · · · · · · · · · ·			
0024070 0024080	C0000000 00000000 D4E2C4C2 D940D5C6				DC XL16'C000000000000000000000000000000000000			
0024080 00240B0	C0100000 00000000				DC XL16'C010000000000000C010000000000000'			
00240C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+2.0/-0'			
00240C0 00240F0	C0100000 00000000			3712	· · · · · · · · · · · · · · · · · · ·			
0024060	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+2.0/+0'			
0024100	C0100000 00000000				DC XL16'C010000000000000C010000000000000'			
0024130	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+2.0/+0'			
0024170	C0100000 00000000			3717				
0024170	D4E2C4C2 D940D5C6			3718				
00241B0	C0180000 00000000			3719	· · · · · · · · · · · · · · · · · · ·			
00241C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+2.0'			
00241E0	C0180000 00000000				DC XL16'C01800000000000C018000000000000'			
0024200	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+2.0/+inf'			
	FFF00000 00000000				DC XL16'FFF000000000000FFF000000000000000			
0024240					DC CL48'MSDB NF -2.0/+2.0/+inf'			
0024270	FFF00000 00000000				DC XL16'FFF000000000000FFF000000000000000'			
0024280	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+2.0/-QNaN'			
00242B0	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
00242C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+2.0/-QNaN'			
00242F0	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B000000000000'			
0024300	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+2.0/+SNaN'			
0024330	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A000000000000'			
0024340	D4E2C4C2 40D5C640			3732	DC CL48'MSDB NF -2.0/+2.0/+SNaN'			
				3733	DC XL16'7FF8A00000000007FF0A000000000000'			
0024380	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+inf/-inf'			
00243B0					DC XL16'7FF8000000000000FFF000000000000000'			
00243C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+inf/-inf'			
00243F0	7FF80000 00000000				DC XL16'7FF8000000000000FFF000000000000000'			
0024400	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+inf/-2.0'			
0024430	FFF00000 00000000				DC XL16'FFF0000000000000FFF000000000000000'			
0024440	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+inf/-2.0'			
0024470	FFF00000 00000000				DC XL16'FFF0000000000000FFF0000000000000000'			
0024480	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+inf/-0'			
00244B0	FFF00000 00000000				DC XL16'FFF0000000000000FFF000000000000000'			
000244C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+inf/-0'			
00244F0	FFF00000 00000000				DC XL16'FFF0000000000000FFF000000000000000'			
0024500	D4E2C4C2 D940D5C6			3746	DC CL48'MSDBR NF -2.0/+inf/+0'			

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
0024530	FFF00000 00000000			3747	DC XL16'FFF0000000000000FFF000000000000000'			
0024540	D4E2C4C2 40D5C640			3748	DC CL48'MSDB NF -2.0/+inf/+0'			
0024570	FFF00000 00000000			3749	DC XL16'FFF0000000000000FFF000000000000000'			
0024580	D4E2C4C2 D940D5C6			3750	DC CL48'MSDBR NF -2.0/+inf/+2.0'			
00245B0	FFF00000 00000000			3751				
00245C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+inf/+2.0'			
00245F0	FFF00000 00000000				DC XL16'FFF0000000000000FFF000000000000000'			
0024600	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+inf/+inf'			
0024630	FFF00000 00000000			3755				
0024640	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+inf/+inf'			
0024670	FFF00000 00000000			3757				
0024680	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+inf/-QNaN'			
00246B0	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B000000000000'			
00246C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+inf/-QNaN'			
00246F0	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
0024700 0024730	D4E2C4C2 D940D5C6 7FF8A000 00000000			3762 3763	DC CL48'MSDBR NF -2.0/+inf/+SNaN' DC XL16'7FF8A00000000007FF0A00000000000'			
0024730	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+inf/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A00000000000'			
0024770	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-QNaN/-inf'			
0024780 00247B0	FFF8B000 00000000			3767	• • • • • • • • • • • • • • • • • • • •			
00247C0	D4E2C4C2 40D5C640			3768				
00247E0	FFF8B000 00000000			3769				
0024800	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-QNaN/-2.0'			
0024830	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
0024840	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-QNaN/-2.0'			
0024870	FFF8B000 00000000			3773	· · · · · · · · · · · · · · · · · · ·			
0024880	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-QNaN/-0'			
00248B0	FFF8B000 00000000			3775				
00248C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-QNaN/-0'			
00248F0	FFF8B000 00000000			3777	DC XL16'FFF8B00000000000FFF8B000000000000'			
0024900	D4E2C4C2 D940D5C6			3778	DC CL48'MSDBR NF -2.0/-QNaN/+0'			
0024930	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B0000000000000'			
0024940	D4E2C4C2 40D5C640			3780	DC CL48'MSDB NF -2.0/-QNaN/+0'			
0024970	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B000000000000'			
0024980	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-QNaN/+2.0'			
00249B0	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B000000000000'			
00249C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-QNaN/+2.0'			
00249F0	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B00000000000'			
0024A00	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-QNaN/+inf'			
0024A30	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
0024A40	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-QNaN/+inf'			
0024A70	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
0024A80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-QNaN/-QNaN'			
0024AB0	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
0024AC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/-QNaN/-QNaN'			
0024AF0	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
0024B00	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/-QNaN/+SNaN'			
0024B30 0024B40	7FF8A000 00000000				DC XL16'7FF8A000000000007FF0A00000000000' DC CL48'MSDB NF -2.0/-QNaN/+SNaN'			
0024B40 0024B70	D4E2C4C2 40D5C640 7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A00000000000'			
0024B70 0024B80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+SNaN/-inf'			
	7FF8A000 00000000				DC XL16'7FF8A0000000000FFF000000000000000			
0024BC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+SNaN/-inf'			
	7FF8A000 00000000				DC XL16'7FF8A0000000000FFF00000000000000			
	, 1 1 0 A 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0			700T	DE ALIO / I GAGGGGGGGGGGGGGGGGGGGGGGGGGGGGGGGGG			

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
0024C30	7FF8A000 00000000			3803	DC XL16'7FF8A0000000000000000000000000000000			
0024C40	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+SNaN/-2.0'			
	7FF8A000 00000000				DC XL16'7FF8A00000000000000000000000000000000			
0024C80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+SNaN/-0'			
	7FF8A000 00000000				DC XL16'7FF8A00000000000800000000000000000000000			
0024CC0	D4E2C4C2 40D5C640			3808	· · · · · · · · · · · · · · · · · · ·			
0024CF0 0024D00	7FF8A000 00000000 D4E2C4C2 D940D5C6				DC XL16'7FF8A00000000000800000000000000000000000			
	7FF8A000 00000000				DC XL16'7FF8A0000000000000000000000000000000			
0024D40	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+SNaN/+0'			
	7FF8A000 00000000				DC XL16'7FF8A00000000000000000000000000000			
0024D80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+SNaN/+2.0'			
	7FF8A000 00000000				DC XL16'7FF8A0000000000040000000000000000000			
0024DC0	D4E2C4C2 40D5C640			3816	DC CL48'MSDB NF -2.0/+SNaN/+2.0'			
	7FF8A000 00000000				DC XL16'7FF8A00000000000400000000000000000000			
0024E00	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+SNaN/+inf'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0000000000000000			
0024E40	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+SNaN/+inf'			
0024E70	7FF8A000 00000000 D4E2C4C2 D940D5C6				DC XL16'7FF8A000000000007FF000000000000000000000			
	7FF8A000 00000000				DC XL16'7FF8A0000000000FFF8B0000000000'			
0024EC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -2.0/+SNaN/-QNaN'			
	7FF8A000 00000000				DC XL16'7FF8A0000000000FFF8B0000000000'			
0024F00	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -2.0/+SNaN/+SNaN'			
	7FF8A000 00000000			3827				
0024F40	D4E2C4C2 40D5C640			3828	DC CL48'MSDB NF -2.0/+SNaN/+SNaN'			
	7FF8A000 00000000			3829				
0024F80	D4E2C4C2 D940D5C6			3830				
	7FF80000 00000000				DC XL16'7FF8000000000000FFF000000000000000'			
0024FC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/-inf/-inf'			
	7FF80000 00000000				DC XL16'7FF800000000000FFF00000000000000'			
0025000	D4E2C4C2 D940D5C6 7FF80000 00000000				DC CL48'MSDBR NF -0/-inf/-2.0' DC XL16'7FF800000000000000000000000000000000000			
0025040	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/-inf/-2.0'			
	7FF80000 00000000				DC XL16'7FF80000000000000000000000000000000			
0025080	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/-inf/-0'			
	7FF80000 00000000				DC XL16'7FF80000000000008000000000000000000			
00250C0					DC CL48'MSDB NF -0/-inf/-0'			
00250F0	7FF80000 00000000			3841	DC XL16'7FF8000000000000800000000000000000000			
0025100					DC CL48'MSDBR NF -0/-inf/+0'			
	7FF80000 00000000				DC XL16'7FF8000000000000000000000000000000000			
0025140					DC CL48'MSDB NF -0/-inf/+0'			
	7FF80000 00000000				DC XL16'7FF800000000000000000000000000000000000			
0025180	D4E2C4C2 D940D5C6 7FF80000 00000000				DC CL48'MSDBR NF -0/-inf/+2.0' DC XL16'7FF8000000000000400000000000000000000000			
					DC CL48'MSDB NF -0/-inf/+2.0'			
	7FF80000 00000000				DC XL16'7FF80000000000040000000000000000000			
0025110					DC CL48'MSDBR NF -0/-inf/+inf'			
	7FF80000 00000000				DC XL16'7FF8000000000007FF00000000000000			
0025240					DC CL48'MSDB NF -0/-inf/+inf'			
	7FF80000 00000000				DC XL16'7FF8000000000007FF000000000000000'			
0025280					DC CL48'MSDBR NF -0/-inf/-QNaN'			
	7FF80000 00000000				DC XL16'7FF800000000000FFF8B000000000000'			
000252C0					DC CL48'MSDB NF -0/-inf/-QNaN'			
	7FF80000 00000000				DC XL16'7FF8000000000000FFF8B00000000000'			
0025300	D4E2C4C2 D940D5C6			3858	DC CL48'MSDBR NF -0/-inf/+SNaN'			

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
0025330	7FF80000 00000000			3859	DC XL16'7FF8000000000007FF0A00000000000'			
0025340	D4E2C4C2 40D5C640			3860	DC CL48'MSDB NF -0/-inf/+SNaN'			
0025370	7FF80000 00000000			3861	DC XL16'7FF8000000000007FF0A00000000000'			
0025380	D4E2C4C2 D940D5C6			3862	DC CL48'MSDBR NF -0/-2.0/-inf'			
00253B0	7FF00000 00000000			3863	DC XL16'7FF00000000000007FF000000000000000'			
00253C0	D4E2C4C2 40D5C640			3864	DC CL48'MSDB NF -0/-2.0/-inf'			
00253F0	7FF00000 00000000				DC XL16'7FF00000000000007FF000000000000000'			
0025400	D4E2C4C2 D940D5C6			3866	DC CL48'MSDBR NF -0/-2.0/-2.0'			
0025430	40000000 00000000			3867				
0025440	D4E2C4C2 40D5C640			3868	·			
0025470	40000000 00000000			3869				
0025480	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/-2.0/-0'			
00254B0	00000000 00000000				DC XL16'000000000000000000000000000000000000			
000254C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/-2.0/-0'			
00254F0	00000000 00000000			3873				
0025500	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/-2.0/+0'			
0025530	00000000 00000000				DC XL16'000000000000000000000000000000000000			
0025540	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/-2.0/+0'			
0025570	00000000 00000000				DC XL16'000000000000000000000000000000000000			
0025580	D4E2C4C2 D940D5C6			3878				
00255B0	C0000000 00000000			3879				
00255C0	D4E2C4C2 40D5C640			3880	·			
00255F0	C0000000 00000000			3881				
0025600	D4E2C4C2 D940D5C6			3882	·			
0025630	FFF00000 00000000			3883				
0025640	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/-2.0/+inf'			
0025670	FFF00000 00000000			3885				
00025680 000256B0	D4E2C4C2 D940D5C6 FFF8B000 00000000			3887	DC CL48'MSDBR NF -0/-2.0/-QNaN' DC XL16'FFF8B0000000000FFF8B00000000000'			
00256C0	D4E2C4C2 40D5C640			3888				
00256F0	FFF8B000 00000000			3889				
0025700	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/-2.0/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A00000000000'			
0025730					DC CL48'MSDB NF -0/-2.0/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A00000000000'			
0025780					DC CL48'MSDBR NF -0/-0/-inf'			
	7FF00000 00000000				DC XL16'7FF0000000000007FF00000000000000			
00257D0					DC CL48'MSDB NF -0/-0/-inf'			
	7FF00000 00000000				DC XL16'7FF0000000000007FF00000000000000			
0025800	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/-0/-2.0'			
0025830					DC XL16'4000000000000000400000000000000000			
0025840	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/-0/-2.0'			
0025870					DC XL16'400000000000000040000000000000000'			
0025880	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/-0/-0'			
00258B0					DC XL16'000000000000000000000000000000000000			
00258C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/-0/-0'			
00258F0	00000000 00000000				DC XL16'000000000000000000000000000000000000			
0025900	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/-0/+0'			
0025930	00000000 00000000				DC XL16'000000000000000000000000000000000000			
0025940	D4E2C4C2 40D5C640			3908	DC CL48'MSDB NF -0/-0/+0'			
0025970	00000000 00000000				DC XL16'000000000000000000000000000000000000			
0025980	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/-0/+2.0'			
00259B0					DC XL16'C000000000000000000000000000000000000			
00259C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/-0/+2.0'			
00259F0					DC XL16'C000000000000000000000000000000000000			
0025A00	D4E2C4C2 D940D5C6			201/	DC CL48'MSDBR NF -0/-0/+inf'			

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
0025A30	FFF00000 00000000			3915	DC XL16'FFF0000000000000FFF000000000000000'			
0025A40	D4E2C4C2 40D5C640			3916	DC CL48'MSDB NF -0/-0/+inf'			
0025A70	FFF00000 00000000			3917	DC XL16'FFF0000000000000FFF000000000000000'			
0025A80	D4E2C4C2 D940D5C6			3918	DC CL48'MSDBR NF -0/-0/-QNaN'			
0025AB0	FFF8B000 00000000			3919	DC XL16'FFF8B0000000000FFF8B000000000000'			
0025AC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/-0/-QNaN'			
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B000000000000'			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/-0/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A000000000000'			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/-0/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A0000000000000'			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/+0/-inf'			
	7FF00000 00000000				DC XL16'7FF00000000000007FF000000000000000'			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/+0/-inf'			
	7FF00000 00000000				DC XL16'7FF0000000000007FF000000000000000'			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/+0/-2.0'			
	4000000 00000000				DC XL16'400000000000000040000000000000000000			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/+0/-2.0'			
	40000000 00000000				DC XL16'400000000000000040000000000000000000			
0025C80	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/+0/-0'			
	00000000 00000000				DC XL16'000000000000000000000000000000000000			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/+0/-0'			
	00000000 00000000				DC XL16'000000000000000000000000000000000000			
0025D00	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/+0/+0'			
	80000000 00000000				DC XL16'8000000000000008000000000000000000000			
0025D40	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/+0/+0'			
	80000000 00000000 D4E2C4C2 D940D5C6				DC XL16'8000000000000008000000000000000000000			
0025D80 0025DB0	C0000000 00000000				DC XL16'C000000000000000000000000000000000000			
0025DC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/+0/+2.0'			
					DC XL16'C000000000000000000000000000000000000			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/+0/+inf'			
	FFF00000 00000000				DC XL16'FFF000000000000FFF000000000000000			
					DC CL48'MSDB NF -0/+0/+inf'			
	FFF00000 00000000				DC XL16'FFF000000000000FFF000000000000000			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/+0/-QNaN'			
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
					DC CL48'MSDB NF -0/+0/-QNaN'			
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
					DC CL48'MSDBR NF -0/+0/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A0000000000'			
					DC CL48'MSDB NF -0/+0/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A0000000000'			
					DC CL48'MSDBR NF -0/+2.0/-inf'			
	7FF00000 00000000				DC XL16'7FF0000000000007FF00000000000000			
					DC CL48'MSDB NF -0/+2.0/-inf'			
	7FF00000 00000000				DC XL16'7FF0000000000007FF00000000000000'			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/+2.0/-2.0'			
					DC XL16'4000000000000000400000000000000000'			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/+2.0/-2.0'			
					DC XL16'400000000000000400000000000000000'			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF -0/+2.0/-0'			
	0000000 00000000				DC XL16'000000000000000000000000000000000000			
00260C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF -0/+2.0/-0'			
					DC XL16'000000000000000000000000000000000000			
					DC CL48'MSDBR NF -0/+2.0/+0'			

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
00028430	7FF80000 00000000				DC XL16'7FF800000000000000000000000000000000			
00028440	D4E2C4C2 40D5C640				DC CL48'MSDB NF +0/+inf/-2.0'			
	7FF80000 00000000			4253				
0028480	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF +0/+inf/-0'			
	7FF80000 00000000				DC XL16'7FF800000000000080000000000000000000000			
000284C0	D4E2C4C2 40D5C640 7FF80000 00000000				DC CL48'MSDB NF +0/+inf/-0' DC XL16'7FF800000000000800000000000000000000			
0028500	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF +0/+inf/+0'			
	7FF80000 00000000			4259				
0028540	D4E2C4C2 40D5C640			4260				
	7FF80000 00000000				DC XL16'7FF800000000000000000000000000000000			
00028580	D4E2C4C2 D940D5C6			4262	DC CL48'MSDBR NF +0/+inf/+2.0'			
	7FF80000 00000000				DC XL16'7FF8000000000000400000000000000000000			
000285C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF +0/+inf/+2.0'			
	7FF80000 00000000				DC XL16'7FF8000000000000400000000000000000000			
00028600					DC CL48'MSDBR NF +0/+inf/+inf'			
	7FF80000 00000000			4267				
00028640	D4E2C4C2 40D5C640 7FF80000 00000000				DC CL48'MSDB NF +0/+inf/+inf' DC XL16'7FF8000000000007FF000000000000000			
00028680	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF +0/+inf/-QNaN'			
	7FF80000 00000000				DC XL16'7FF800000000000FFF8B00000000000'			
00286C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF +0/+inf/-QNaN'			
	7FF80000 00000000			4273				
0028700	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF +0/+inf/+SNaN'			
0028730	7FF80000 00000000			4275	DC XL16'7FF8000000000007FF0A00000000000'			
00028740	D4E2C4C2 40D5C640				DC CL48'MSDB NF +0/+inf/+SNaN'			
	7FF80000 00000000			4277				
00028780	D4E2C4C2 D940D5C6			4278				
	FFF8B000 00000000			4279				
000287C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF +0/-QNaN/-inf'			
000287F0 00028800	FFF8B000 00000000 D4E2C4C2 D940D5C6				DC XL16'FFF8B00000000000FFF8B00000000000' DC CL48'MSDBR NF +0/-ONaN/-2.0'			
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B000000000000			
0028840					DC CL48'MSDB NF +0/-QNaN/-2.0'			
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
0028880	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF +0/-QNaN/-0'			
	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B000000000000'			
00288C0	D4E2C4C2 40D5C640			4288	DC CL48'MSDB NF +0/-QNaN/-0'			
	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B0000000000000'			
0028900					DC CL48'MSDBR NF +0/-QNaN/+0'			
	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B000000000000'			
0028940					DC CL48'MSDB NF +0/-QNaN/+0'			
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
0028980	D4E2C4C2 D940D5C6 FFF8B000 00000000				DC CL48'MSDBR NF +0/-QNaN/+2.0' DC XL16'FFF8B0000000000FFF8B00000000000'			
100289B0 100289C0					DC CL48'MSDB NF +0/-QNaN/+2.0'			
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
0028A00	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF +0/-QNaN/+inf'			
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000'			
0028A40					DC CL48'MSDB NF +0/-QNaN/+inf'			
	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B000000000000'			
00028A80					DC CL48'MSDBR NF +0/-QNaN/-QNaN'			
	FFF8B000 00000000				DC XL16'FFF8B0000000000FFF8B00000000000000'			
00028AC0	D4E2C4C2 40D5C640				DC CL48'MSDB NF +0/-QNaN/-QNaN'			
	FFF8B000 00000000				DC XL16'FFF8B00000000000FFF8B000000000000'			
0028B00	D4E2C4C2 D940D5C6			4306	DC CL48'MSDBR NF +0/-QNaN/+SNaN'			

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LOC	OBJECT CODE	ADDR1 ADDR2	STMT				
0002ED30	7FF8A000 00000000		5091				
0002ED40	D4E2C4C2 40D5C640		5092	• • • • • • • • • • • • • • • • • • • •			
002ED70	7FF8A000 00000000		5093				
002ED80	D4E2C4C2 D940D5C6 7FF8A000 00000000			DC CL48'MSDBR NF -QNaN/+SNaN/+2.0' DC XL16'7FF8A000000000004000000000000000000000000			
0002EDB0 0002EDC0	D4E2C4C2 40D5C640			DC CL48'MSDB NF -QNaN/+SNaN/+2.0'			
002EDF0	7FF8A000 00000000			DC XL16'7FF8A000000000040000000000000000000000000			
002EE00	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF -QNaN/+SNaN/+inf'			
0002EE30	7FF8A000 00000000		5099				
0002EE40	D4E2C4C2 40D5C640			<pre>DC CL48'MSDB NF -QNaN/+SNaN/+inf'</pre>			
0002EE70	7FF8A000 00000000			DC XL16'7FF8A00000000007FF000000000000000'			
0002EE80	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF -QNaN/+SNaN/-QNaN'			
0002EEB0	7FF8A000 00000000			DC XL16'7FF8A00000000000FFF8B00000000000'			
0002EEC0	D4E2C4C2 40D5C640			DC CL48'MSDB NF -QNaN/+SNaN/-QNaN'			
0002EEF0 0002EF00	7FF8A000 00000000 D4E2C4C2 D940D5C6			<pre>DC XL16'7FF8A00000000000FFF8B00000000000' DC CL48'MSDBR NF -QNaN/+SNaN/+SNaN'</pre>			
0002EF00	7FF8A000 00000000			DC XL16'7FF8A00000000007FF0A000000000000000			
0002EF40	D4E2C4C2 40D5C640			DC CL48'MSDB NF -QNaN/+SNaN/+SNaN'			
0002EF70	7FF8A000 00000000			DC XL16'7FF8A00000000007FF0A00000000000'			
0002EF80	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/-inf/-inf'			
0002EFB0	7FF8A000 00000000			DC XL16'7FF8A0000000000FFF000000000000000'			
002EFC0	D4E2C4C2 40D5C640			<pre>DC CL48'MSDB NF +SNaN/-inf/-inf'</pre>			
002EFF0	7FF8A000 00000000			DC XL16'7FF8A00000000000FFF000000000000000'			
002F000	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/-inf/-2.0'			
002F030	7FF8A000 00000000			DC XL16'7FF8A00000000000000000000000000000000000			
0002F040 0002F070	D4E2C4C2 40D5C640 7FF8A000 00000000		5116	DC CL48'MSDB NF +SNaN/-inf/-2.0' DC XL16'7FF8A00000000000000000000000000000000000			
0002F070	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/-inf/-0'			
0002F0B0	7FF8A000 00000000			DC XL16'7FF8A00000000008000000000000000000000000			
0002F0C0	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/-inf/-0'			
0002F0F0	7FF8A000 00000000			DC XL16'7FF8A0000000000000000000000000000000			
0002F100	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/-inf/+0'			
	7FF8A000 00000000			DC XL16'7FF8A000000000000000000000000000000000			
0002F140	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/-inf/+0'			
002F170	7FF8A000 00000000			DC XL16'7FF8A00000000000000000000000000000000000			
0002F180	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/-inf/+2.0'			
0002F1B0 0002F1C0	7FF8A000 00000000 D4E2C4C2 40D5C640			DC XL16'7FF8A000000000004000000000000000000000000			
0002F1E0	7FF8A000 00000000		5129				
0002F100	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/-inf/+inf'			
002F230	7FF8A000 00000000			DC XL16'7FF8A00000000007FF000000000000000			
002F240	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/-inf/+inf'			
002F270	7FF8A000 00000000		5133				
002F280	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/-inf/-QNaN'			
002F2B0	7FF8A000 00000000		5135				
002F2C0	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/-inf/-QNaN'			
002F2F0	7FF8A000 00000000		5137				
0002F300 0002F330	D4E2C4C2 D940D5C6 7FF8A000 00000000			<pre>DC CL48'MSDBR NF +SNaN/-inf/+SNaN' DC XL16'7FF8A00000000007FF0A000000000000'</pre>			
002F340	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/-inf/+SNaN'			
002F340	7FF8A000 00000000			DC XL16'7FF8A00000000007FF0A0000000000000			
0002F380	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/-2.0/-inf'			
0002F3B0	7FF8A000 00000000		5143				
0002F3C0	D4E2C4C2 40D5C640		5144	DC CL48'MSDB NF +SNaN/-2.0/-inf'			
0002F3F0	7FF8A000 00000000			DC XL16'7FF8A0000000000FFF000000000000000'			
002F400	D4E2C4C2 D940D5C6		5146	DC CL48'MSDBR NF +SNaN/-2.0/-2.0'			

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
002F430	7FF8A000 00000000			5147	DC XL16'7FF8A00000000000C0000000000000000000			
002F440	D4E2C4C2 40D5C640			5148	DC CL48'MSDB NF +SNaN/-2.0/-2.0'			
	7FF8A000 00000000			5149	DC XL16'7FF8A00000000000C00000000000000000000			
002F480	D4E2C4C2 D940D5C6			5150	DC CL48'MSDBR NF +SNaN/-2.0/-0'			
	7FF8A000 00000000				DC XL16'7FF8A00000000000800000000000000000000			
	D4E2C4C2 40D5C640			5152				
	7FF8A000 00000000			5153				
	D4E2C4C2 D940D5C6			5154	·			
	7FF8A000 00000000				DC XL16'7FF8A00000000000000000000000000000000000			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF +SNaN/-2.0/+0'			
	7FF8A000 00000000			5157				
	D4E2C4C2 D940D5C6			5158	DC CL48'MSDBR NF +SNaN/-2.0/+2.0'			
	7FF8A000 00000000			5159				
	D4E2C4C2 40D5C640			5160	DC CL48'MSDB NF +SNaN/-2.0/+2.0'			
	7FF8A000 00000000				DC XL16'7FF8A000000000004000000000000000000000000			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF +SNaN/-2.0/+inf'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0000000000000000000000			
	D4E2C4C2 40D5C640			5164				
	7FF8A000 00000000			5165				
	D4E2C4C2 D940D5C6			5166	· · · · · · · · · · · · · · · · · · ·			
002F6C0	7FF8A000 00000000 D4E2C4C2 40D5C640			5167				
	7FF8A000 00000000			5169	DC CL48'MSDB NF +SNaN/-2.0/-QNaN' DC XL16'7FF8A0000000000FFF8B00000000000'			
002F700	D4E2C4C2 D940D5C6			5170	DC CL48'MSDBR NF +SNaN/-2.0/+SNaN'			
002F730	7FF8A000 00000000			5170				
	D4E2C4C2 40D5C640			5171	DC CL48'MSDB NF +SNaN/-2.0/+SNaN'			
	7FF8A000 00000000			5172				
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF +SNaN/-0/-inf'			
	7FF8A000 00000000				DC XL16'7FF8A0000000000FFF00000000000000			
	D4E2C4C2 40D5C640			5176				
	7FF8A000 00000000			5177	DC XL16'7FF8A0000000000FFF00000000000000'			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF +SNaN/-0/-2.0'			
	7FF8A000 00000000				DC XL16'7FF8A00000000000C0000000000000000000			
002F840	D4E2C4C2 40D5C640			5180				
0002F870	7FF8A000 00000000			5181	DC XL16'7FF8A00000000000C000000000000000000			
002F880	D4E2C4C2 D940D5C6			5182	DC CL48'MSDBR NF +SNaN/-0/-0'			
002F8B0	7FF8A000 00000000			5183	DC XL16'7FF8A0000000000000000000000000000000			
0002F8C0	D4E2C4C2 40D5C640			5184	DC CL48'MSDB NF +SNaN/-0/-0'			
002F8F0	7FF8A000 00000000			5185	DC XL16'7FF8A00000000000800000000000000000000			
002F900	D4E2C4C2 D940D5C6			5186	DC CL48'MSDBR NF +SNaN/-0/+0'			
002F930	7FF8A000 00000000			5187	DC XL16'7FF8A000000000000000000000000000000000			
002F940	D4E2C4C2 40D5C640			5188	DC CL48'MSDB NF +SNaN/-0/+0'			
002F970	7FF8A000 00000000			5189	DC XL16'7FF8A00000000000000000000000000000000			
002F980	D4E2C4C2 D940D5C6			5190	DC CL48'MSDBR NF +SNaN/-0/+2.0'			
002F9B0	7FF8A000 00000000			5191				
002F9C0	D4E2C4C2 40D5C640				DC CL48'MSDB NF +SNaN/-0/+2.0'			
002F9F0	7FF8A000 00000000			5193	DC XL16'7FF8A00000000000400000000000000000000			
002FA00	D4E2C4C2 D940D5C6			5194				
002FA30	7FF8A000 00000000			5195	DC XL16'7FF8A00000000007FF00000000000000000			
0002FA40	D4E2C4C2 40D5C640			5196	DC CL48'MSDB NF +SNaN/-0/+inf'			
002FA70	7FF8A000 00000000			5197	DC XL16'7FF8A00000000007FF00000000000000000			
002FA80	D4E2C4C2 D940D5C6			5198	DC CL48'MSDBR NF +SNaN/-0/-QNaN'			
0002FAB0	7FF8A000 00000000			5199	DC XL16'7FF8A0000000000FFF8B00000000000'			
0002FAC0	D4E2C4C2 40D5C640			5200	DC CL48'MSDB NF +SNaN/-0/-QNaN'			
002FAF0	7FF8A000 00000000			5201	DC XL16'7FF8A0000000000FFF8B00000000000'			
002FB00	D4E2C4C2 D940D5C6			5202	DC CL48'MSDBR NF +SNaN/-0/+SNaN'			

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LOC	OBJECT CODE	ADDR1 ADDR2	STMT				
0002FB30	7FF8A000 00000000		5203				
0002FB40	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/-0/+SNaN'			
002FB70	7FF8A000 00000000			DC XL16'7FF8A000000000007FF0A000000000000'			
002FB80	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+0/-inf'			
0002FBB0 0002FBC0	7FF8A000 00000000 D4E2C4C2 40D5C640			DC XL16'7FF8A00000000000FFF000000000000000000000			
00021 BC0 0002FBF0	7FF8A000 00000000			DC XL16'7FF8A0000000000FFF0000000000000000			
002FC00	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+0/-2.0'			
0002FC30	7FF8A000 00000000			DC XL16'7FF8A0000000000C000000000000000000			
0002FC40	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+0/-2.0'			
0002FC70	7FF8A000 00000000			DC XL16'7FF8A00000000000C000000000000000000			
0002FC80	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+0/-0'			
0002FCB0	7FF8A000 00000000			DC XL16'7FF8A0000000000000000000000000000000			
0002FCC0	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+0/-0'			
0002FCF0 0002FD00	7FF8A000 00000000 D4E2C4C2 D940D5C6			DC XL16'7FF8A00000000000800000000000000000000000			
0002FD00 0002FD30	7FF8A000 00000000			DC XL16'7FF8A00000000000000000000000000000000000			
0002FD40	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+0/+0'			
0002FD70	7FF8A000 00000000			DC XL16'7FF8A00000000000000000000000000000000000			
0002FD80	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+0/+2.0'			
0002FDB0	7FF8A000 00000000			DC XL16'7FF8A00000000000400000000000000000000			
002FDC0	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+0/+2.0'			
002FDF0	7FF8A000 00000000			DC XL16'7FF8A00000000000400000000000000000000			
002FE00	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+0/+inf'			
002FE30	7FF8A000 00000000			DC XL16'7FF8A000000000007FF000000000000000'			
0002FE40	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+0/+inf'			
0002FE70 0002FE80	7FF8A000 00000000 D4E2C4C2 D940D5C6		5229 5230				
00021	7FF8A000 00000000			DC XL16'7FF8A0000000000FFF8B00000000000'			
0002FEC0	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+0/-QNaN'			
0002FEF0	7FF8A000 00000000			DC XL16'7FF8A0000000000FFF8B00000000000'			
0002FF00	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+0/+SNaN'			
	7FF8A000 00000000			DC XL16'7FF8A00000000007FF0A00000000000'			
0002FF40	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+0/+SNaN'			
0002FF70	7FF8A000 00000000		5237				
0002FF80	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+2.0/-inf'			
0002FFB0	7FF8A000 00000000			DC XL16'7FF8A00000000000FFF00000000000000'			
0002FFC0 0002FFF0	D4E2C4C2 40D5C640 7FF8A000 00000000			DC CL48'MSDB NF +SNaN/+2.0/-inf' DC XL16'7FF8A0000000000FFF0000000000000000000000			
00021110	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+2.0/-2.0'			
00030000	7FF8A000 00000000		5243				
00030040	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+2.0/-2.0'			
0030070	7FF8A000 00000000			DC XL16'7FF8A00000000000C0000000000000000000			
080080	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+2.0/-0'			
00300B0	7FF8A000 00000000		5247				
00300C0	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+2.0/-0'			
00300F0	7FF8A000 00000000		5249				
0030100	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+2.0/+0'			
00030130 00030140	7FF8A000 00000000 D4E2C4C2 40D5C640			DC XL16'7FF8A00000000000000000000000000000000000			
00030140	7FF8A000 00000000		5253				
00030170	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+2.0/+2.0'			
0030180 000301B0	7FF8A000 00000000			DC XL16'7FF8A000000000040000000000000000000000000			
000301C0	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+2.0/+2.0'			
000301F0	7FF8A000 00000000			DC XL16'7FF8A00000000000400000000000000000000			
0030200	D4E2C4C2 D940D5C6		5258	DC CL48'MSDBR NF +SNaN/+2.0/+inf'			

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LOC	OBJECT CODE	ADDR1 ADD	DR2 STMT				
	7FF8A000 00000000			DC XL16'7FF8A00000000007FF00000000000000'			
	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+2.0/+inf'			
	7FF8A000 00000000			DC XL16'7FF8A00000000007FF000000000000000'			
	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+2.0/-QNaN'			
	7FF8A000 00000000 D4E2C4C2 40D5C640			DC XL16'7FF8A0000000000FFF8B0000000000' DC CL48'MSDB NF +SNaN/+2.0/-QNaN'			
	7FF8A000 00000000			DC XL16'7FF8A0000000000FFF8B0000000000'			
	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+2.0/+SNaN'			
	7FF8A000 00000000			DC XL16'7FF8A0000000007FF0A0000000000'			
	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+2.0/+SNaN'			
	7FF8A000 00000000			DC XL16'7FF8A00000000007FF0A00000000000'			
0030380	D4E2C4C2 D940D5C6		5270	DC CL48'MSDBR NF +SNaN/+inf/-inf'			
	7FF8A000 00000000			DC XL16'7FF8A0000000000FFF000000000000000'			
	D4E2C4C2 40D5C640			DC CL48 MSDB NF +SNaN/+inf/-inf'			
	7FF8A000 00000000			DC XL16'7FF8A0000000000FFF000000000000000'			
	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+inf/-2.0'			
	7FF8A000 00000000			DC XL16'7FF8A00000000000000000000000000000000000			
	D4E2C4C2 40D5C640 7FF8A000 00000000			DC CL48'MSDB NF +SNaN/+inf/-2.0' DC XL16'7FF8A00000000000000000000000000000000000			
	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+inf/-0'			
	7FF8A000 00000000			DC XL16'7FF8A00000000008000000000000000000			
	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+inf/-0'			
	7FF8A000 00000000			DC XL16'7FF8A0000000000800000000000000000			
	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+inf/+0'			
0030530	7FF8A000 00000000		5283	DC XL16'7FF8A0000000000000000000000000000000			
	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+inf/+0'			
	7FF8A000 00000000						
	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+inf/+2.0'			
	7FF8A000 00000000						
	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+inf/+2.0'			
	7FF8A000 00000000 D4E2C4C2 D940D5C6			DC XL16'7FF8A000000000004000000000000000000000000			
	7FF8A000 00000000			DC XL16'7FF8A0000000007FF0000000000000			
	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+inf/+inf'			
	7FF8A000 00000000						
	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+inf/-QNaN'			
	7FF8A000 00000000			DC XL16'7FF8A0000000000FFF8B000000000000'			
	D4E2C4C2 40D5C640		5296	DC CL48'MSDB NF +SNaN/+inf/-QNaN'			
	7FF8A000 00000000		5297	DC XL16'7FF8A0000000000FFF8B000000000000'			
	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/+inf/+SNaN'			
	7FF8A000 00000000		5299	DC XL16'7FF8A00000000007FF0A0000000000'			
	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/+inf/+SNaN'			
	7FF8A000 00000000 D4E2C4C2 D940D5C6			DC XL16'7FF8A000000000007FF0A00000000000' DC CL48'MSDBR NF +SNaN/-QNaN/-inf'			
	7FF8A000 00000000			· · · · · · · · · · · · · · · · · · ·			
	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/-QNaN/-inf'			
	7FF8A000 00000000			DC XL16'7FF8A000000000FFF00000000000000			
	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/-QNaN/-2.0'			
	7FF8A000 00000000			DC XL16'7FF8A0000000000C00000000000000000'			
	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/-QNaN/-2.0'			
	7FF8A000 00000000		5309	DC XL16'7FF8A0000000000C00000000000000000000			
	D4E2C4C2 D940D5C6			DC CL48'MSDBR NF +SNaN/-QNaN/-0'			
	7FF8A000 00000000			DC XL16'7FF8A0000000000080000000000000000000			
	D4E2C4C2 40D5C640			DC CL48'MSDB NF +SNaN/-QNaN/-0'			
	7FF8A000 00000000			DC XL16'7FF8A00000000008000000000000000000000000			
0030900	D4E2C4C2 D940D5C6		5314	DC CL48'MSDBR NF +SNaN/-QNaN/+0'			

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
0030930	7FF8A000 00000000			5315	DC XL16'7FF8A000000000000000000000000000000			
0030940	D4E2C4C2 40D5C640			5316	DC CL48'MSDB NF +SNaN/-QNaN/+0'			
	7FF8A000 00000000				DC XL16'7FF8A000000000000000000000000000000000			
					DC CL48'MSDBR NF +SNaN/-QNaN/+2.0'			
					DC XL16'7FF8A000000000000400000000000000000000000			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF +SNaN/-QNaN/+2.0'			
	7FF8A000 00000000 D4E2C4C2 D940D5C6				DC XL16'7FF8A000000000004000000000000000000000000			
					DC CL48'MSDBR NF +SNaN/-QNaN/+inf' DC XL16'7FF8A00000000007FF00000000000000			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF +SNaN/-QNaN/+inf'			
					DC XL16'7FF8A0000000007FF0000000000000			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF +SNaN/-QNaN/-QNaN'			
	7FF8A000 00000000				DC XL16'7FF8A0000000000FFF8B00000000000'			
	D4E2C4C2 40D5C640			5328	DC CL48'MSDB NF +SNaN/-QNaN/-QNaN'			
					DC XL16'7FF8A0000000000FFF8B0000000000000'			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF +SNaN/-QNaN/+SNaN'			
					DC XL16'7FF8A00000000007FF0A00000000000'			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF +SNaN/-QNaN/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A0000000000'			
	D4E2C4C2 D940D5C6 7FF8A000 00000000				DC CL48'MSDBR NF +SNaN/+SNaN/-inf' DC XL16'7FF8A0000000000FFF00000000000000			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF +SNaN/+SNaN/-inf'			
					DC XL16'7FF8A0000000000FFF00000000000000			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF +SNaN/+SNaN/-2.0'			
	7FF8A000 00000000				DC XL16'7FF8A0000000000C0000000000000000'			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF +SNaN/+SNaN/-2.0'			
0030C70	7FF8A000 00000000				DC XL16'7FF8A00000000000C0000000000000000000			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF +SNaN/+SNaN/-0'			
	7FF8A000 00000000				DC XL16'7FF8A0000000000080000000000000000000			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF +SNaN/+SNaN/-0'			
	7FF8A000 00000000				DC XL16'7FF8A00000000000800000000000000000000000			
	D4E2C4C2 D940D5C6 7FF8A000 00000000				DC CL48'MSDBR NF +SNaN/+SNaN/+0' DC XL16'7FF8A00000000000000000000000000000000000			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF +SNaN/+SNaN/+0'			
					DC XL16'7FF8A0000000000000000000000000000000			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF +SNaN/+SNaN/+2.0'			
	7FF8A000 00000000				DC XL16'7FF8A000000000040000000000000000			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF +SNaN/+SNaN/+2.0'			
0030DF0	7FF8A000 00000000			5353	DC XL16'7FF8A0000000000040000000000000000000			
	D4E2C4C2 D940D5C6				<pre>DC CL48'MSDBR NF +SNaN/+SNaN/+inf'</pre>			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF000000000000000'			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF +SNaN/+SNaN/+inf'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0000000000000'			
	D4E2C4C2 D940D5C6 7FF8A000 00000000				DC CL48'MSDBR NF +SNaN/+SNaN/-QNaN' DC XL16'7FF8A0000000000FFF8B00000000000'			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF +SNaN/+SNaN/-QNaN'			
	7FF8A000 00000000				DC XL16'7FF8A0000000000FFF8B0000000000'			
	D4E2C4C2 D940D5C6				DC CL48'MSDBR NF +SNaN/+SNaN/+SNaN'			
	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A0000000000'			
	D4E2C4C2 40D5C640				DC CL48'MSDB NF +SNaN/+SNaN/+SNaN'			
0030F70	7FF8A000 00000000				DC XL16'7FF8A00000000007FF0A000000000000'			
		00000400	00000001	5367				
		00030F80	00000001		LBFPNFFL_GOOD EQU *			
2030F80	D4E2C4C2 D961D4E2			5370	DC CL48 MSDBR/MSDB NF -inf/-inf/-inf FPCR'			

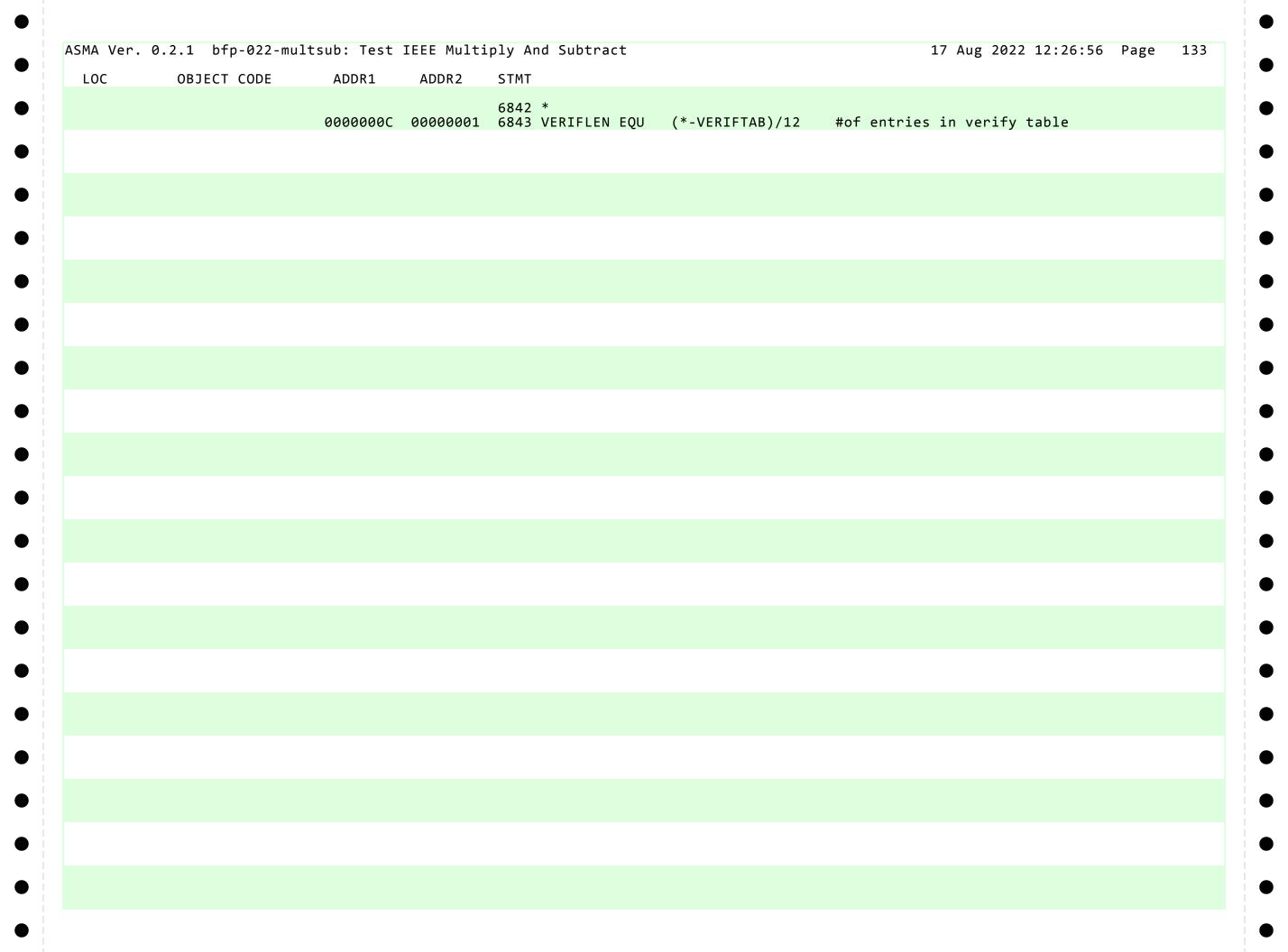
LOC						Page	124
	OBJECT CODE	ADDR1	ADDR2	STMT			
	00180000 F8001C00			6435			
	D4E2C4C2 D961D4E2			6436	·		
	00180000 F8001000			6437			
	D4E2C4C2 D961D4E2			6438	•		
	00000000 F8000000 D4E2C4C2 D961D4E2			6439 6440			
	00080000 F8000C00			6441			
	D4E2C4C2 D961D4E2				DC CL48'MSDBR/MSDB F Trun FPCR'		
	00080000 F8000800				DC XL16'00080000F80008000080000F8000800'		
		00000007	00000001	6444	LBFPFLGS_NUM EQU (*-LBFPFLGS_GOOD)/64		
				6445			
				6446			
00000460	D4526462 D064D452	000394C0	00000001		LBFPRMO_GOOD_EQU *		
	D4E2C4C2 D961D4E2 3FF90000 00000007				DC CL48'MSDBR/MSDB RM +NZ RNTE' DC XL16'3FF9000000000073FF900000000007'		
	D4E2C4C2 D961D4E2			6449 6450			
	3FF90000 00000007			6451			
	D4E2C4C2 D961D4E2			6452			
	3FF90000 00000008			6453			
00039580 Г	D4E2C4C2 D961D4E2			6454			
	3FF90000 00000007			6455			
	D4E2C4C2 D961D4E2			6456	•		
	3FF90000 00000007			6457			
	D4E2C4C2 D961D4E2 BFF90000 00000007			6458 6459			
	D4E2C4C2 D961D4E2			6460	DC CL48'MSDBR/MSDB RM -NZ RZ'		
	BFF90000 00000007			6461			
	D4E2C4C2 D961D4E2			6462			
	BFF90000 00000007			6463	DC XL16'BFF9000000000007BFF9000000000007'		
	D4E2C4C2 D961D4E2			6464	·		
	BFF90000 00000008			6465			
	D4E2C4C2 D961D4E2			6466			
	BFF90000 00000007 D4E2C4C2 D961D4E2				DC XL16'BFF9000000000007BFF9000000000007' DC CL48'MSDBR/MSDB RM +NA RNTE'		
	3FF90000 0000000D			6469			
	D4E2C4C2 D961D4E2				DC CL48'MSDBR/MSDB RM +NA RZ'		
	3FF90000 0000000C				DC XL16'3FF90000000000C3FF9000000000C'		
	D4E2C4C2 D961D4E2				DC CL48'MSDBR/MSDB RM +NA RP'		
	3FF90000 000000D				DC XL16'3FF90000000000D3FF9000000000D'		
	D4E2C4C2 D961D4E2				DC CL48'MSDBR/MSDB RM +NA RM'		
	3FF90000 0000000C				DC XL16'3FF900000000000C3FF90000000000C'		
	D4E2C4C2 D961D4E2 3FF90000 0000000D				DC CL48'MSDBR/MSDB RM +NA RFS' DC XL16'3FF900000000000D3FF90000000000D'		
	D4E2C4C2 D961D4E2				DC CL48'MSDBR/MSDB RM -NA RNTE'		
	BFF90000 0000000D			6479	·		
	D4E2C4C2 D961D4E2				DC CL48'MSDBR/MSDB RM -NA RZ'		
000398F0 E	BFF90000 0000000C			6481	DC XL16'BFF900000000000CBFF900000000000C'		
	D4E2C4C2 D961D4E2				DC CL48'MSDBR/MSDB RM -NA RP'		
	BFF90000 0000000C				DC XL16'BFF900000000000CBFF90000000000C'		
	D4E2C4C2 D961D4E2			6484	·		
	BFF90000 0000000D D4E2C4C2 D961D4E2			6485 6486	DC XL16'BFF900000000000DBFF900000000000D' DC CL48'MSDBR/MSDB RM -NA RFS'		
	BFF90000 0000000D			6487			
	D4E2C4C2 D961D4E2				DC CL48'MSDBR/MSDB RM +TZ RNTE'		
	3FF90000 00000008				DC XL16'3FF9000000000083FF90000000008'		
	D4E2C4C2 D961D4E2				DC CL48'MSDBR/MSDB RM +TZ RZ'		

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
0039A30	3FF90000 00000008			6491 DC XL16'3FF9000000000083FF90000000000	•		
	D4E2C4C2 D961D4E2			6492 DC CL48'MSDBR/MSDB RM +TZ RP'			
	3FF90000 00000009			6493 DC XL16'3FF9000000000093FF900000000000	, 1		
	D4E2C4C2 D961D4E2			6494 DC CL48'MSDBR/MSDB RM +TZ RM'			
	3FF90000 00000008 D4E2C4C2 D961D4E2			6495 DC XL16'3FF9000000000083FF900000000000	; •		
	3FF90000 00000009			6496 DC CL48'MSDBR/MSDB RM +TZ RFS' 6497 DC XL16'3FF900000000093FF900000000009	•		
	D4E2C4C2 D961D4E2			6498 DC CL48'MSDBR/MSDB RM -TZ RNTE'			
0039B30	BFF90000 00000008			6499 DC XL16'BFF900000000008BFF900000000008	1		
	D4E2C4C2 D961D4E2			6500 DC CL48'MSDBR/MSDB RM -TZ RZ'			
0039B70	BFF90000 00000008			6501 DC XL16'BFF900000000008BFF900000000000	1		
0039B80	D4E2C4C2 D961D4E2			6502 DC CL48'MSDBR/MSDB RM -TZ RP'			
0039BB0				6503 DC XL16'BFF900000000008BFF9000000000008			
	D4E2C4C2 D961D4E2			6504 DC CL48'MSDBR/MSDB RM -TZ RM'	_		
0039BF0	BFF90000 00000009			6505 DC XL16'BFF900000000009BFF90000000000	, <b>.</b>		
	D4E2C4C2 D961D4E2			6506 DC CL48'MSDBR/MSDB RM -TZ RFS'			
0039C30				6507 DC XL16'BFF900000000009BFF900000000000	, <del>-</del>		
	D4E2C4C2 D961D4E2 3FF90000 0000001A			6508 DC CL48'MSDBR/MSDB RM +TA RNTE' 6509 DC XL16'3FF90000000001A3FF90000000001A			
	D4E2C4C2 D961D4E2			6510 DC CL48'MSDBR/MSDB RM +TA RZ'			
	3FF90000 00000019			6511 DC XL16'3FF9000000000193FF900000000019	, •		
	D4E2C4C2 D961D4E2			6512 DC CL48'MSDBR/MSDB RM +TA RP'			
	3FF90000 0000001A			6513 DC XL16'3FF90000000001A3FF90000000001A	T .		
0039D00	D4E2C4C2 D961D4E2			6514 DC CL48'MSDBR/MSDB RM +TA RM'			
	3FF90000 00000019			6515 DC XL16'3FF9000000000193FF900000000019	•		
	D4E2C4C2 D961D4E2			6516 DC CL48'MSDBR/MSDB RM +TA RFS'			
	3FF90000 00000019			6517 DC XL16'3FF9000000000193FF900000000019	, •		
				6518 DC CL48'MSDBR/MSDB RM -TA RNTE'			
0039DB0	BFF90000 0000001A D4E2C4C2 D961D4E2			6519 DC XL16'BFF90000000001ABFF900000000001A	, ·		
0039DE0				6520 DC CL48'MSDBR/MSDB RM -TA RZ' 6521 DC XL16'BFF900000000019BFF900000000019			
	D4E2C4C2 D961D4E2			6522 DC CL48'MSDBR/MSDB RM -TA RP'			
	BFF90000 00000019			6523 DC XL16'BFF900000000019BFF900000000019	, •		
	D4E2C4C2 D961D4E2			6524 DC CL48'MSDBR/MSDB RM -TA RM'			
0039E70				6525 DC XL16'BFF90000000001ABFF900000000001A	1		
	D4E2C4C2 D961D4E2			6526 DC CL48'MSDBR/MSDB RM -TA RFS'			
0039EB0	BFF90000 00000019			6527 DC XL16'BFF900000000019BFF900000000019	•		
		00000028	00000001	6528 LBFPRMO_NUM EQU (*-LBFPRMO_GOOD)/64			
				6529 *			
		00020500	0000000	6530 *			
0020560	D4E3C4C3 D0C4D4E3	00039EC0	00000001	6531 LBFPRMOF_GOOD EQU *			
	D4E2C4C2 D961D4E2 00080000 00080000			6532 DC CL48'MSDBR/MSDB RM +NZ RNTE, RZ FPCR' 6533 DC XL16'0008000000000000000000000000000000000	T.		
	D4E2C4C2 D961D4E2			6534 DC CL48'MSDBR/MSDB RM +NZ RP, RM FPCR'			
				6535 DC XL16'00080002000800020008000300080003	•		
	D4E2C4C2 D961D4E2			6536 DC CL48'MSDBR/MSDB RM +NZ RFS FPCR'			
				6537 DC XL16'000800070008000700000000000000000	1		
	D4E2C4C2 D961D4E2			6538 DC CL48'MSDBR/MSDB RM -NZ RNTE, RZ FPCR'			
	00080000 00080000			6539 DC XL16'0008000000080000008000100080001	•		
	D4E2C4C2 D961D4E2			6540 DC CL48'MSDBR/MSDB RM -NZ RP, RM FPCR'			
				6541 DC XL16'00080002000800020008000300080003	•		
				6542 DC CL48'MSDBR/MSDB RM -NZ RFS FPCR'	.1		
				6543 DC XL16'00080007000800070000000000000000000000			
040A500	D4E2C4C2 D961D4E2			6544 DC CL48'MSDBR/MSDB RM +NA RNTE, RZ FPCR' 6545 DC XL16'0008000000080000000000000000000000000	•		
0021070	00080000 00080000						

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
				6622 ****** 6623 * 6624 ******		VERIFICATI	**************************************
0003A560				6626 VERISUB	DS	0Н	
				6627 * 6628 ** 6629 *	Loop	through the VERIF	Y TABLE
0003A560 0003A564 0003A568	4110 C32C 4120 000C		0003A7EC 0000000C	6631 6632 6633	LA LA BASR	R1,VERIFTAB R2,VERIFLEN R3,0	R1> Verify table R2 <== Number of entries Set top of loop
						·	·
0003A56A	9846 1000 4D70 C0C2		00000000 0003A582	6635 6636	LM BAS	R4,R6,0(R1) R7,VERIFY	Load verify table values Verify results
	4110 100C		0000000C		LA	R1,12(,R1) R2,R3	Next verify table entry Loop through verify table
0003A578 0003A57C	9500 C278 078D		0003A738	6640 6641	CLI BER	FAILFLAG,X'00' R13	Did all tests verify okay? Yes, return to caller
0003A57E	47F0 F238		00000238	6642	В	FAIL	No, load FAILURE disabled wait PSW
				6644 * 6645 ** 6646 *	Loop	through the ACTUA	L / EXPECTED results
0003A582	0D80			6648 VERIFY	BASR	R8,0	Set top of loop
	4770 C0DA	00000000	00000030 0003A59A	6651	CLC BNE	0(16,R4),48(R5) VERIFAIL	Actual results == Expected results? No, show failure
	4140 4010 4150 5040 0668		00000010 00000040	6652 VERINEXT 6653 6654	LA LA BCTR	R4,16(,R4) R5,64(,R5) R6,R8	Next actual result Next expected result Loop through results
0003A598	07F7			6656	BR	R7	Return to caller

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
				6754 *	Issue	HERCULES MESSAGE poin	**************************************
0003A73A	4900 C3BC		0003A87C	6757 MSG	СН	R0,=H'0'	Do we even HAVE a message?
0003A73E	07D2			6758	BNHR	R2	No, ignore
0003A740	9002 C2B0		0003A770	6760	STM	R0,R2,MSGSAVE	Save registers
0003A748	4900 C3BE 47D0 C290 4100 005F		0003A87E 0003A750 0000005F	6762 6763 6764	CH BNH LA	R0,=AL2(L'MSGMSG) MSGOK R0,L'MSGMSG	Message length within limits? Yes, continue No, set to maximum
0003A750 0003A752	1820 0620 4420 C2BC		0003A77C	6766 MSGOK 6767 6768	LR	R2,R0 R2,0 R2,MSGMVC	Copy length to work register Minus-1 for execute Copy message to O/P buffer
	4120 200A 4110 C2C2		0000000A 0003A782	6770 6771	LA LA	R2,1+L'MSGCMD(,R2) R1,MSGCMD	Calculate true command length Point to true command
	83120008 4780 C2AA 0000		0003A76A	6773 6774 6775	DC BZ DC	X'83',X'12',X'0008' MSGRET H'0'	Issue Hercules Diagnose X'008' Return if successful CRASH for debugging purposes
0003A76A 0003A76E	9802 C2B0 07F2		0003A770	6777 MSGRET 6778	LM BR	RØ,R2,MSGSAVE R2	Restore registers Return to caller
	00000000 00000000 D200 C2CB 1000	0003A78B	00000000	6780 MSGSAVE 6781 MSGMVC	DC MVC	3F'0' MSGMSG(0),0(R1)	Registers save area Executed instruction
	D4E2C7D5 D6C8405C 40404040 40404040			6783 MSGCMD 6784 MSGMSG	DC DC	C'MSGNOH * ' CL95' '	*** HERCULES MESSAGE COMMAND *** The message text to be displayed

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LOC	OBJECT CODE	ADDR1 ADD	R2 STMT				
			6786	*****	*****	***************	
			6787	*		VERIFY TABLE	
			6788	******	*****	****************	
			6789	*			
			6790	*	A(act	ual results), A(expected results), A(#of results)	
			6791	*			
			6792	*****	*****	****************	
0003A7EC			6794	VERIFTAB	DC	0F'0'	
0003A7EC	00001000		6795		DC	A(SBFPNFOT)	
0003A7F0	00010000		6796		DC	A(SBFPNFOT_GOOD)	
0003A7F4	00000200		6797		DC	A(SBFPNFOT_NUM)	
			6798	*		·	
0003A7F8	00003000		6799		DC	A(SBFPNFFL)	
0003A7FC	00018000		6800		DC	A(SBFPNFFL GOOD)	
0003A800	00000200		6801		DC	A(SBFPNFFL_NUM)	
3337.300			6802	*		·····················/	
0003A804	00005000		6803		DC	A(SBFPOUT)	
0003A808	00020000		6804		DC	A(SBFPOUT GOOD)	
0003A80C	00000007		6805		DC	A(SBFPOUT_NUM)	
OOOSAOOC	20000007		6806	*	<i>D</i> C	A(3511001_N011)	
0003A810	00005100		6807		DC	A(SBFPFLGS)	
0003A814	00003100 000201C0		6808		DC	A(SBFPFLGS GOOD)	
0003A818	00000007		6809		DC	A(SBFPFLGS_NUM)	
0003A010	0000007		6810	*	DC	A(3bi Fi Ed3_Noil)	
0003A81C	00005200		6811		DC	A(SBFPRMO)	
0003A81C	00020380		6812		DC	A(SBFPRMO GOOD)	
0003A820	00000018		6813		DC	A(SBFPRMO_NUM)	
0003A624	00000018		6814	*	DC	A(SBPPKMO_NOM)	
0003A828	00005500		6815	•	DC	A/CDEDDMOE\	
0003A82C	00020980		6816		DC	A(SBFPRMOF)	
0003A82C			6817		DC DC	A(SBFPRMOF_GOOD)	
0003A630	00000018			*	DC	A(SBFPRMOF_NUM)	
00024024	00000000		6818	1	DC	A/I DEDNEOT)	
0003A834	00006000		6819		DC	A(LBFPNFOT)	
0003A838	00020F80		6820		DC	A(LBFPNFOT_GOOD)	
0003A83C	00000400		6821	*	DC	A(LBFPNFOT_NUM)	
00024040	00001000		6822		DC	4/1 DEDNEEL \	
0003A840	0000A000		6823		DC	A(LBFPNFFL)	
0003A844	00030F80		6824		DC	A(LBFPNFFL_GOOD)	
0003A848	00000200		6825	*	DC	A(LBFPNFFL_NUM)	
00024046	0000000		6826	-1*	DC	A/I DEDOUT	
0003A84C	0000C000		6827		DC	A(LBFPOUT)	
0003A850	00038F80		6828		DC	A(LBFPOUT_GOOD)	
0003A854	0000000E		6829	Ψ	DC	A(LBFPOUT_NUM)	
00024056	00000000		6830	т	D.C	A ( L DEDEL CC )	
0003A858	0000C200		6831		DC	A(LBFPFLGS)	
0003A85C	00039300		6832		DC	A(LBFPFLGS_GOOD)	
0003A860	00000007		6833	ala.	DC	A(LBFPFLGS_NUM)	
0000151	00000500		6834	↑	20	A (   DEDDUC)	
0003A864	0000C500		6835		DC	A(LBFPRMO)	
0003A868	000394C0		6836		DC	A(LBFPRMO_GOOD)	
0003A86C	00000028		6837		DC	A(LBFPRMO_NUM)	
			6838	*			
0003A870	0000CA00		6839		DC	A(LBFPRMOF)	
0003A874	00039EC0		6840		DC	A(LBFPRMOF_GOOD)	
0003A878	00000018		6841		DC	A(LBFPRMOF_NUM)	



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LOC	OBJECT CODE	ADDR1	ADDR2	STMT					
3A87C 3A87C	0000			6845 6846	END	=H'0'			
3A87E	005F E68195A3 7A40			6847 6848		=AL2(L'MSGMSG) =CL6'Want: '			
3A886	C796A37A 4040			6849		=CL6'Got: '			

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFER	ENCES												
ACTUAL	-	03A70C	4	6747	6674	6709												
	F		4															
EXPECT	F	03A708	4	6746	6676	6681												
HELPERS	A	00027C	4	201	191	232												
PMULS	J	000000	239756	117														
.ANKEQ	С	03A6DE	3	6743	6682	6710												
IARHEX	C	03A728	16	6749	6750													
LR0	F	0002F0	4	242	210	211	212											
\IL	I	000238	4	199	6642													
ILADR	Ċ	03A6D6	8	6742	6681	6683	6709	6711										
AILDESC	Č	03A6A0	48	6738	6667	0005	0,05	0,11										
AILFLAG	X	03A738	1	6751	6640	6663												
			_															
AILMSG1	C	03A68C	68	6736	6668	6669	6700	6720										
AILMSG2	C	03A6D0	53	6740	6701	6702	6729	6730										
AILPSW	Х	0002E0	8	240	199													
AILVALS	C	03A6E1	36	6744	6685	6686	6687	6689	6690	6691	6693	6694	6695	6697	6698	6699	6713	6714
					6715	6717	6718	6719	6721	6722	6723	6725	6726	6727				
PCMCT	U	000005	1	709	450	649	-				_	-	-					
PCMODES	Č	00064C	1	703	709	453	652											
PCREGNT	X	00004C	4	243	332	344	389	405	455	464	532	544	589	605	654	663		
PCREGTR		0002F4		243	338	350	397		538	550	597	612	505	003	034	005		
	X		4		228	220	39/	412	228	שככ	<b>59</b> /	017						
PRØ	U	000000	1	138	222	~ ~ -	2.42	201	202	40-	450		<b>F</b> 2 2	F 2 -	F 4 2	F 0 1	<b>F</b> 0 0	
PR1	U	000001	1	139	330	334	340	391	393	401	458	460	530	534	540	591	593	601
					657	659												
PR10	U	00000A	1	148														
PR11	U	00000B	1	149														
PR12	U	00000C	1	150														
PR13	Ü	00000D	1	151														
PR14	Ü	00000E	1	152														
		00000E	_															
PR15	U		1	153														
PR2	U	000002	1	140														
PR3	U	000003	1	141														
PR4	U	000004	1	142	329	334	340	346	352	390	393	401	408	415	457	460	468	529
					534	540	546	552	590	593	601	608	615	656	659	666		
PR5	U	000005	1	143														
PR6	Ü	000006	$\bar{1}$	144														
PR7	Ü	000007	1	145														
			<del>-</del>		222	224	225	220	2/10	2/1	2/5	246	247	251	252	252	202	202
PR8	U	800000	1	146	333	334	335	339	340	341	345	346	347	351	352	353	392	393
					394	398	401	402	406	408	409	413	415	416	459	460	461	466
					468	469	533	534	535	539	540	541	545	546	547	551	552	553
					592	593	594	598	601	602	606	608	609	613	615	616	658	659
					660	665	666	667										
PR9	U	000009	1	147														
OODPSW	X	0002D0	8	239	236													
ELPERS	Ĥ	03A4C0	2	6582	156	201												
EXTRTAB	Ü	03A638	16	6750	6591	6595	6599	6603	6607	6683	6687	6691	6695	6699	6711	6715	6719	6723
TATIVIAD	U	02A030	10	0/30	6727	0090	しょうう	0003	0007	0003	0007	OOST	0093	0099	0/11	0/13	0/13	0/23
4ACE	1	000000	220756	^	0/2/													
IAGE	1	000000	239756	0	~													
BFPCT	U	000007	1	1010	277													
BFPF	I	000568	4	583	224													
BFPFLGS	U	00C200	1	1106	280	6831												
BFPFLGS GOOD	Ü	039300	1	6429	6444	6832												
BFPFLGS NUM	Ü	000007	1	6444	6833	5552												
			_	960		270												
BFPIN BFPINRM	D	000768	8		1010	278												
メーレーバドグ	F	000810	4	1036	1075	284												
		000454			~ ~ ~													
BFPNF BFPNFCT	H U	0004D6 000008	2 1	511 940	222 271													

SMA Ver. 0.2.1	01 p-022	-muıtsub			тстрту	Anu S	ubtrac	τ						17 Aug	2022	12:26:	30 Pa	ge	13
SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFER	ENCES													
BFPNFFL	U	00A000	1	1101	274	6823													
FPNFFL_GOOD	U	030F80	1	5369	6394	6824													
FPNFFL_NUM	U	000200	1	6394	6825														
FPNFIN	F	000728	4	931	940	272													
FPNFLP	H	0004E2	2	517	565	-/-													
FPNFOT	Ü	006000	1	1099	273	6819													
FPNFOT_GOOD	Ü	020F80	1	3317	5366	6820													
						0020													
FPNFOT_NUM	U	000400	1	5366	6821	6027													
FPOUT COOR	U	00C000	1	1104	279	6827													
FPOUT_GOOD	U	038F80	1	6397	6426	6828													
FPOUT_NUM	U	00000E	1	6426	6829														
FPRM	I	0005E2	4	642	226														
FPRMCT	U	800000	1	1075	283														
FPRMO	U	00C500	1	1109	285	6835													
FPRMOF	U	00CA00	1	1111	286	6839													
FPRMOF_GOOD	Ü	039EC0	$\bar{1}$	6531	6580	6840													
FPRMOF NUM	Ü	000018	1	6580	6841														
FPRMO_GOOD	Ü	0394C0	1	6447	6528	6836													
FPRMO_NUM	Ü	000028	1	6528	6837	0000													
NGF	5																		
		00033C	4	276	223														
NGNF	F -	00032C	4	270	221		6700	6734											
G	Ţ	03A73A	4	6757	6611	6670	6703	6/31											
GCMD	С	03A782	9	6783	6770	6771													
GMSG	C	03A78B	95	6784	6764	6781	6762												
GMVC	I	03A77C	6	6781	6768														
GOK	I	03A750	2	6766	6763														
GRET	I	03A76A	4	6777	6774														
GSAVE	F	03A770	4	6780	6760	6777													
INTCD	H	00008E	2	169	186	6589													
NOTDTA	T.	00020C	4	190	187	0303													
OLDPSW	Ū	000150	1	171	188	6593	6507	6601	6605										
MCK	Н	03A4C0	2	6588	192	0333	0337	0001	0003										
	П																		
MCOMMA	C	03A536	1	6618	6590	6504	6505	6507	6500	6500	C C O 1	cc02	6603	6605	6606	6607			
MPSW	C	03A53C	36	6620	6593	6594	6595	6597	6598	6599	6601	6602	6603	6605	6606	6607			
OGCHK	Н	000200	2	185	177														
OGCODE	С	03A532	4	6617	6589	6591													
OGMSG	C	03A51E	66	6615	6609	6610													
OGPSW	D	000228	8	198	197														
	U	000000	1	119	190	193	210	212	6609	6662	6668	6701	6729	6733	6757	6760	6762	6764	ļ
					6766	6777													
	U	000001	1	120	325	359	447	453	456	465	525	559	646	652	655	664	6610	6631	L
		555551	_		6635	6637	6669	6702	6730	6771	6781	200	0.10	552			5520	5551	
.0	U	00000A	1	129	214	216	218	221	223	225	312	313	318	323	383	384	443	444	1
.0	U	OOOOOA		123	512	513	518	523	583	584	642	643	210	525	202	504	++3	444	-
1	11	AAAAAA	1	120	212	212	210	323	202	304	042	043							
1	U	00000B	1	130	150	101	222	220	262	207	422	440	405	F 2 0	F C 3	F 0 7	<b>C</b> 22	~ A ¬	7
2	U	00000C	1	131	156	191	232	320	362	387	422	448	485	520	562	587	622	647	1
_					682														
3	U	00000D	1	132	192	215	217	219	222	224	226	233	315	366	386	423	446	487	1
					515	566	586	623	645	684	6613	6641							
4	U	00000E	1	133	195	196	234	235											
5	Ü	00000F	1	134	155	190	193												
	Ŭ	000002	1	121	312	314	365	383	385	422	443	445	485	512	514	565	583	585	5
	3	30002	_		622	642	644	682	6611	6632	6638	6670	6703	6731	6758	6760	6766	6767	
					6768	6770	6777	6778	0011	0032	0000	5576	0/03	0/31	0/30	0700	0700	0/0/	
					0/00	0//0	0///	0//0											
,	U	000003	1	122	312	329	364	383	390	391	392	398	406	408	413	415	419	443	,

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFER	FNCES								_				
STILDOE	111 5	VALUE	EENGIII	DETIN			<b>613</b>	615	610	642	656	657	650	CCE		600	6622	6620
Λ	U	000004	1	123	606 318	608 362	613 518	615 562	619 6635	642 6650	656 6652	657 6674	658 6713	665 6717	666 6721	680 6725	6633	6638
4	U	000004	1	123	318	330	346	352	361	450	453	475	518	530	546	552	561	649
R5	U	200003	1	124	652	673	6650	6653	6662	6667	6675	6676	6685	6689	6693	6697	6733	049
R6	U	000006	1	125	323	359	523	559	6635	6654	00/5	00/0	0005	0009	0093	0097	0/33	
R7	U	000007	1 1	125	323	333	339	345	351	358	384	394	402	409	416	420	444	461
( )	U	000007	1	120	469 620	472 643	483 660	523 667	533 670	539 6636	545 6656	551	558	584	594	602	609	616
88	U	000008	1	127	313	335	341	347	353	356	384	395	403	410	417	421	444	462
					470 621	473 643	484 661	513 668	535 671	541 681	547 6648	553 6654	556	584	595	603	610	617
R9	U	000009	1	128	313 650	336 673	342	348	354	357	451	475	513	536	542	548	554	557
MLONGS	F	00034C	4	282	225	3,3												
RMSHORTS	F	00031C	4	264	218													
SAVERØR5	F	03A710	4	6748	6662	6733												
SAVEREGS	F	00023C	4	200	190	193												
SBFPCT	i i	000230	1	832	259	1))												
BFPF	T	000007 0003EE	4	383	217													
SBFPFLGS	ij	005100	1	1090	262	6807												
BFPFLGS_GOOD	U	0201C0	1	3195	3210	6808												
SBFPFLGS_GOOD	11	000007	1	3210	6809	0000												
BFPIN		000674	4	779	832	260												
BFPINRM	, E	000674 0006C8	4	858	897	266												
BEPNE	H	0000C8	2	311	215	200												
BFPNFCT	Ü	000008	1	759	253													
BEPNEFL	Ü	003000	1	1085	256	6799												
BFPNFFL GOOD	Ü	018000	1	2149	3174	6800												
SBFPNFFL NUM	Ü	000200	1	3174	6801	0000												
SBFPNFIN	F	000654	4	750	759	254												
SBFPNFLP	H	000354	2	317	365	234												
SBFPNFOT	ii	001000	1	1083	255	6795												
SBFPNFOT_GOOD	Ü	010000	1	1121		6796												
SBFPNFOT_NUM	Ü	000200	1	2146	6797	0750												
SBFPOUT	Ü	005000	1	1088	261	6803												
SBFPOUT GOOD	ij	020000	1	3177	3192	6804												
SBFPOUT NUM	IJ	000007	1	3192	6805	0007												
SBFPRM	T	000468	4	443	219													
SBFPRMCT	II	000008	1	897	265													
SBFPRMO	ij	005200	1	1093	267	6811												
SBFPRMOF	IJ	005500	1	1095	268	6815												
SBFPRMOF GOOD	IJ	020980	1	3265	3314	6816												
SBFPRMOF NUM	Ü	000018	1	3314	6817	0010												
SBFPRMO_GOOD	Ü	020380	1	3213	3262	6812												
SBFPRMO NUM	IJ	000018	1	3262	6813	0012												
SHORTF	F	00030C	4	258	216													
SHORTNF	F	00030C	4	252	214													
START	H	000210	2	209	174													
STRTLABL	Ü	000000	1	118	168 1106	171 1109	173 1111	176 1119	184	1083	1085	1088	1090	1093	1095	1099	1101	1104
/ERIFAIL	I	03A59A	4	6662	6651													
	Ū	00000C	1	6843	6632													
/EKIFLEN	_		4	6794	6843	6631												
/ERIFLEN /ERIFTAB	F	DOA/EC	4	U/J <del>+</del>	0073													
/ERIFTAB	F	03A7EC 03A582	2			0051												
	F I I	03A582 03A58E	2 4	6648 6652	6636 6734	0031												

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SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES				
NTGOT L2(L'MSGMSG) L6'Got: '	C R	03A6D0 03A87E	6 2	6741 6847	6680 6708 6762				
L6'Got: ' L6'Want: ' '0'	C C	03A87E 03A886 03A880 03A87C	6 6	6849 6848	6708 6680				
0	п	03A87C	2	0846	6/5/				

ACMA Van 0 2 1 bCn 022 multaub. Test TEEE Multiply And Cubtonet	17 4 2022 12.26.56	Daga	120
ASMA Ver. 0.2.1 bfp-022-multsub: Test IEEE Multiply And Subtract MACRO DEFN REFERENCES	17 Aug 2022 12:26:56	Page	139
No defined macros			

