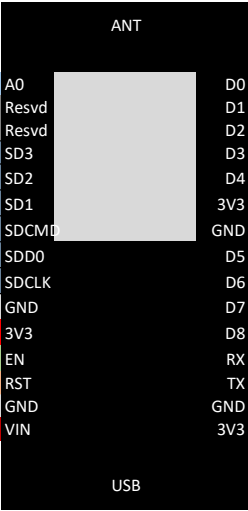


Func4	Func3	Func2	Func1	Func0
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ADC0				2	A0
					Resvd
					Resvd
HSPWIP	GPIO10	SPWIP	SD_DATA3	12	SD3
HSPHID	GPIO9	SPHID	SD_DATA2	11	SD2
U1RXD	GPIO8	SPID	SD_DATA1	13	SD1
U1RTS	GPIO11	SPICSO	SD_CMD	9	SDCM0
U1TXD	GPIO7	SPIQ	SD_DATA0	10	SDD0
U1CTS	GPIO6	SPICLK	SD_CLK	14	SDCLK
GND					GND
3.3 V					3V3
ENABLE				3	EN
RESET				1	RST
GND					GND
VIN					VIN



Func0	Func1	Func2	Func3	Func4
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D0	4	XPD_DCDC	RTC_GPIO0	EXT_WAKEUP	DEEPSLEEP	ANT_SW_BIT0	↕
D1	20	GPIO5	CLK_RTC				
D2	19	GPIO4	CLK_XTAL				
D3	18	GPIO0	SPICS2			CLK_OUT	↕
D4	17	GPIO2	I2SO_WS	U1TXD		U0TXD	↕
3V3				3.3 V			
GND				GND			
D5	5	MTMS	I2SI_WS	HSPICLK	GPIO14	U0DSR	↕
D6	6	MTDI	I2SI_DATA	HSPIQ_MISO	GPIO12	U0DTR	↕
D7	7	MTCK	I2SI_BCK	HSPID_MOSI	GPIO13	U0CTS	↕
D8	16	MTDO	I2SO_BCK	HSPICS	GPIO15	U0RTS	↕
RX	21	U0RXD	I2SO_DATA		GPIO3	CLK_XTAL	↕
TX	22	U0TXD	SPICS1		GPIO1	CLK_RTC	↕
GND				GND			
3V3				3.3 V			

