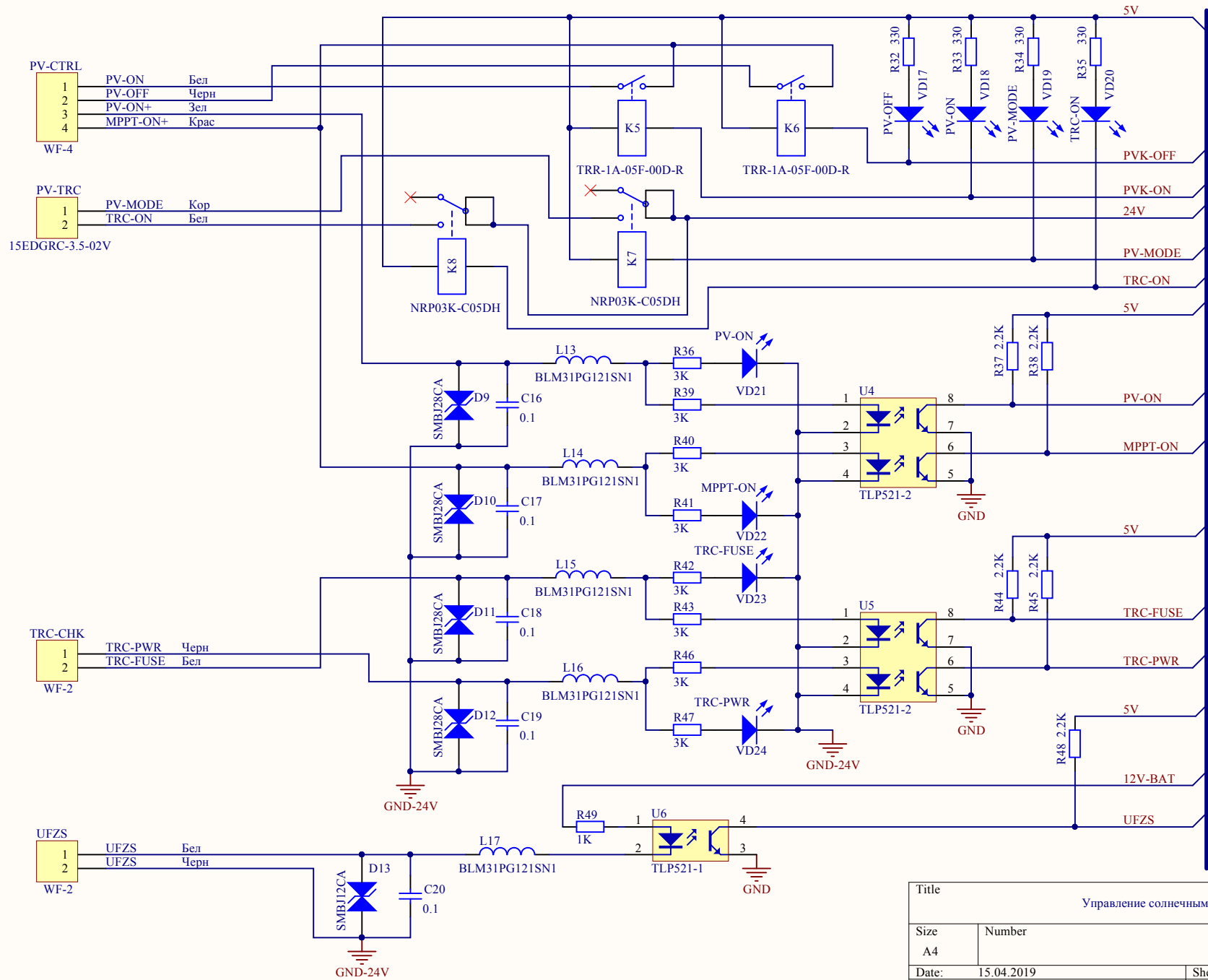


Title		
Стабилизаторы питания		
Size	Number	Revision
A4		1.0
Date:	15.04.2019	Sheet of 1
File:	C:\EmbeddedProject\Power.SchDoc	Drawn By: SRG

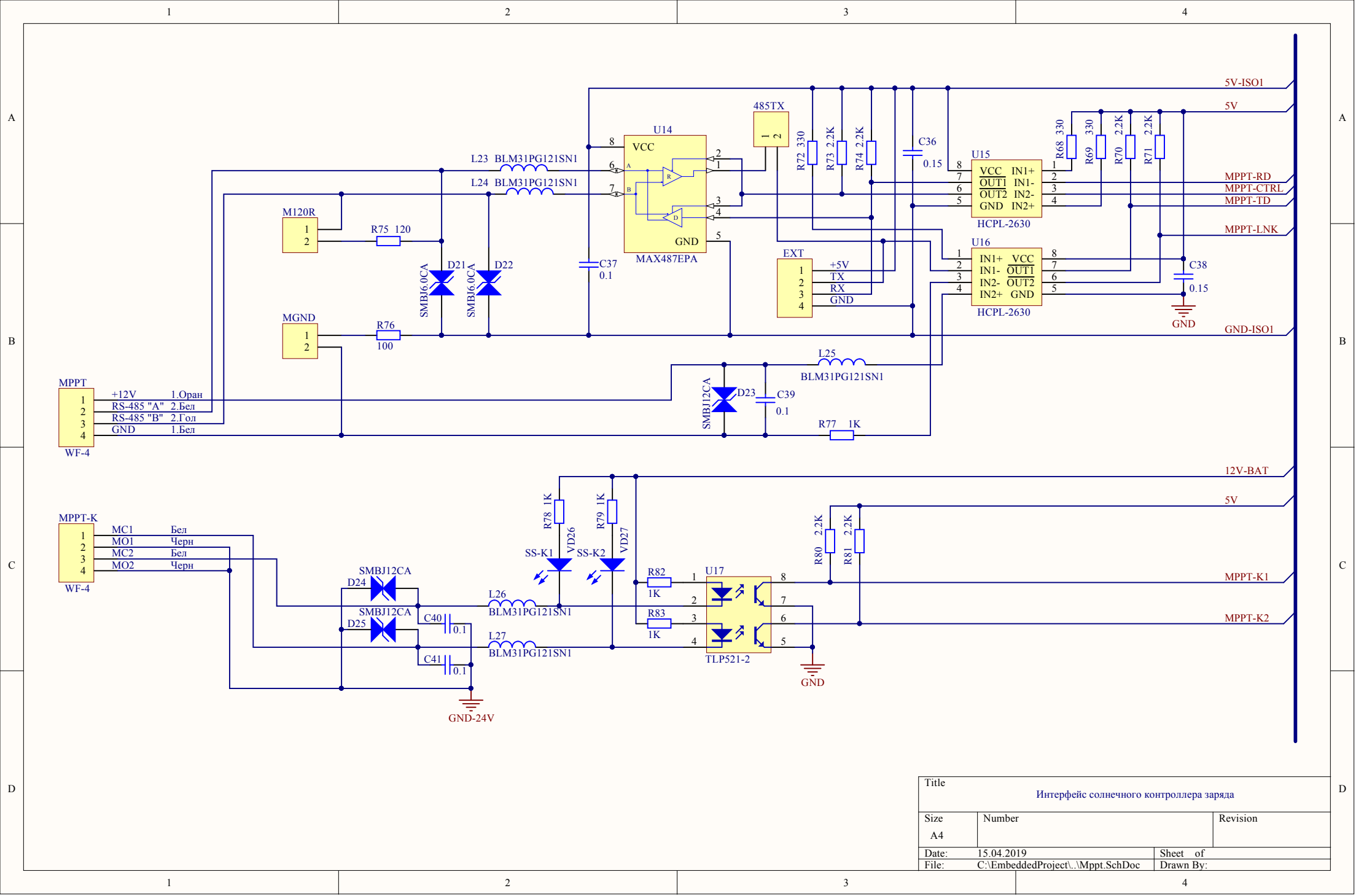




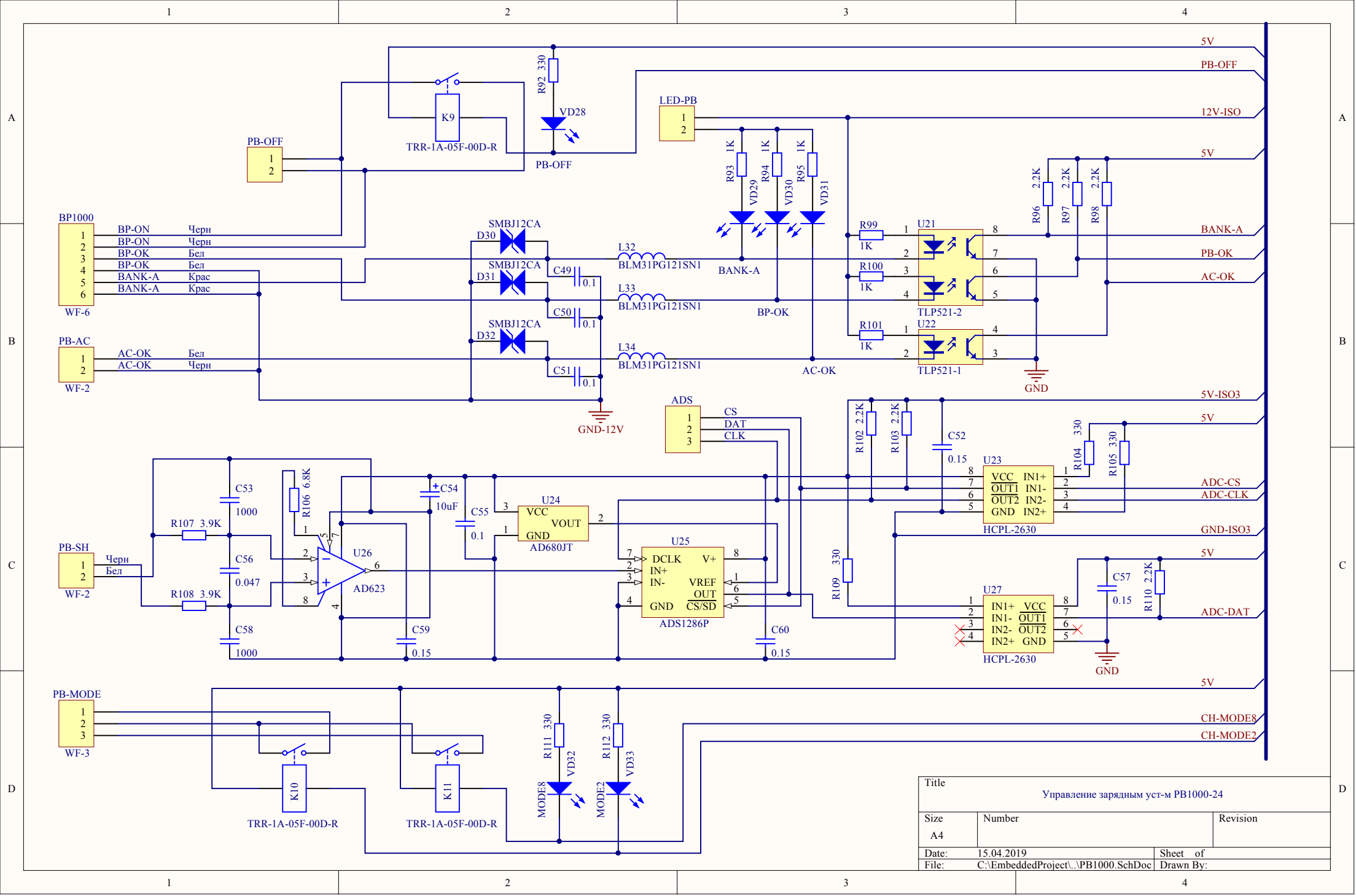
Title		
Управление солнечными панелями		
Size	Number	Revision
A4		
Date:	15.04.2019	Sheet of
File:	C:\EmbeddedProject\..PVCtrl.SchDoc	Drawn By:

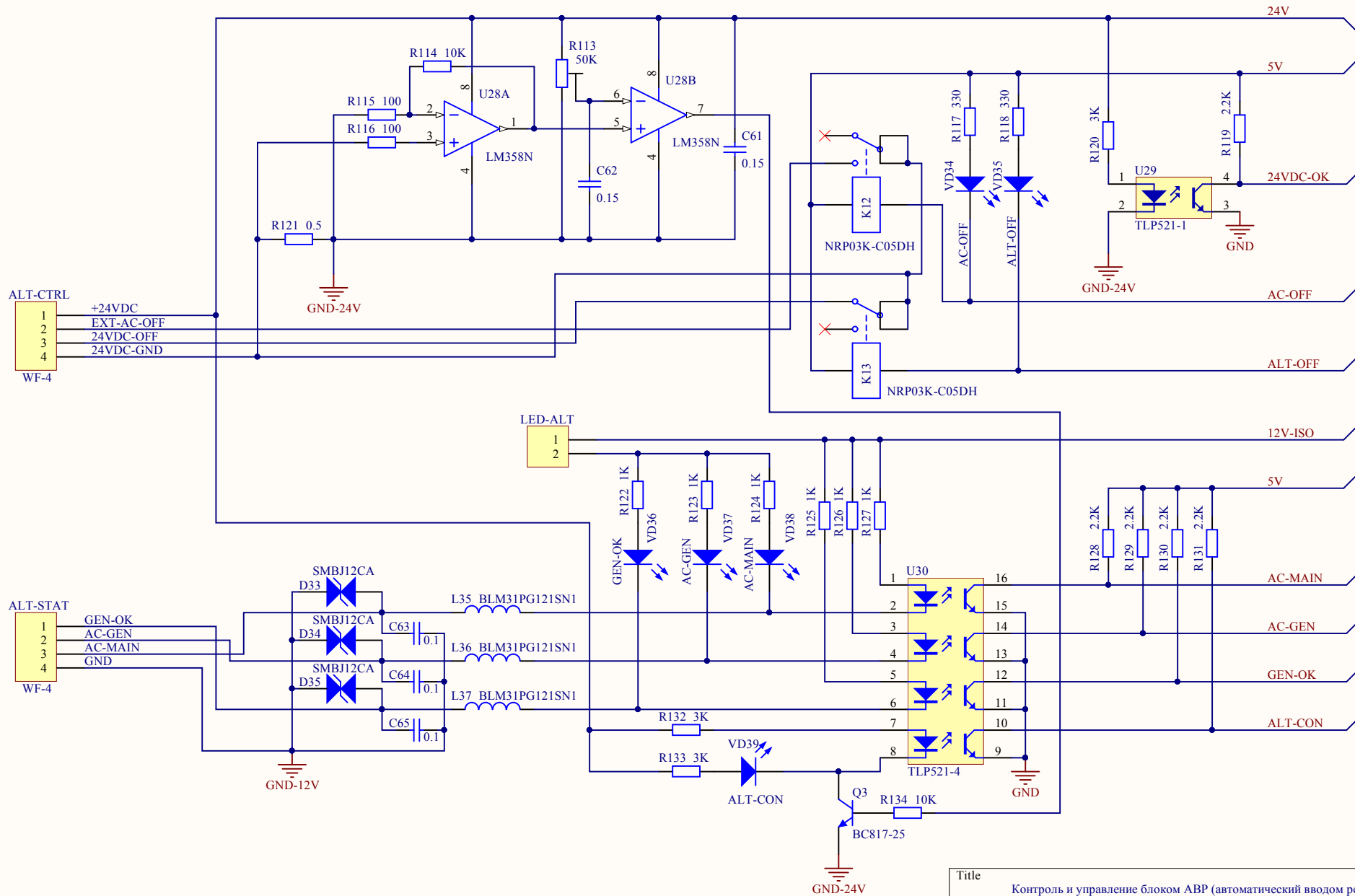


Title			Интерфейсы: BMW-600S/RS232(ISP)		
Size	Number			Revision	
A4					
Date:	15.04.2019			Sheet of	
File:	C:\EmbeddedProject\...\RS232.SchDoc			Drawn By:	



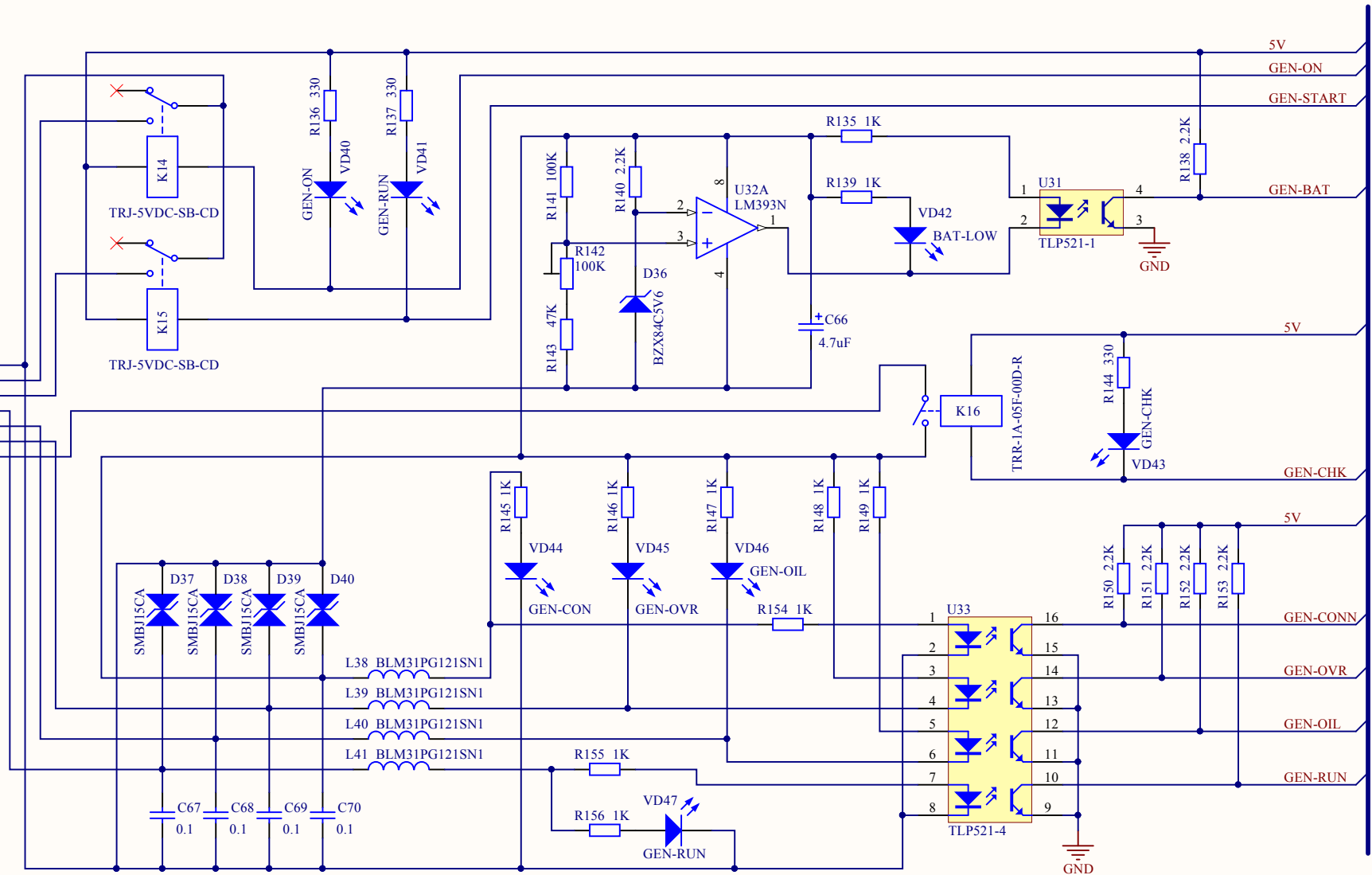
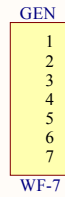






Title		
Контроль и управление блоком АБП (автоматический ввод резерва)		
Size	Number	Revision
A4		
Date:	15.04.2019	Sheet 8 of
File:	C:\EmbeddedProject\Alt.SchDoc	Drawn By:





Title		
Управление генератором		
Size	Number	Revision
A4		
Date:	15.04.2019	Sheet 9 of
File:	C:\EmbeddedProject\Gen.SchDoc	Drawn By:

A

B

C

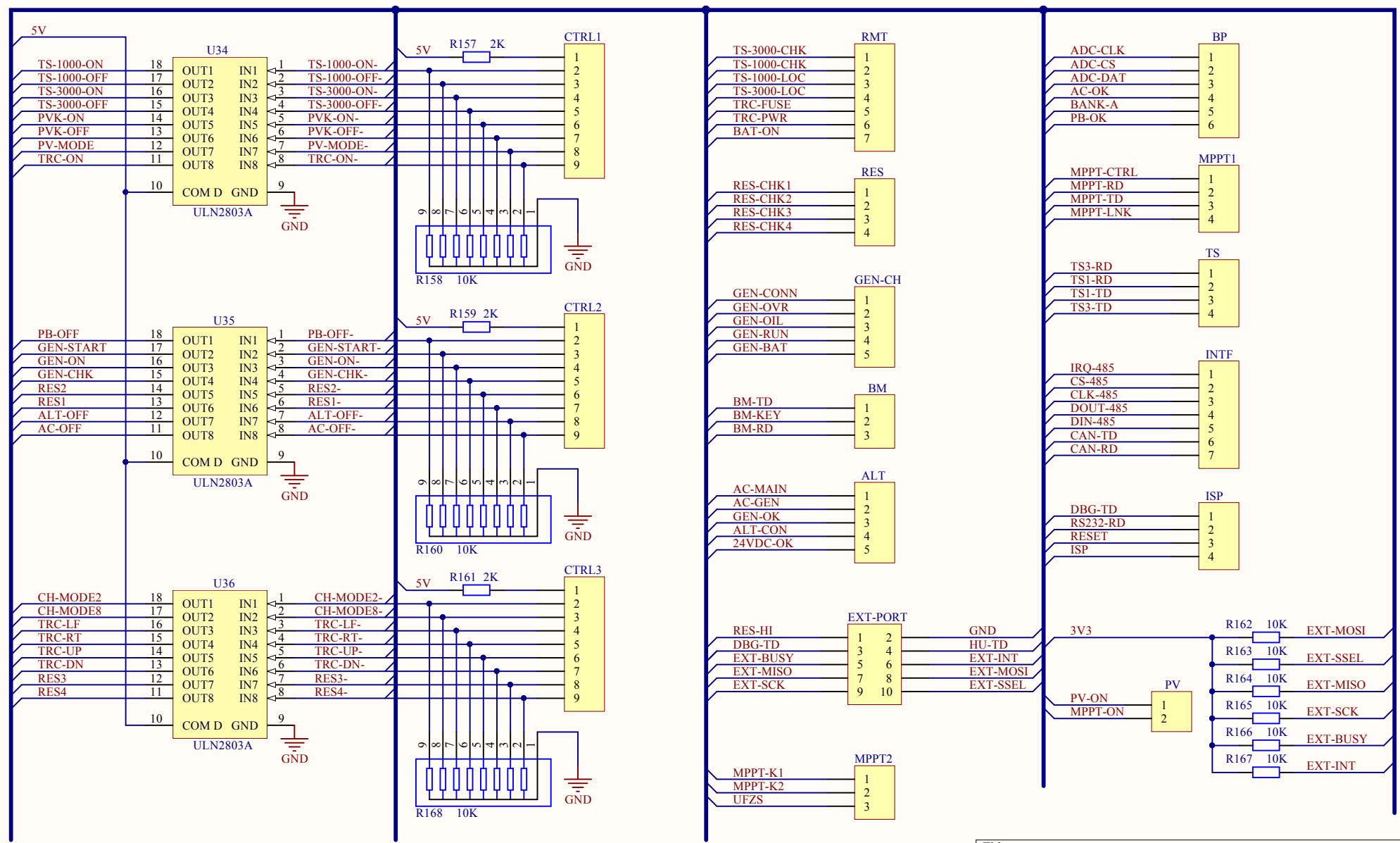
D

A

B

C

D



Title		
Драйверы управления реле, контрольные точки		
Size	Number	Revision
A4		
Date:	15.04.2019	Sheet of
File:	C:\EmbeddedProject\Drivers.SchDoc	Drawn By:



U43A

TS3-RD	U3-TXD	66	P0[0]/CAN_RD1/U3_TXD/I2C1_SDA/U0_TXD
TS3-TD	U3-RXD	67	P0[1]/CAN_TD1/U3_RXD/I2C1_SCL/U0_RXD
DBG-TD	U0-TXD	141	P0[2]/U0_TXD/U3_TXD
DBG-RD	U0-RXD	142	P0[3]/U0_RXD/U3_RXD
CAN-RD	CAN-RD2	116	P0[4]/I2S_RX_SCK/CAN_RD2/T2_CAP0/LCD_VD[0]
CAN-TD	CAN-TD2	115	P0[5]/I2S_RX_WS/CAN_TD2/T2_CAP1/LCD_VD[1]
CS-485	SSP1-SEL	113	P0[6]/I2S_RX_SDA/SSP1_SSEL/T2_MAT0/U1_RTS/LCD_VD[8]
CLK-485	SSP1-SCK	112	P0[7]/I2S_TX_SCK/SSP1_SCK/T2_MAT1/RTC_EV0/LCD_VD[9]
DOUT-485	SSP1-MISO	111	P0[8]/I2S_TX_WS/SSP1_MISO/T2_MAT2/RTC_EV1/LCD_VD[16]
DIN-485	SSP1-MOSI	109	P0[9]/I2S_TX_SDA/SSP1_MOSI/T2_MAT3/RTC_EV2/LCD_VD[17]
TS1-RD	U2-TXD	69	P0[10]/U2_TXD/I2C2_SDA/T3_MAT0
TS1-TD	U2-RXD	70	P0[11]/U2_RXD/I2C2_SCL/T3_MAT1
CPU-M2	P0.12	29	P0[12]/USB_PPWR2/SSP1_MISO/ADC0_IN[6]
CPU-M1	P0.13	32	P0[13]/USB_UP_LED2/SSP1_MOSI/ADC0_IN[7]
EXT-INT	P0.14	48	P0[14]/USB_HSTEN2/SSP1_SSEL/USB_CONNECT2
ADC-CLK	SSP0-SCK	89	P0[15]/U1_TXD/SSP0_SCK
ADC-CS	SSP0-SSEL	90	P0[16]/U1_RXD/SSP0_SSEL
ADC-DAT	SSP0-MISO	87	P0[17]/U1_CTS/SSP0_MISO
IRQ-485	P0.18	86	P0[18]/U1_DCD/SSP0_MOSI
SD-CLK	SD-CLK	85	P0[19]/U1_DSR/SD_CLK/I2C1_SDA
SD-CMD	SD-CMD	83	P0[20]/U1_DTR/UART1/SD_CMD/I2C1_SCL
SD-PWR	SD-PWR	82	P0[21]/U1_RI/SD_PWR/U4_OE/CAN_RD1/U4_SCLK
SD-DAT0	SD-DAT0	80	P0[22]/U1_RTS/SD_DAT[0]/U4_TXD/CAN_TD1
AC-MAIN	P0.23	13	P0[23]/ADC0_IN[0]/I2S_RX_SCK/T3_CAP0
GEN-OK	P0.24	11	P0[24]/ADC0_IN[1]/I2S_RX_WS/T3_CAP1
GEN-RUN	P0.25	10	P0[25]/ADC0_IN[2]/I2S_RX_SDA/U3_TXD
DAC	DAC-OUT	8	P0[26]/ADC0_IN[3]/DAC_OUT/U3_RXD
MPPT-K2	P0.27	35	P0[27]/I2C0_SDA/USB_SDA1
MPPT-K1	P0.28	34	P0[28]/I2C0_SCL/USB_SCL1
AC-OK	P0.29	42	P0[29]/USB_D+/EINT0
BANK-A	P0.30	43	P0[30]/USB_D-/EINT1
BM-KEY	P0.31	36	P0[31]/USB_D+2
EXT-SCK	SSP2-SCK	136	P1[0]/ENET_TXD0/T3_CAP1/SSP2_SCK
EXT-MOSI	SSP2-MOSI	135	P1[1]/ENET_TXD1/T3_MAT3/SSP2_MOSI
EXT-MISO	SSP2-MISO	133	P1[4]/ENET_TX_EN/T3_MAT2/SSP2_MISO
EXT-SSEL	SSP2-SSEL	132	P1[8]/ENET_CRS(ENET_CRS_DV)/T3_MAT1/SSP2_SSEL
GEN-ON-	P1.9	131	P1[9]/ENET_RXD0/T3_MAT0
GEN-CHK-	P1.10	129	P1[10]/ENET_RXD1/T3_CAP0
RES2-	P1.14	128	P1[14]/ENET_RX_ER/T2_CAP0
RES1-	P1.15	126	P1[15]/ENET_RX_CLK(ENET_REF_CLK)/I2C2_SDA
ALT-OFF-	P1.16	125	P1[16]/ENET_MDC/I2S_TX_MCLK
AC-OFF-	P1.17	123	P1[17]/ENET_MDIO/I2S_RX_MCLK
TS-1000-ON-	P1.18	46	P1[18]/USB_UP_LED1/PWM1[1]/T1_CAP0/SSP1_MISO
TS-1000-OFF-	P1.19	47	P1[19]/USB_TX_EI/USB_PPWR1/T1_CAP1/MC_0A/SSP1_SCK/U2_OE
TS-3000-ON-	P1.20	49	P1[20]/USB_TX_DP1/PWM1[2]/QE1_PHA/MC_FB0/SSP0_SCK/LCD_VD[6]/LCD_VD[10]
TS-3000-OFF-	P1.21	50	P1[21]/USB_TX_DM1/PWM1[3]/SSP0_SSEL/MC_ABORT/LCD_VD[7]/LCD_VD[11]
PVK-ON-	P1.22	51	P1[22]/USB_RCV1/USB_PWRD1/T1_MAT0/MC_0B/SSP1_MOSI/LCD_VD[8]/LCD_VD[12]
PV-MODE-	P1.23	53	P1[23]/USB_RX_DP1/PWM1[4]/QE1_PHB/MC_FB1/SSP0_MISO/LCD_VD[9]/LCD_VD[13]
TRC-ON-	P1.24	54	P1[24]/USB_RX_DM1/PWM1[5]/QE1_IDX/MC_FB2/SSP0_MOSI/LCD_VD[10]/LCD_VD[14]
RES3-	P1.25	56	P1[25]/USB_LSI/USB_HSTEN1/T1_MAT1/MC_1A/CLKOUT/LCD_VD[11]/LCD_VD[15]
TRC-DN-	P1.26	57	P1[26]/USB_SSPND1/PWM1[6]/T0_CAP0/MC_1B/SSP1_SSEL/LCD_VD[12]/LCD_VD[20]
TRC-UP-	P1.27	61	P1[27]/USB_INT1/USB_OVRCR1/T0_CAP1/CLKOUT/LCD_VD[13]/LCD_VD[21]
TRC-RT-	P1.28	63	P1[28]/USB_SCL1/PWM1_CAP0/T0_MAT0/MC_2A/SSP0_SSEL/LCD_VD[14]/LCD_VD[22]
TRC-LF-	P1.29	64	P1[29]/USB_SDA1/PWM1_CAP1/T0_MAT1/MC_2B/U4_TXD/LCD_VD[15]/LCD_VD[23]
PB-OFF-	P1.30	30	P1[30]/USB_PWRD2/USB_VBUS/ADC0_IN[4]/I2C0_SDA/U3_OE
GEN-START-	P1.31	28	P1[31]/USB_OVRCR2/SSP1_SCK/ADC0_IN[5]/I2C0_SCL

LPC1788FBD144

Title		
Контроллер		
Size	Number	Revision
A4		
Date:	15.04.2019	Sheet of
File:	C:\EmbeddedProject\...\CPU1.SchDoc	Drawn By:

A

A

B

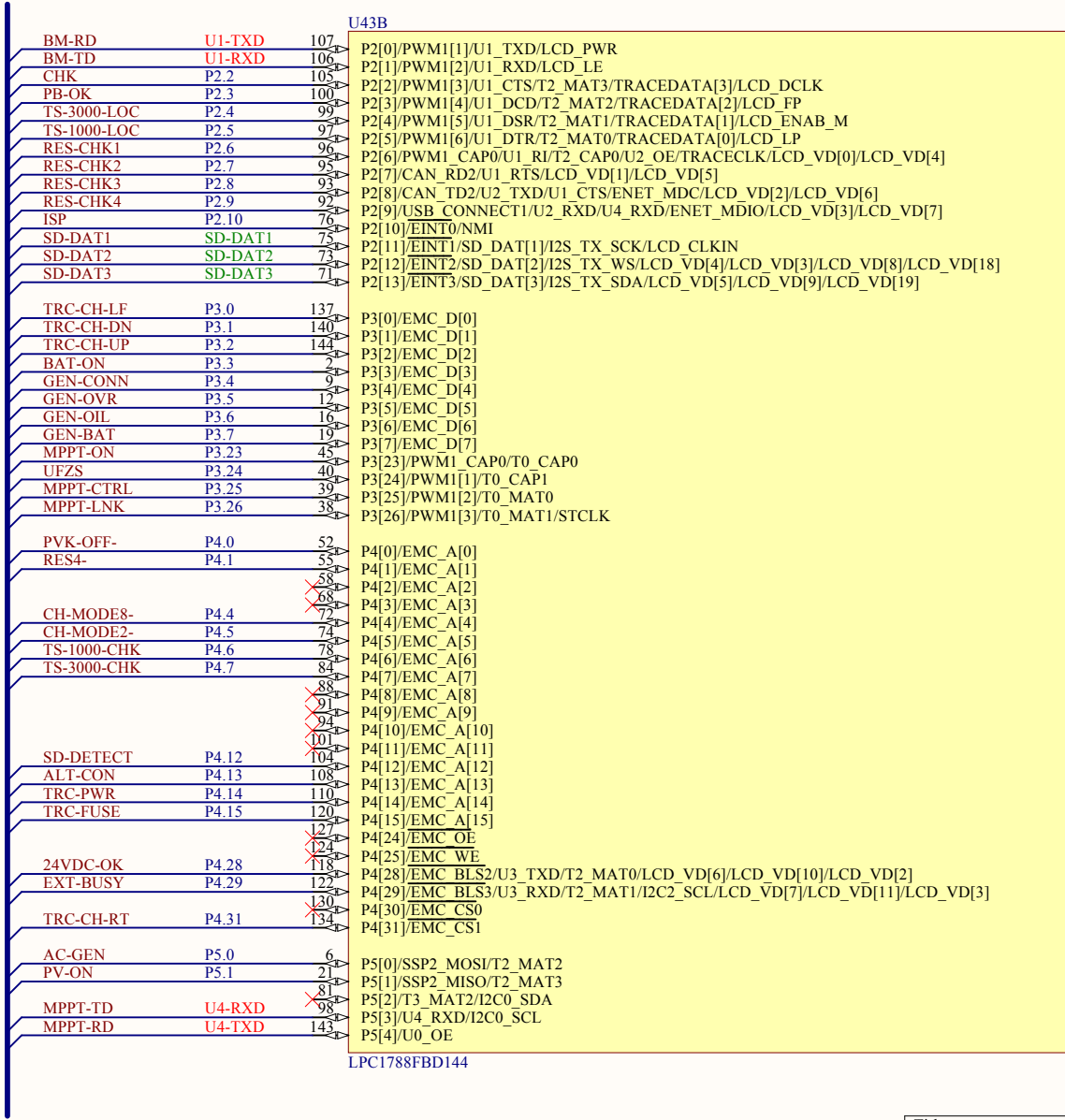
B

C

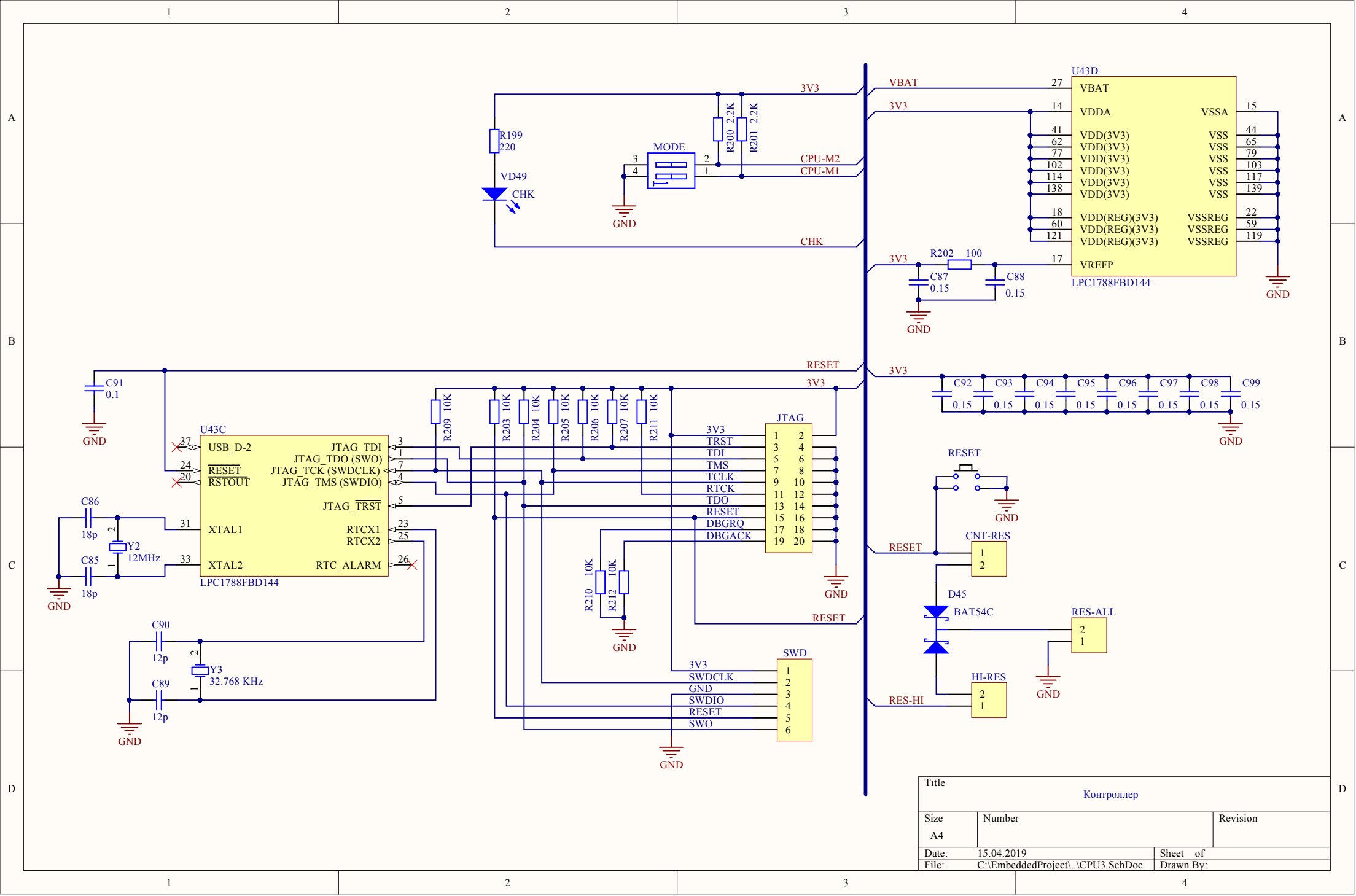
C

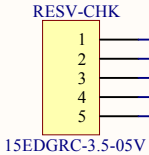
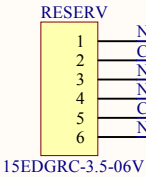
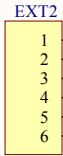
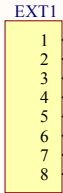
D

D



Title		
Контроллер		
Size	Number	Revision
A4		
Date:	15.04.2019	Sheet of
File:	C:\EmbeddedProject\...\CPU2.SchDoc	Drawn By:





Title		
Резервные входы/выходы		
Size	Number	Revision
A4		
Date:	15.04.2019	Sheet of
File:	C:\EmbeddedProject\Reserv.SchDoc	Drawn By: