

TIM6\_DAC\_IRQHandler



```
graph LR; A[TIM6_DAC_IRQHandler] --> B[tasks_1ms]; B --> C[enable_inverters];
```

The diagram illustrates a sequence of three components connected by arrows. The first component, 'TIM6\_DAC\_IRQHandler', is in a white box. An arrow points to the second component, 'tasks\_1ms', also in a white box. A second arrow points from 'tasks\_1ms' to the third component, 'enable\_inverters', which is in a gray box.

tasks\_1ms

enable\_inverters