

C:/Users/dwegg/Desktop
/Inverter/SW/Inverter
/Core/Inc/dma.h

```
graph BT; A["C:/Users/dwegg/Desktop<br>/Inverter/SW/Inverter<br>/Core/Inc/dma.h"] <--> B["C:/Users/dwegg/Desktop<br>/Inverter/SW/Inverter<br>/Core/Src/dma.c"]; A <--> C["C:/Users/dwegg/Desktop<br>/Inverter/SW/Inverter<br>/Core/Src/main.c"];
```

C:/Users/dwegg/Desktop
/Inverter/SW/Inverter
/Core/Src/dma.c

C:/Users/dwegg/Desktop
/Inverter/SW/Inverter
/Core/Src/main.c