

C:/Users/dwegg/Desktop
/Inverter/SW/Inverter
/Core/Src/PCB_IO.c

PCB_IO.h

main.h

stm32f7xx_hal.h

```
graph TD; A["C:/Users/dwegg/Desktop /Inverter/SW/Inverter /Core/Src/PCB_IO.c"] --> B["PCB_IO.h"]; A --> C["main.h"]; B --> D["stm32f7xx_hal.h"]; C --> D;
```