

C:/Users/dwegg/Desktop
/Inverter/SW/Inverter
/Core/Src/dma.c

```
graph TD; A["C:/Users/dwegg/Desktop<br/>/Inverter/SW/Inverter<br/>/Core/Src/dma.c"] --> B["dma.h"]; B --> C["main.h"]; C --> D["stm32f7xx_hal.h"];
```

A vertical flowchart with four rectangular boxes connected by blue arrows pointing downwards. The top box is dark gray and contains a file path. The second and third boxes are white with black borders and contain header file names. The bottom box is light gray and contains another header file name.

dma.h

main.h

stm32f7xx_hal.h