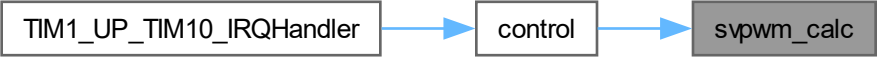


TIM1_UP_TIM10_IRQHandler



```
graph LR; A[TIM1_UP_TIM10_IRQHandler] --> B[control]; B --> C[svpwm_calc];
```

The diagram illustrates a sequential flow of three components. The first component, 'TIM1_UP_TIM10_IRQHandler', is a white box with a black border. A blue arrow points from its right side to the second component, 'control', which is also a white box with a black border. Another blue arrow points from the right side of the 'control' box to the third component, 'svpwm_calc', which is a gray box with a black border.

control

svpwm_calc