

C:/Users/dwegg/Desktop
/Inverter/SW/Inverter
/Core/Inc/usb_otg.h

```
graph BT; A["C:/Users/dwegg/Desktop/Inverter/SW/Inverter/Core/Inc/usb_otg.h"] <--> B["C:/Users/dwegg/Desktop/Inverter/SW/Inverter/Core/Src/main.c"]; A <--> C["C:/Users/dwegg/Desktop/Inverter/SW/Inverter/Core/Src/usb_otg.c"];
```

C:/Users/dwegg/Desktop
/Inverter/SW/Inverter
/Core/Src/main.c

C:/Users/dwegg/Desktop
/Inverter/SW/Inverter
/Core/Src/usb_otg.c