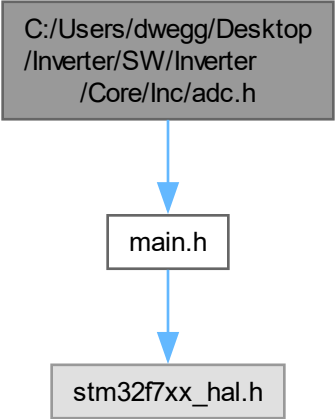


C:/Users/dwegg/Desktop  
/Inverter/SW/Inverter  
/Core/Inc/adc.h



```
graph TD; A["C:/Users/dwegg/Desktop<br>/Inverter/SW/Inverter<br>/Core/Inc/adc.h"] --> B["main.h"]; B --> C["stm32f7xx_hal.h"]
```

main.h

stm32f7xx\_hal.h