

C:/Users/dwegg/Desktop
/Inverter/SW/Inverter
/Core/Src/TASKS_1ms.c

```
graph TD; A["C:/Users/dwegg/Desktop<br>/Inverter/SW/Inverter<br>/Core/Src/TASKS_1ms.c"] --> B["TASKS_1ms.h"]; A --> C["PCB_IO.h"]; C --> D["stm32f7xx_hal.h"];
```

TASKS_1ms.h

PCB_IO.h

stm32f7xx_hal.h