

C:/Users/dwegg/Desktop  
/Inverter/SW/Inverter  
/Core/Inc/i2c.h

```
graph BT; A["C:/Users/dwegg/Desktop<br/>/Inverter/SW/Inverter<br/>/Core/Inc/i2c.h"] --> B["C:/Users/dwegg/Desktop<br/>/Inverter/SW/Inverter<br/>/Core/Src/i2c.c"]; A --> C["C:/Users/dwegg/Desktop<br/>/Inverter/SW/Inverter<br/>/Core/Src/main.c"];
```

C:/Users/dwegg/Desktop  
/Inverter/SW/Inverter  
/Core/Src/i2c.c

C:/Users/dwegg/Desktop  
/Inverter/SW/Inverter  
/Core/Src/main.c