

Hu WAN

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RESEARCH

My research interests are in building reliable and high performance non-volatile memory systems.
My current research focuses on designing efficient transactions for persistent memories.

EDUCATION

Tsinghua University Department of Computer Science and Technology Supervisor: Prof. Jiwu Shu, R.A. Youyou Lu	Jul. 2015 - Present Visiting Student
Capital Normal University College of Information Engineering Advisor: Asst Prof. Yuanchao Xu	Sep. 2014 - Present M.S. Student
Institute of Computing Technology, Chinese Academy of Sciences State Key Laboratory of Computer Architecture Mentor: Prof. Dongrui Fan	Sep. 2013 - Aug. 2014 R & D Intern
Capital Normal University College of Information Engineering Outstanding graduate of Capital Normal University.	Sep. 2009 - Jul. 2013 Bachelor of Engineering

PUBLICATIONS

- **Efficient Storage Management for Aged File Systems on Persistent Memory.**
Kaisheng Zeng, Youyou Lu, **Hu Wan**, Jiwu Shu
To appear in *Design, Automation and Test in Europe (DATE)*, 2017.
- **i-BEP: a Non-Redundant High Concurrency Memory Persistency Model.**
Yuanchao Xu, Zeyi Hou, Junfeng Yan, Lu Yang and **Hu Wan**
To appear in *Design, Automation and Test in Europe (DATE)*, 2017.
- **Empirical Study of Redo and Undo Logging in Persistent Memory.**
Hu Wan, Youyou Lu, Yuanchao Xu, Jiwu Shu
In the 5th *Non-Volatile Memory Systems and Applications Symposium (NVMSA)*, August, 2016.
- **Reducing Synchronization Cost for Single-Level Store in Mobile Systems.**
Yuanchao Xu, **Hu Wan**, Keni Qiu, Tao Li, Weigong Zhang
Journal of Computer Science and Technology, 2016, 31(4): 836-848.
- **A Survey of Security and Privacy on Emerging Non-volatile Memory.**
Yuanchao Xu, Junfeng Yan, **Hu Wan**, Weigong Zhang
Journal of Computer Research and Development, 2016, 53(9): 1930-1942 (in Chinese).
- **Mitigating sync overhead in single-level store systems.**
Yuanchao Xu, **Hu Wan**, Zeyi Hou, Keni Qiu
In *Proceedings of the ACM International Conference on Computing Frontiers (CF)*, May, 2016.
- **Mitigating Log Cost through Non-Volatile Memory and Checkpoint Optimization.**
Hu Wan, Yuanchao Xu, Junfeng Yan, Fengyun Sun, Weigong Zhang
Journal of Computer Research and Development, 2015, 52(6): 1351-1361 (in Chinese).

HONORS AND AWARDS

- **First-class Academic Scholarship for Postgraduates** (Capital Normal University) - November 2016
- **National Scholarship for Postgraduates** (Ministry of Education of China) - December 2015
- **Merit Student** (Capital Normal University) - May 2016, May 2015
- **Scholarship for Students of Chinese Academy of Sciences** (Chinese Academy of Sciences) - January 2014
- **Outstanding Graduate** (Capital Normal University) - June 2013
- **National Scholarship for Encouragement** (Ministry of Education of China) - 2012, 2011, 2010
- **The Excellence Award of Beijing College Students Research and Career-creation Program** (Beijing Municipal Education Commission) - April 2012
- **The 3rd prize of the 6th National Information Technology Application Level Competition** (Ministry of Education of China) - December 2011
- **The 2nd prize of the 2nd Beijing College Students Computer Application Contest** (Beijing Municipal Education Commission) - October 2011