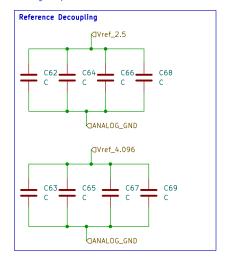
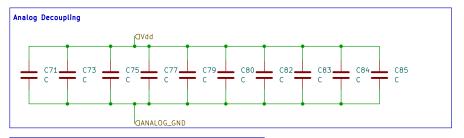
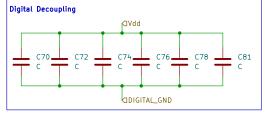


This is a placeholder for a CPU module. Could be a Raspberry Pi or ESP32 or another microcontroller module with (TBD) GPIO lines, at least one SPI bus with (3) CS lines, and one 12C Bus. □ADC\_BATT+ □ADC\_BATT− □ADC\_TEMP ♦12C\_SDA D12C\_SCL ⟨ISPI\_MISO | DSPI\_MOSI DSPI\_SCLK DSPI\_CS0 DSPI\_CS1 DSPI\_CS2 □+5.0V □+3.3V ♦GND\_CB DEN/SH\_0 DEN/SH\_1 DEN/SH\_2 Sheet: /Micocontroller/ File: microcontroller.kicad\_sch Title: Size: A4 Date: Rev: KiCad E.D.A. 8.0.9 ld: 6/8

J21 Some sensors will need to come with their +5٧ SLOT 1 own digital breakout boards (DO, CO2, EC, etc.)
To support these, this board provides a common interface to the CPU and signal conditioning to those units. EN\_SH \_SLOT\_ADDR\_0 \_SLOT\_ADDR\_1 \_SLOT\_ADDR\_2 SPI\_MISO SPI\_MOSI CALIBRATE SPI\_SCLK SPI\_CS I2C\_SCL I2C\_SDA Sheet: /Digital Sensor Module 4/ File: digital\_sensor\_4.kicad\_sch Title: Size: A4 Date: Rev: KiCad E.D.A. 8.0.9 ld: 7/8 Decoupling Capacitors are connected as close as possible to each IC of the given power domain.







Sheet: /Sensor Module 8/Sensor 8 decoupling/ File: sensor\_8\_decoupling.kicad\_sch

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 Size: A4
 Date:
 Rev:

 KiCad E.D.A. 8.0.9
 Id: 8/8