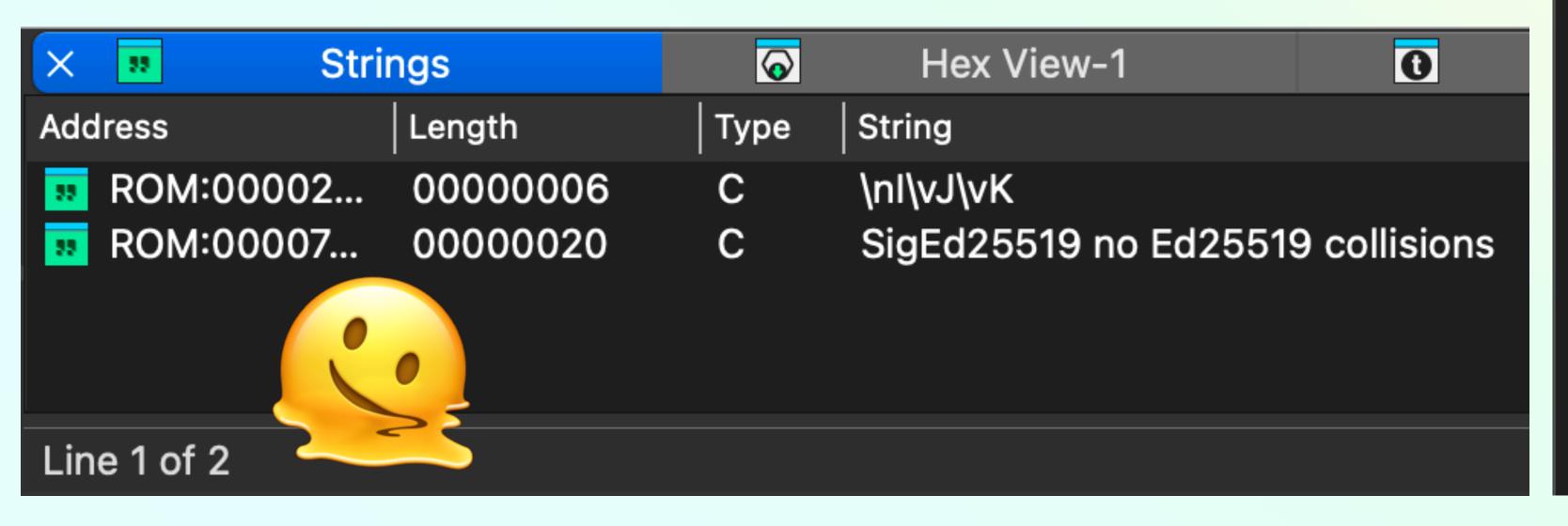
# ANALYSIS CHALLENGES

### Dense code

- No strings
- No symbols
- No debug statements/printf-like functions
- No external libraries





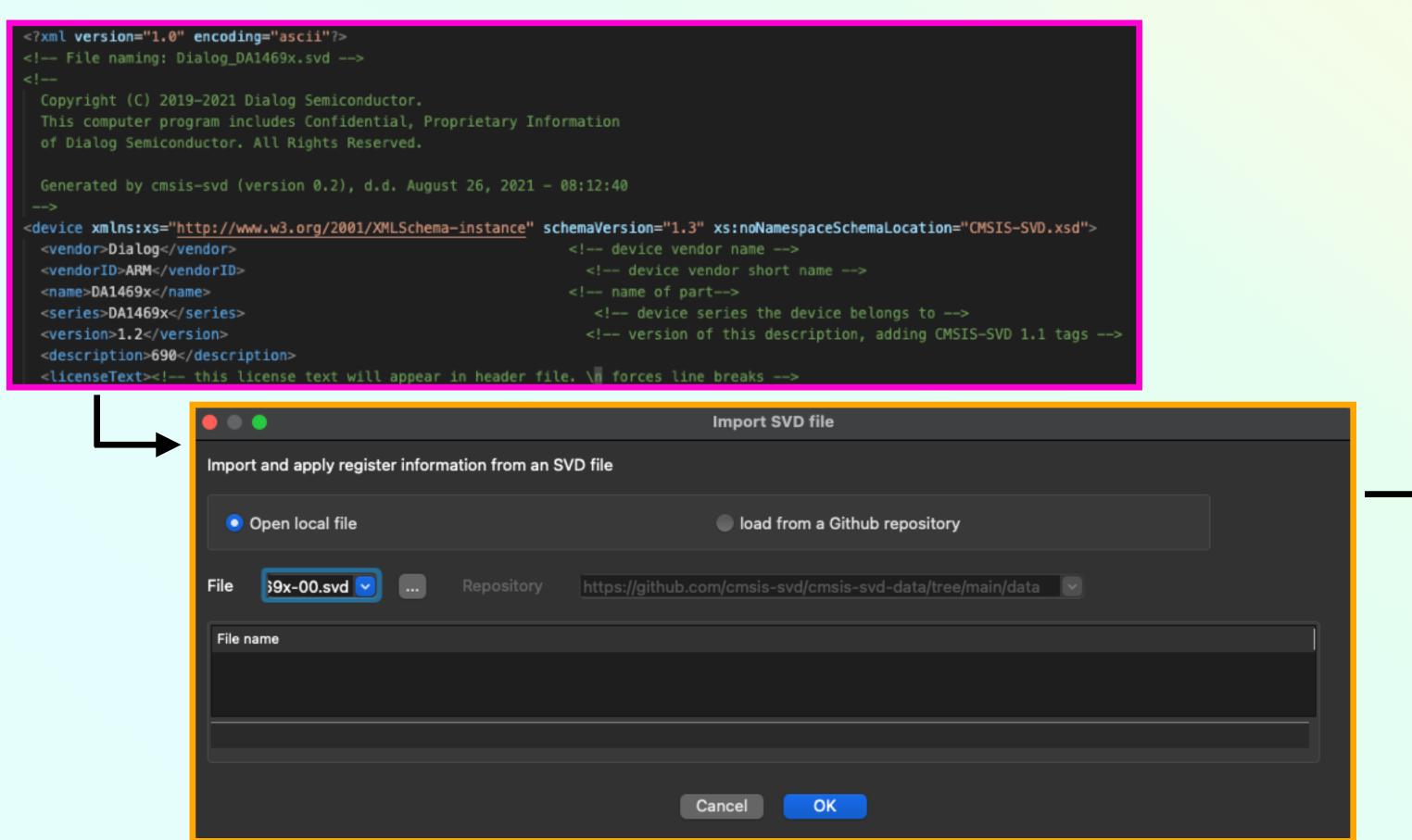
Fun	nction name	Start	<b>\</b>
f	sub_100	00000100	
f	sub_1E0	000001E0	
f	sub_228	00000228	
f	sub_32C	0000032C	
f	sub_344	00000344	
f	sub_384	00000384	
f	sub_3C4	000003C4	
f	sub_3E0	000003E0	
f	sub_3F4	000003F4	
	sub_418	00000418	
f	sub_4D0	000004D0	

# LETTHEHWGUIDEYOU

#### **Datasheet Provides Addresses**

#### **IDA Segments**

- Create manually/IDAPython
- SVD Loader (Edit>Plugins>SVD File Management)
  - SDK\_10.0.12.146.1/config/embsys/Dialog\_Semiconductor/DA1469x-00.xml



Resource	Start Address	End Address	Size (kB)	
WDOG	50000700	50000800	0,25	
Reserved				
XTAL32M_C	50010000	50010200	0,5	
TIMER	50010200	50010300	0,25	
TIMER2	50010300	50010400	0,25	
MAC_TIM	50010400	50010500	0,25	
Reserved				
UART	50020000	50020100	0,25	
UAR2	50020100	50020200	0,25	
UART3	50020200	50020300	0,25	
SPI	50020300	50020400	0,25	
SPI2	50020400	50020500	0,25	

