```
J-Link>setbp 0x1140 <--- function exit
Breakpoint set @ addr 0x00001140 (Handle = 1)
```

```
PC = 00001140, CycleCnt = 02ACD568
R0 = 00000031, R1 = 00000002, R2 = 00000010, R3 = 38000000
R4 = 00000000, R5 = 00000000, R6 = 00000000, R7 = 2003FFE8
R8 = 9220C050, R9 = D08C106E, R10= 20030000, R11= 00000000
R12= ABA08801
SP(R13)= 2003FFE8, MSP= 2003FFE8, PSP= 00000000, R14(LR) = 0000113B
J-Link>mem32 2003FFE8,0x20
2003FFE8 = 00000066 2004000B 24070001 24242424
2003FFF8 = BF00BF00 BF00BF00 BF00BF00 BF00BF00
20040008 = E7FEBF00 A0F10400 33338047 33333131
20040018 = 33333131 33333131 33333131 33333131
20040028 = 33333131 33333131 33333131 33333131
20040038 = 33333131 33333131 33333131 33333131
20040048 = 33333131 33333131 33333131 33333131
20040058 = 33333131 33333131 33333131 33333131
J-Link>s
00001140:   80 BD             POP       {R7,PC}
```

```
J-Link>regs
PC = 2004000A, CycleCnt = 02ACD570
R0 = 00000031, R1 = 00000002, R2 = 00000010, R3 = 38000000
R4 = 00000000, R5 = 00000000, R6 = 00000000, R7 = 00000066
R8 = 9220C050, R9 = D08C106E, R10= 20030000, R11= 00000000
R12= ABA08801
SP(R13)= 2003FFF0, MSP= 2003FFF0, PSP= 00000000, R14(LR) =
J-Link>s
2004000A:   FE E7             B         #-0x04
J-Link>s
2004000A:   FE E7             B         #-0x04
```

```
    QSPI_Read_Product_Headers((struct_configuration_script_ptr *)&configuration_script, product_img_offsets);
  }
  while ( BYTE1(configuration_script) != 1 );
  WDOG_feed_ff();
  if ( check_current_fw_addr_and_update_addr(product_img_offsets) )// differing offsets mean we have an update, enter
  {
    if ( !SECUREBOOT_check_and_validate(
            product_img_offsets[1],
            (FW_ImageHeader *)&dword_2003C950,
            (int)product_img_offsets,
            (struct_configuration_script_ptr *)&configuration_script) )// / returns 1 if secure boot is not enabled
    {
      QSPI_process_product_update((struct_configuration_script_ptr *)&configuration_script, product_img_offsets);// re
      WDOG_Pet_1c34();
    }
  }                                             // no update path
                                                // this returns 0 from [8] offset check
                                                //   if ( !(*qspi_data_value_ptr)[8] )
                                                //     return 0;
  else if ( !SECUREBOOT_check_and_validate(
              product_img_offsets[0],
              (FW_ImageHeader *)&dword_2003C950,
              (int)product_img_offsets,
              (struct_configuration_script_ptr *)&configuration_script) )// returns 1 if secure boot is not enabled
  {
    WDOG_Pet_1c34();                            // we hit this
  }
  SECUREBOOT_CHECK_key_revocation(dword_2003C950);
  sub_A6E((struct_configuration_script_ptr *)&configuration_script, product_img_offsets);
  WDOG_feed_ff();
  if ( !SECUREBOOT_CHECK_setup_QSPIC_CTR_195C(
          (struct_configuration_script_ptr *)&configuration_script,
          product_img_offsets) )
    WDOG_Pet_1c34();
  OTPC_standby();
  Cache_setup_qspi_cache(dword_2003C950, product_img_offsets);
  write_to_sysram(dword_2003C950, product_img_offsets);
  return RESET_to_REMAP_ADR_val(2u);            // SW Reset to QSPI Flash
}
```