

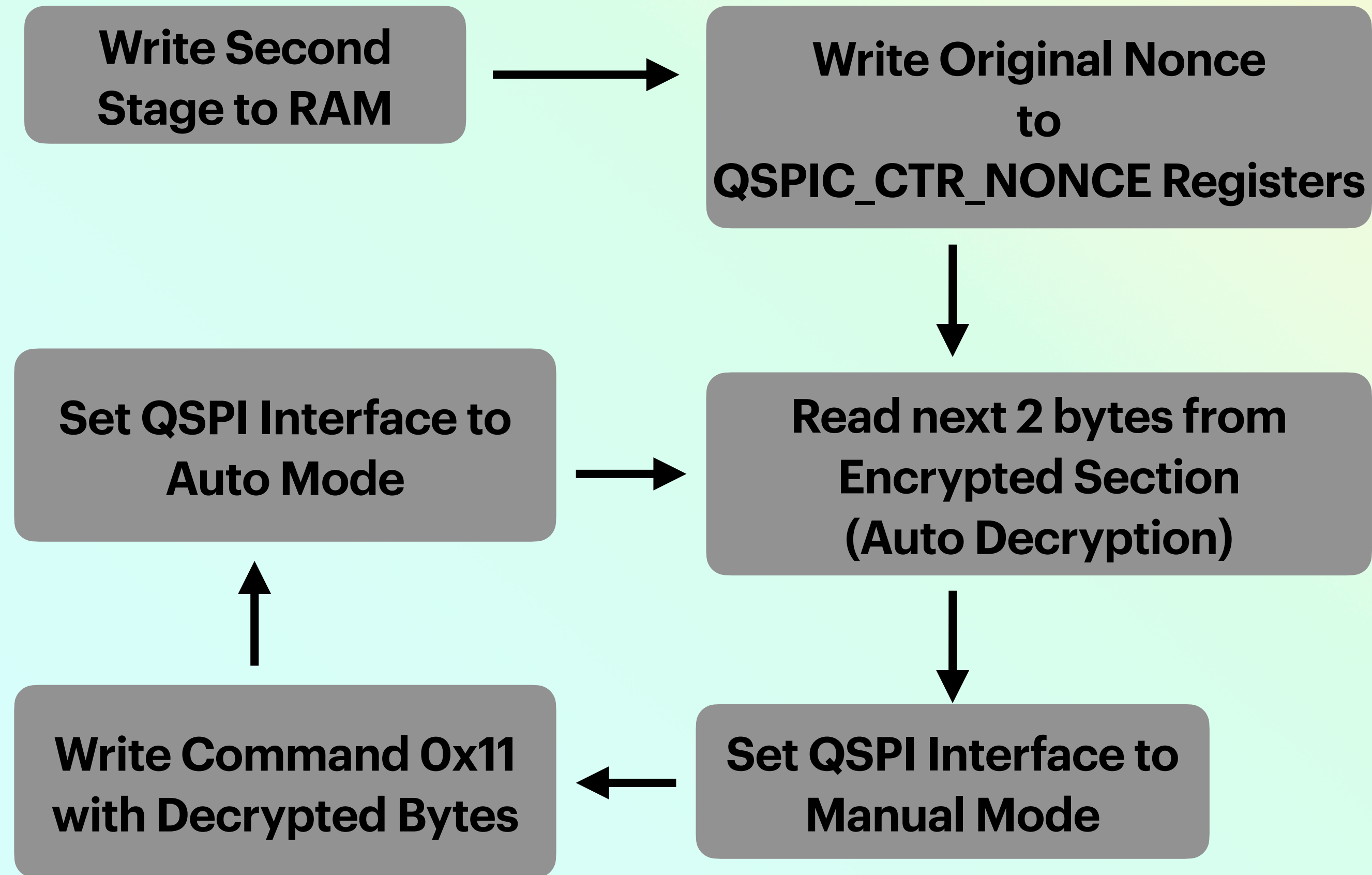
ALMOST THERE

SPI only does decryption in 'Auto' mode, which only allows reading.

'Manual' mode allows writing to SPI, but cannot read decrypted and payload fails to continue.

- Auto Mode: up-to 32 Mbyte transparent Code access for XIP (Execute-In-Place) and Data access with 3-byte and 4-byte addressing modes
 - Manual Mode: Direct register access using the QSPIC register file
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- Decrypt on-the-fly (AES-256b-CTR) capability while in auto mode operation

THE OWL



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C2 F2 03 02    movt  r2, #0x2003
10 46          mov   r0, r2
4F F0 4F 01    mov.w r1, #0x4f
00 F8 01 1B    strb  r1, [r0], #1
4F F0 F0 01    mov.w r1, #0xf0
00 F8 01 1B    strb  r1, [r0], #1
4F F0 60 01    mov.w r1, #0x60
00 F8 01 1B    strb  r1, [r0], #1
4F F0 50 01    mov.w r1, #0x50
00 F8 01 1B    strb  r1, [r0], #1
4F F0 00 01    mov.w r1, #0
00 F8 01 1B    strb  r1, [r0], #1
4F F0 F1 01    mov.w r1, #0xf1
00 F8 01 1B    strb  r1, [r0], #1
4F F0 8C 01    mov.w r1, #0x8c
00 F8 01 1B    strb  r1, [r0], #1
4F F0 00 01    mov.w r1, #0
00 F8 01 1B    strb  r1, [r0], #1
4F F0 42 01    mov.w r1, #0x42
00 F8 01 1B    strb  r1, [r0], #1
4F F0 F2 01    mov.w r1, #0xf2
00 F8 01 1B    strb  r1, [r0], #1
4F F0 0D 01    mov.w r1, #0xd
00 F8 01 1B    strb  r1, [r0], #1
4F F0 61 01    mov.w r1, #0x61
00 F8 01 1B    strb  r1, [r0], #1
4F F0 CF 01    mov.w r1, #0xcf
00 F8 01 1B    strb  r1, [r0], #1
4F F0 F2 01    mov.w r1, #0xf2
00 F8 01 1B    strb  r1, [r0], #1
4F F0 02 01    mov.w r1, #2
00 F8 01 1B    strb  r1, [r0], #1
```