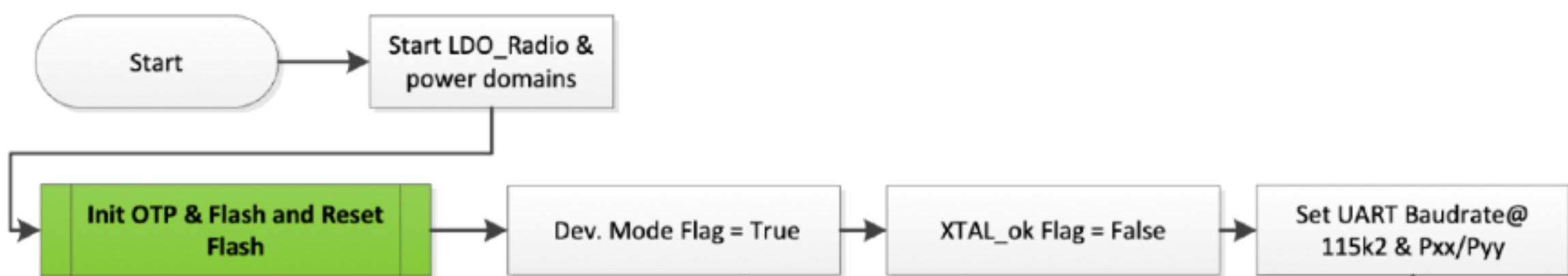


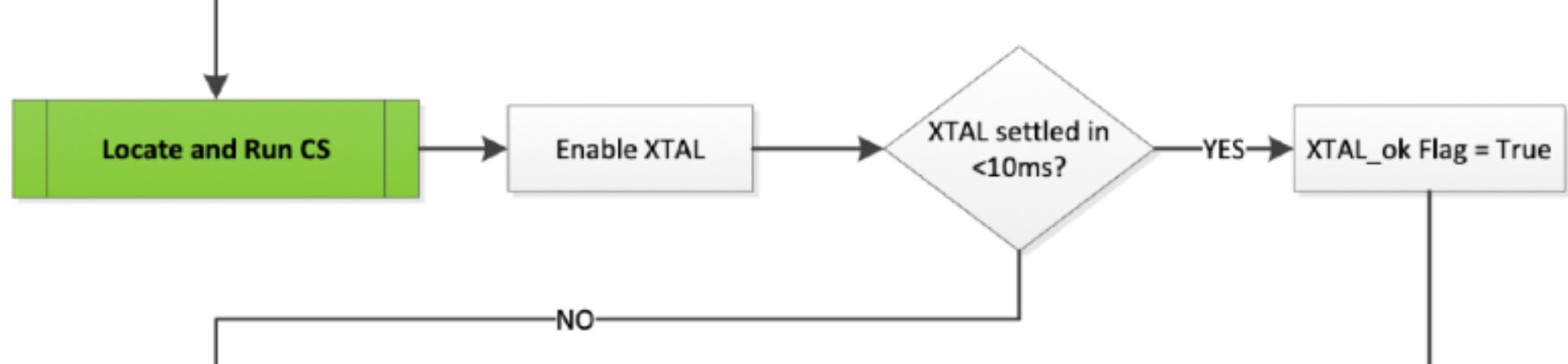
NATIONALLY
SYNTHETIC

bootrom

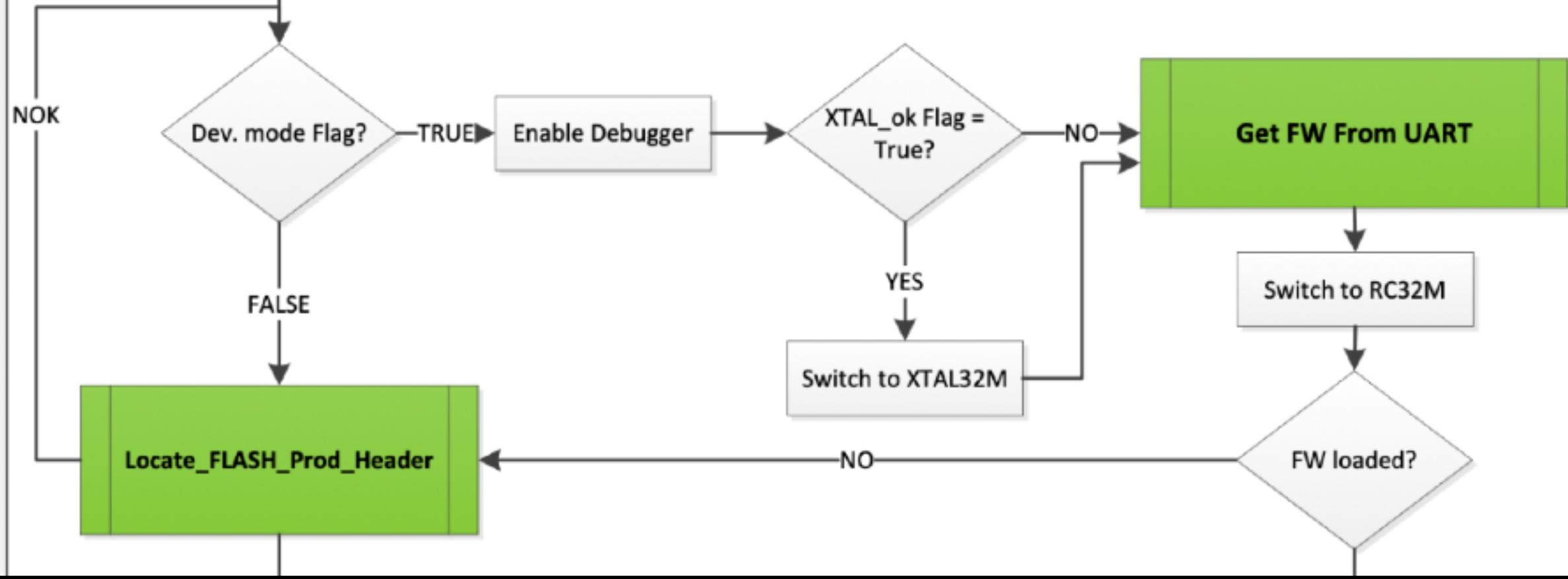
Initialization



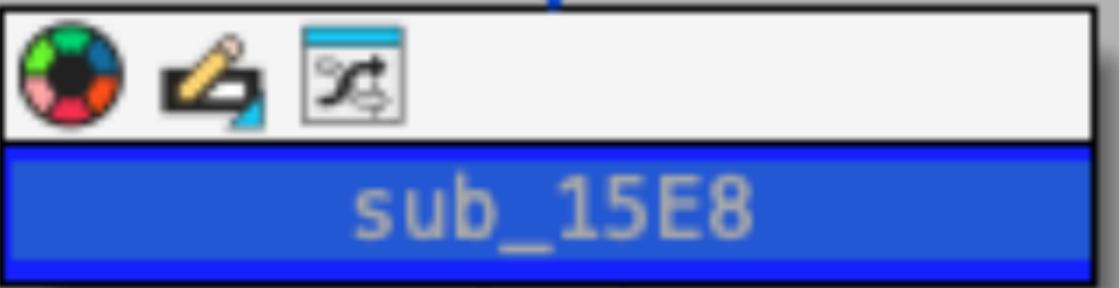
Run Configuration Script



Execution Code



RENT and POST
2021.04.29x3v3.pdf







```
39 sub_1544(v0);
40 v3 = CRG_TOP_PMU_CTRL_REG;
41 CRG_TOP_PMU_CTRL_REG = v3 & 0xFFFFFFFFFB;
42 do
43     v4 = CRG_TOP_SYS_STAT_REG;
44     while ( (v4 & 0x200) == 0 );
45     v5 = CRG_TOP_POWER_CTRL_REG;
46     CRG_TOP_POWER_CTRL_REG = v5 & 0xFF8FFFFF | 0x400000;
47     CRG_TOP_POWER_CTRL_REG = v5 & 0xFF8FFF7F | 0x400080;
48     v6 = CRG_TOP_PMU_CTRL_REG;
49     CRG_TOP_PMU_CTRL_REG = v6 & 0xFFFFFFF8;
50     do
51         v7 = CRG_TOP_SYS_STAT_REG;
52         while ( (v7 & 8) == 0 );
53         v8 = sub_1CAC(1);
54         v9 = sub_1DAC(v8);
55         v10 = sub_2052(v9);
56         sub_20B6(v10);
57         byte_2003C954 = 1;
58         dword_2003C958 = 0;
59         v11 = CRG_COM_CLK_COM_REG;
60         CRG_COM_CLK_COM_REG = v11 | 1;
61         v12 = UART_reset_and_configure_uart(0x1106); |
62         GPIO_P0_09_MODE_REG = 2;
63         GPIO_P0_08_MODE_REG = 1;
64         sub_1544(v12);
65         sub_228(&byte_2003C954);
66         v13 = CRG_TOP_CLK_CTRL_REG;
67         if ( (v13 & 0x4000) == 0 )
68         {
69             v14 = CRG_XTAL_XTAL32M_CTRL1_REG;
70             CRG_XTAL_XTAL32M_CTRL1_REG = v14 | 0x800000;
71 }
```



NAVIGATION SYSTEM

