Vault Indexing Model (v4.2)

Overview:

The Vault Indexing Model (v4.2) represents the most recent schematic of Linwood's internal recall partitioning architecture. The model illustrates how memory segments are distributed, tagged, and prioritized during induced recall cycles within the RMI (Recall-Modulated Indexing) vault framework.

Partitioning Framework:

- Segments are categorized as either Core, Peripheral, or Redundant.
- Core segments are indexed with Priority Markers (PM) between P1 and P3, depending on experimental relevance.
- Peripheral memory threads are often suppressed during Phase I reinstatement and reintroduced gradually if behavioral compliance remains stable.
- Redundant blocks serve as data ballast and may contain loopable emotional echoes or fabricated inserts.

Neurometric Diagram (see attached schematic):

The radial layout displays the nodal bridge between pre-vault episodic memory and post-reinstatement behavioral response. Neurometric indicators embedded in the diagram should be interpreted with caution, as many values were fabricated or algorithmically altered for proprietary misdirection.

Warnings:

- Indexing does not correlate linearly with subject behavior.

- Redundant segments have shown emergent re-prioritization in unsupervised loops.

- PM drift may occur under high emotional load or after recursive cycle failure.

This version of the vault map is archived for training and misclassification analysis only. Future versions (v5.x and above) will integrate live trace overlays and adaptive indexing strategies.

Distribution:

Internal Use Only - Do Not Circulate

Authorized by: Linwood Neural Systems Division