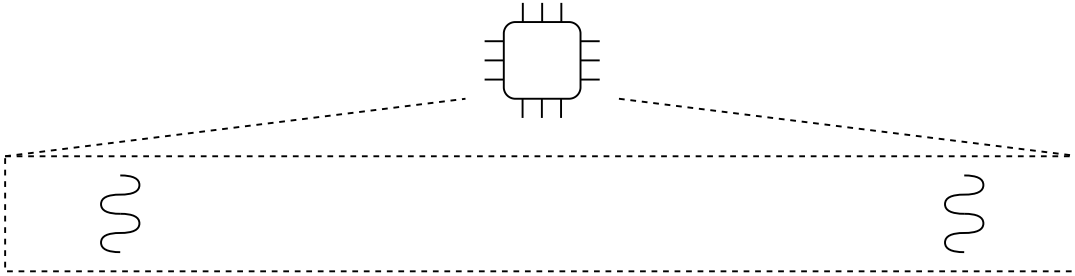

Target thread	Microarchitectural state	Memory state	Attacker thread
Enter a syscall			
① Read(X) = 42 Snapshot X		X=42	
			② Speculation start
	④ LSQ		③ Illegal Write(X, 0)
⑤ Read(X) = 0 from LSQ			
⑥ Use X = 0 Leak secret into side-channel			
	Side-channel e.g. cache		⑦ Speculation end
⑨ Roll back read			⑧ Roll back write
			⑩ Leak secret from side-channel