

## References

- [1] HENNESSY, J. L., AND PATTERSON, D. A. *Computer Architecture, Sixth Edition: A Quantitative Approach*, 6th ed. Morgan Kaufmann Publishers Inc., San Francisco, CA, USA, 2017.

## 1 Hypothesis

Increasing the number of reservation stations and the commit width will increase IPC. By increasing the instruction-level parallelism (ILP), this reduces data hazards and control hazard stalls, and therefore improves performance [1].

## 2 Experiment Design

Benchmark	ROB: commit_width	Reservation Station Entries
splash2-ocean.cont	36	32
splash2-radix		64
splash2-barnes		128
npb-is		512
npb-ep		1024
npb-mg		
npb-ua		

Table 1: Configuration parameters and values swept in the experiment.