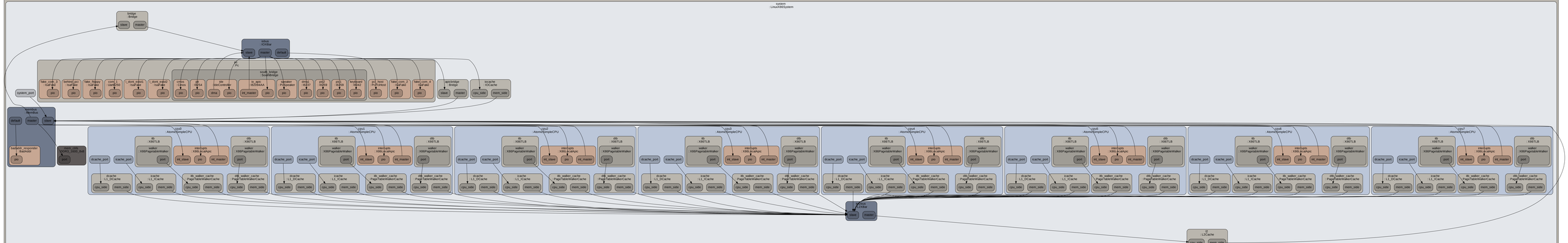
root
Root
system
LinuxX86System
bridge
Bridge
slave
master
iobus
IOXBar
slave
master
default
pc
Pc
south_bridge
SouthBridge
fake_com_3
fake_com_3_bar
pio
behind_pci
behind_pci_bar
pio
fake_floppy
fake_floppy_bar
pio
com_1
Uart8250
pio
i_dont_exist1
i_dont_exist_bar
pio
i_dont_exist2
i_dont_exist_bar
pio
cmos
Cmos
pio
pit
I8254
pio
ide
IdeController
pio
dma
pio
io_apic
I82094AA
int_master
pic
speaker
PcSpeaker
pio
mm1
I8237
pio
mm2
I8237
pio
mm3
I8259
pio
mm4
I8259
pio
keyboard
I8042
pio
pci_host
PciHost
pio
fake_com_2
fake_com_2_bar
pio
fake_com_4
fake_com_4_bar
pio
apicbridge
Bridge
slave
master
iocache
IOCache
cpu_side
mem_side
system_port
interrupts
X86LocalApic
pio
int_slave
int_master
biosbldr
BiosAddr
pio
ioport
Ioport_bar
port
cpu0
AtomicSimpleCPU
itb
X86TLB
walker
X86PagetableWalker
port
interrupts
X86LocalApic
pio
int_slave
int_master
dtb
X86TLB
walker
X86PagetableWalker
port
dcache_port
icache_port
dcache
L1_DCache
cpu_side
mem_side
icache
L1_ICache
cpu_side
mem_side
itb_walker_cache
PageTableWalkerCache
cpu_side
mem_side
dtb_walker_cache
PageTableWalkerCache
cpu_side
mem_side
cpu1
AtomicSimpleCPU
cpu2
AtomicSimpleCPU
cpu3
AtomicSimpleCPU
cpu4
AtomicSimpleCPU
cpu5
AtomicSimpleCPU
cpu6
AtomicSimpleCPU
cpu7
AtomicSimpleCPU
membus
MemBus
default
master
slave
l2bus
L2XBar
slave
master
l2
L2Cache
cpu_side
mem_side