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----- Address Info -----
-- 0x00 - Control signals
--   bit 0 - ap_start (Read/Write/COR)
--   bit 1 - ap_done (Read/COR)
--   bit 2 - ap_idle (Read)
--   bit 3 - ap_ready (Read)
--   bit 7 - axi4_reset (Read/Write)
-- others - reserved
-- 0x04 - Global Interrupt Enable Register (Read/Write)
-- others - reserved
-- 0x08 - IP Interrupt Enable Register (Read/Write)
--   bit 0 - enable ap_start interrupt (Read/Write)
--   bit 1 - enable ap_ready interrupt (Read/Write)
-- others - reserved
-- 0x0C - IP Interrupt Status Register (Read/Write)
--   bit 0 - ap_start (COR/TOW)
--   bit 1 - ap_ready (COR/TOW)
-- others - reserved
-- 0x10 - Data signal of fs
--   bit 0-3 - f2p[3:0] (Read/Write)
--   bit 4 - reserved

```

