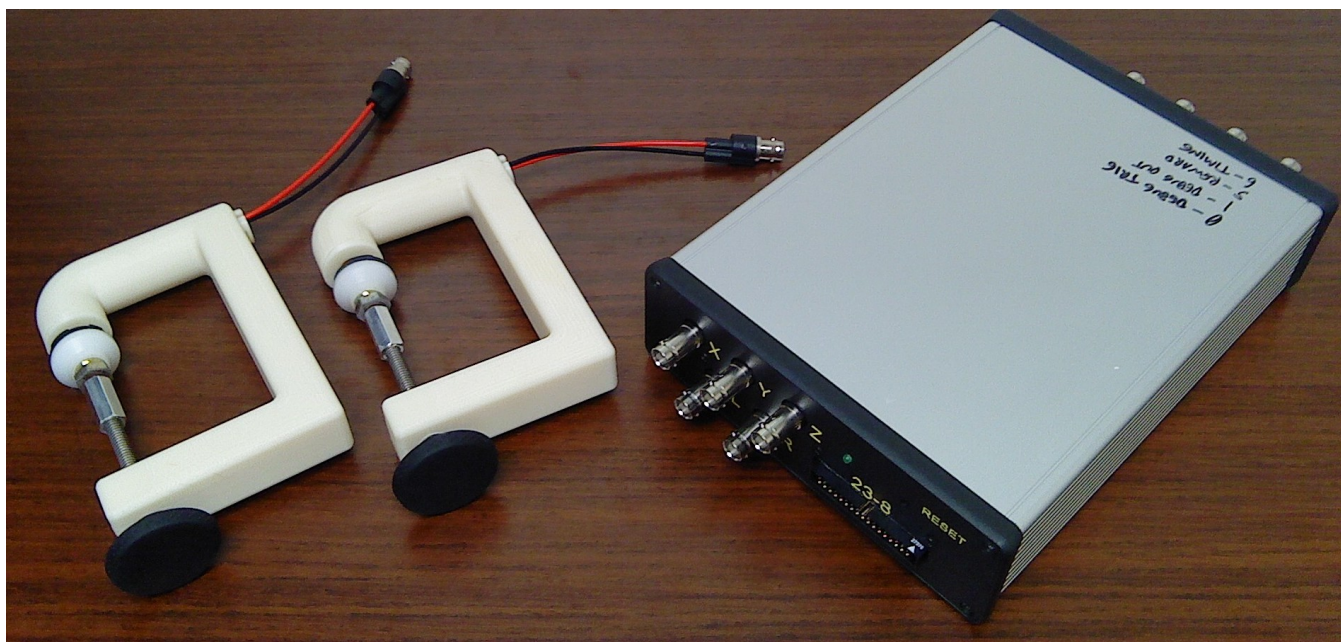


# I/O SynchBox Developer Manual

Written by Christopher Thomas – October 25, 2023.



# Contents

<b>1</b>	<b>Overview</b>	<b>1</b>
<b>2</b>	<b>Command Interface</b>	<b>4</b>
<b>3</b>	<b>Accessories</b>	<b>10</b>
<b>4</b>	<b>Maintenance</b>	<b>11</b>
4.1	Using avrdude On Linux, Windows, and MacOS . . . . .	12
<b>5</b>	<b>Hardware</b>	<b>13</b>
<b>6</b>	<b>Firmware</b>	<b>17</b>
<b>7</b>	<b>Project Files</b>	<b>19</b>
<b>A</b>	<b>BrainAmp Adapter</b>	<b>21</b>

# Chapter 1

## Overview

The I/O SynchBox is a device that lets a host computer and several instruments talk to each other. It was commissioned by the Attention Circuits Control Laboratory to facilitate their experiments<sup>1</sup>. A system diagram of the York University installation is shown in Figure 1.1. A diagram of the I/O signals used by the I/O SynchBox is shown in Figure 1.2.

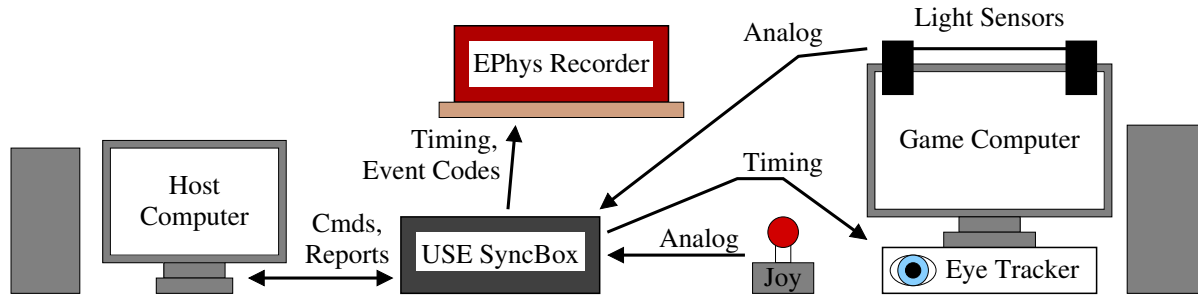


Figure 1.1: System block diagram.

<sup>1</sup>I/O SynchBox integration is described in: “USE: An integrative suite for temporally-precise psychophysical experiments in virtual environments”, Watson, M. R., Voloh, B., Thomas, C. J., Hasan, A. M., and Womelsdorf, T. (2018), *bioRxiv*, 434944 doi:10.1101/434944

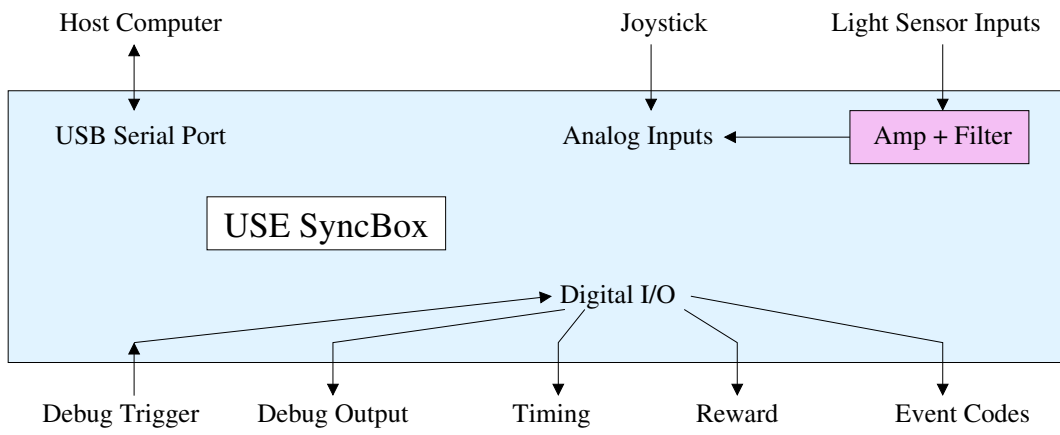


Figure 1.2: I/O SyncBox inputs and outputs.

The I/O SynchBox handles several types of communication:

- It talks to the host computer over a USB serial port connection.
- It generates timing pulses. These are controlled using the **Txx** series of commands. These are typically used to provide events with known timestamps to equipment such as eye-trackers and electrophysiology instruments.
- It generates single pulses of variable duration. These are controlled using the **RWD** command. These are typically used for dispensing a reward to test animals.
- It emits binary number values over a parallel digital interface. These are controlled using the **Nxx** series of commands. These are typically used as event codes for electrophysiology equipment.
- It reads analog information from three general-purpose analog inputs, and shows this data during logging. These are typically used for joystick inputs.
- It reads light sensors to synchronize with the game computer's display. The light sensor inputs have hardware amplification and filtering (they are special-purpose inputs, not general analog inputs). Light levels are shown during logging.
- Logged data is controlled using the **Lxx** series of commands.

The commands are described in detail in Section 2.

To started, connect to the I/O SynchBox (USB serial link at 115200 baud, 8N1), and type “?” (and enter) for help. Type “**QRY**” to see the I/O SynchBox 's settings.

The front and back panels of the I/O SynchBox are shown in Figure 1.3. The pinout of the parallel output is shown in Figure 1.4.

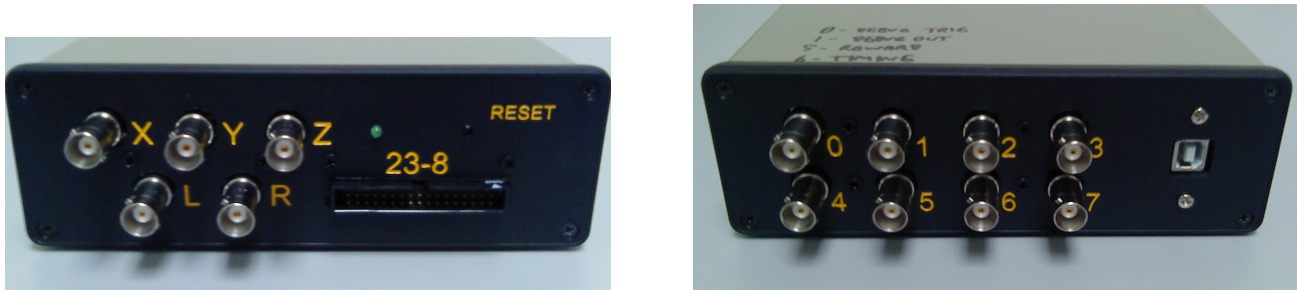


Figure 1.3: I/O SynchBox front and back panels.

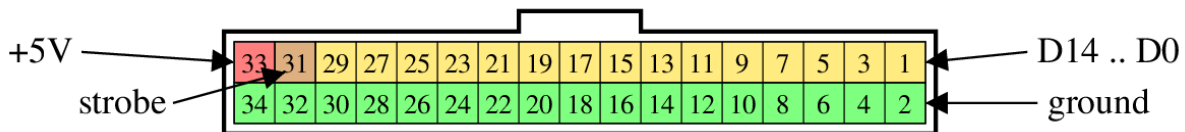


Figure 1.4: Event code connector pinout.

## Chapter 2

# Command Interface

The I/O SynchBox is connected to the host computer via a USB serial link at 115200 baud. The host issues commands, and the I/O SynchBox reports events that occur.

The I/O SynchBox issues timing pulses at regular intervals; issues reward pulses as one-off events; writes parallel event code data to the ephys I/O port; and reports analog input from the joystick and light sensors at regular intervals.

In addition to analog light sensor values, the I/O SynchBox also reports whether each sensor is above- or below-threshold. Light sensor thresholds are calibrated by measuring sensor values during a time window, taking the average of these measured values, and then saving the average as the new threshold (overwriting the old threshold). “Free-running” calibration does this repeatedly (starting a new window when the old window ends).

A list of available commands is shown in Figure 2.1. A timing diagram of user-commanded output events is shown in Figure 2.2. A diagram illustrating light sensor filtering is shown in Figure 2.3.

Sample output (including echoed commands) is shown in Figure 2.4. Format details for verbose, terse, and packed log entries are given in Figure 2.5. Typical configuration/status information is shown in Figure 2.6.

There are several important things to note about interactions with the I/O SynchBox :

- The command parser does **not** recognize backspaces.
- **All times**, whether they’re timestamps or duration arguments, are given in “**clock ticks**”, **not milliseconds**. By default, clock ticks are 0.1 ms in length. One second is 10000 ticks.

Milliseconds would not have been sufficiently precise, and microseconds would have overflowed a 32-bit counter in slightly more than one hour.

Bear in mind that the maximum argument value is 65535 (a duration or interval of about 6.5 seconds).

Commands:

?, HLP : Help screen.  
QRY : Query system state.  
ECH 1/0: Start/stop echoing typed characters back to the host.  
IDQ : Device identification string query.  
INI : Reinitialize (clock reset and events idled).  
LOG 1/0: Start/stop reporting log data.  
LIN n: Set the data reporting interval to n ticks.  
LVB 2/1/0: Set data report verbosity (2 = full, 1 = terse, 0 = packed hex).  
TPW n: Set the timing pulse duration to n ticks.  
TPP n: Set the timing pulse period to n ticks.  
TIM 1/0: Enable/disable timing pulses.  
TBW n: Set the timing channel 2 pulse duration to n ticks.  
TBP n: Set the timing channel 2 pulse period to n ticks.  
TIB 1/0: Enable/disable timing channel 2 pulses.  
RWD n: Send a reward pulse lasting n ticks.  
RWB n: Send a reward channel 2 pulse lasting n ticks.  
NSU n: Set the event code pre-strobe setup time to n ticks.  
NHD n: Set the event code post-strobe hold time to n ticks.  
NSE 1/0: Enable/disable strobing the most significant event code bit.  
NPD n: Set the event code strobe pulse duration to n ticks.  
NEU n: Emit value n over the event code parallel interface.  
NDW n: Set event code data width to n bits (8 or 16).  
CSL/R/I : Light sensor calibration slaved to left, right, or independent.  
CAO/F n: Calibrate light sensors over n-tick window (one-shot/free-running).  
CTR/L n: Force left/right light sensor threshold value to n.  
FIL n: Set analog noise filter window to  $2^n$  samples (0 disables).  
FST n: Set analog spike rejection threshold to  $2^n$  times the variance.  
FSW n: Set spike rejection variance window to  $2^n$  samples (0 disables).  
Debugging commands:  
XAL 1/0: Start/stop streaming full-rate analog light sample data.  
XJL 1/0: Swaps or un-swaps joystick XY and light sensor LR inputs.  
XTR 1/0: Enable/disable reward triggered by rising edge of GP00.  
XTT 1/0: Enable/disable timing active on high level on GP00.  
XTN 1/0: Enable/disable event code triggered by rising edge of GP00.  
XMA n: Emit a rising edge on GP01 when analog 0..4 (X/Y/Z/L/R) rises.  
XMD : Disable GP01 analog diagnostic output.

Figure 2.1: Command summary.

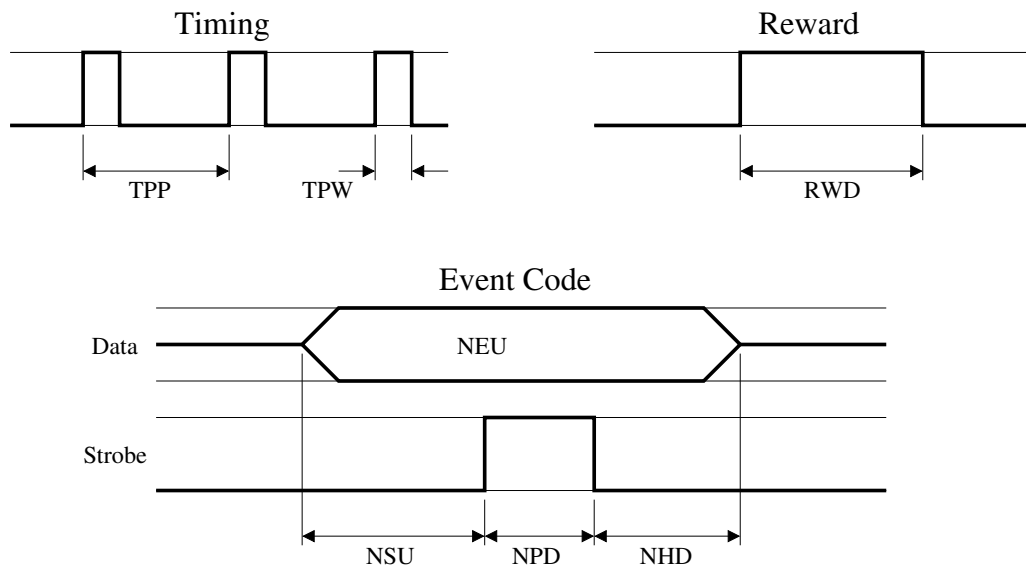


Figure 2.2: Output signal timing diagram.

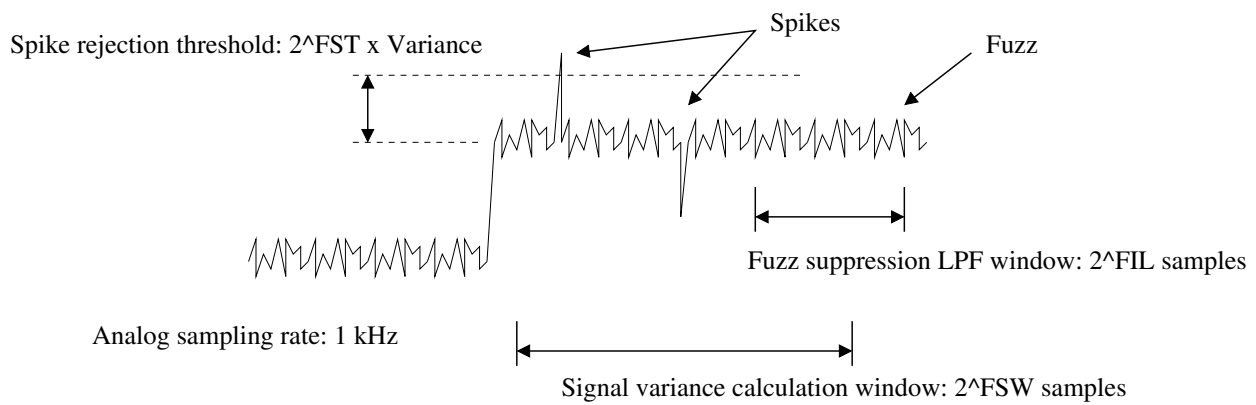


Figure 2.3: Light sensor filtering.



- Number values entered by the user are given in **decimal**, but number values reported from the I/O SynchBox are in **hexadecimal** (with the exception of status information).

Processing user information in decimal made the command parser simpler (and makes life easier for manually entering data). Reporting information in hexadecimal makes output more compact (important for high data rate output), and avoids time-consuming data conversion (converting to hexadecimal does not require divisions, only bit-shifts; converting to decimal does require divisions). The data rate is high enough that this matters.

The data reporting rate is limited by the serial connection. If events occur too fast to report (or if full-rate analog debug reporting exceeds the serial connection's ability to report), reports will be dropped. Events themselves will still occur.

At 115200 baud, the maximum reporting rate without packet loss is 150 Hz with verbose logging, 200 Hz with terse logging, and 300 Hz with packed hex logging.

```

log 1
Time: 0005764f Joy (x/y/c): 0c 00 00 Opt (l/r): 76 75 BLK BLK
Time: 00057a37 Joy (x/y/c): 0f 00 00 Opt (l/r): 76 76 BLK BLK
Time: 00057e1f Joy (x/y/c): 0e 00 00 Opt (l/r): 76 76 BLK BLK
lvb 1
T: 000631d0 XYC: 0d 00 00 LR: 76 76 B B
T: 000635b8 XYC: 0f 00 00 LR: 76 76 B B
T: 0006399f XYC: 0e 00 00 LR: 76 76 B B
lvb 0
T0006ddb0J0f0000L7676BB
T0006e198J0e0000L7676BB
T0006e57fJ0e0000L7676BB
lvb 2
Time: 000785a7 Joy (x/y/c): 0c 00 00 Opt (l/r): 76 76 BLK BLK
Time: 0007898f Joy (x/y/c): 0d 00 00 Opt (l/r): 76 76 BLK BLK
Time: 00078d77 Joy (x/y/c): 0f 00 00 Opt (l/r): 76 76 BLK BLK
log 0

tim 1
Synch: 00087e8e
Synch: 0008a59e
Synch: 0008ccae
tim 0

rwd 1000
Reward: 000a3433 03e8
rwd 1000
Reward: 000aa549 03e8
rwd 500
Reward: 000ba7ee 01f4

xal 1
99387676
99457675
99527675
995e7675
xal 0

neu 12345
Code: 000f2e19 3039
neu 32767
Code: 00101097 7fff

```

Figure 2.4: Sample I/O SynchBox output.

Verbose:

**Time:** 0003f6ef **Joy (x/y/c):** 062a 063f 0647 **Opt (l/r):** 0610 061b **BLK** **BLK**

Terse:

**T:** 0004d853 **XYC:** 055f 058f 05b3 **LR:** 05b0 056c **B** **B**

Packed 16-bit:

**T** 00054937 **J** 05b8 05c2 05c5 **L** 056f 05cb **BB**

Packed 8-bit:

**T** 00054937 **J** 55 58 5b **L** 56 5c **BB**

Figure 2.5: Log entry format details.

devicetype: USE SyncBox subtype: v1 revision: 20200128

System state (all values in base 10):

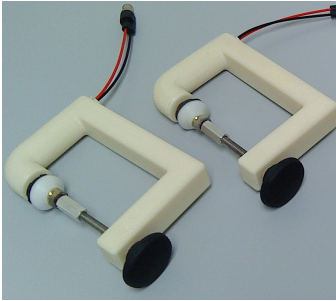
Version: 20200128  
Timestamp: 37580 ticks  
Clock ticks per second: 10000  
Reward I/O: D11/B5/GP05 Reward ch2 I/O: D10/B4/GP04  
Timing I/O: D12/B6/GP06 Timing ch2 I/O: D13/B7/GP07  
Debug trigger input: D6/H3/GP00  
Debug monitor output: D7/H4/GP01  
Logging: OFF  
Log interval: 100 ticks  
Verbosity: Full  
Timing: OFF  
Timing pulse width/period: 10 / 10000 ticks  
Timing ch2: OFF  
Timing ch2 pulse width/period: 10 / 10000 ticks  
Event code data front-porch/strobe/back-porch: 2 / 2 / 2 ticks  
Event code strobe: ON  
Event code data width: 16 bits  
Light sensor thresholds (left/right): 65535 / 65535  
Light threshold slaving: INDEPENDENT  
Analog sample size: 8 bits  
Noise suppression filter: 2 bits (decay length 4 samples)  
Spike suppression filter:  
Variance LPF: 6 bits (decay length 64 samples)  
Spike rejection threshold: 2<sup>2</sup> (4x)  
External event triggers: none  
Analog input being monitored: none  
Joystick analog threshold: 127  
End of system state.

Figure 2.6: I/O SyncBox identification string and status report.

## Chapter 3

# Accessories

Accessories produced for the I/O SynchBox include the following:



**Light sensor clamps** may be attached to a wide variety of monitors (including notebook computers). Patches of the display are strobed, and this flickering is recorded in order to measure when frame redraws occur.

## Chapter 4

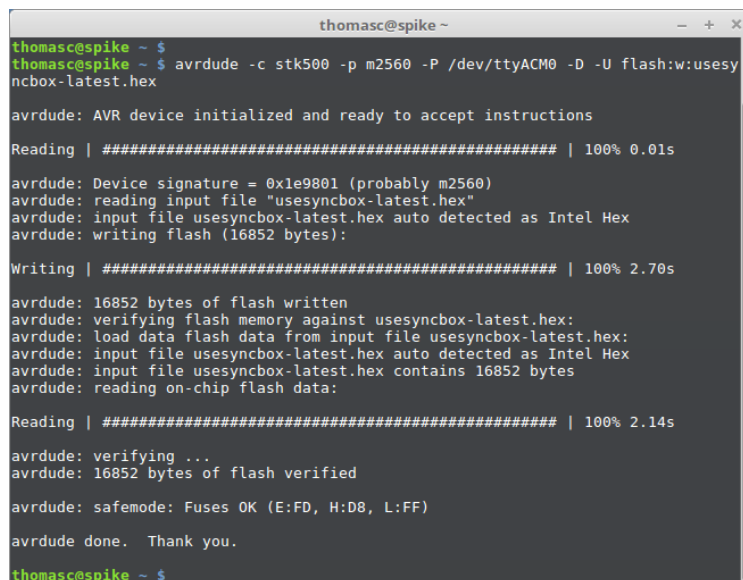
# Maintenance

From time to time, new versions of the I/O SynchBox firmware are released. These can be flashed using the “avrdude” utility, which is bundled with the Arduino development environment or may be downloaded on its own.

To use “avrdude”, plug in the I/O SynchBox, determine what the name of the Arduino’s USB serial device is, bring up a “command terminal” window, navigate to the directory with the firmware file, and type:

```
avrdude -c stk500 -p m2560 -P (device) -D -U flash:w:(firmware file)
```

For a firmware file named “usesynchbox-latest.hex”, and a USB serial device called “/dev/ttyACM0”, the result will be similar to the following:



```
thomasc@spike ~  
thomasc@spike ~ $ avrdude -c stk500 -p m2560 -P /dev/ttyACM0 -D -U flash:w:usesynchbox-latest.hex  
avrdude: AVR device initialized and ready to accept instructions  
Reading | ##### | 100% 0.01s  
avrdude: Device signature = 0x1e9801 (probably m2560)  
avrdude: reading input file "usesynchbox-latest.hex"  
avrdude: input file usesynchbox-latest.hex auto detected as Intel Hex  
avrdude: writing flash (16852 bytes):  
Writing | ##### | 100% 2.70s  
avrdude: 16852 bytes of flash written  
avrdude: verifying flash memory against usesynchbox-latest.hex:  
avrdude: load data flash data from input file usesynchbox-latest.hex:  
avrdude: input file usesynchbox-latest.hex auto detected as Intel Hex  
avrdude: input file usesynchbox-latest.hex contains 16852 bytes  
avrdude: reading on-chip flash data:  
Reading | ##### | 100% 2.14s  
avrdude: verifying ...  
avrdude: 16852 bytes of flash verified  
avrdude: safemode: Fuses OK (E:FD, H:D8, L:FF)  
avrdude done. Thank you.  
thomasc@spike ~ $
```

The names of USB serial devices vary widely from operating system to operating system and version to version. Consult appropriate documentation to determine how to find the I/O SynchBox’s USB serial device on your operating system.

## 4.1 Using avrdude On Linux, Windows, and MacOS

While `avrdude` can be installed as a stand-alone package, an easy and reliable way to install it is to install the “Arduino” IDE (found at: <https://www.arduino.cc>). This will install several things:

- The Arduino development environment.
- The `avr-gcc` compiler and its libraries.
- The `avr-binutils` toolchain used with `avr-gcc`.
- The `avrdude` program for flashing AVR-based boards.

Various quirks show up on each operating system:

- Under Linux, you may have to add a “udev rules” file in `/etc/udev/` to get Arduino boards detecting and connecting properly. The Arduino IDE package *should* set this up for you.
- Under Windows 10, neither `avrdude` nor its configuration file will be on path by default. To use it, first find out where it was installed, and then either type the full path name ahead of the command or add the relevant directory to the path.
  - To find out where `avrdude` was installed, look for an “Arduino” shortcut on the desktop. Right-click it to edit its properties; this will tell you what directory the Arduino binary is in. Open the top-level directory in that tree, and tell Windows to search that folder to find “`avrdude`”. This will bring up the directory with `avrdude` in it, which should also contain the “`avrdude.conf`” configuration file.
  - To set the path in Windows 10, go to “control panel”, “system”, “advanced system settings”, “advanced” tab, “environment variables”. Under “user variables”, select “path” and click “edit”. Add the directory as a new entry in the list.
- To run `avrdude` from the command line under Windows, type the following (as one line):  
(`avrdude path`)/`avrdude -C (avrdude path)/avrdude.conf -c stk500 -p m2560 -P (serial port) -D -U flash:w:(firmware file including its path)`
- Under MacOS, **FIXME: Information goes here.**

# Chapter 5

## Hardware

The I/O SynchBox was implemented as a “shield” that mates with a commercial Arduino Mega2560 r3 board. A schematic for this shield is shown in Figure 5.1, and a layout plot in Figure 5.3. As of January 2017, a pre-amplifier was added for the light sensor inputs; this is indicated in simplified form in Figure 5.1.

Salient features are as follows:

- Individual digital output signals (timing and reward) are wired to dedicated BNC connectors (with an additional SMB connector for the timing signal).
- Joystick inputs are accepted from BNC connectors, and divided using pairs of 470 k $\Omega$  resistors. This maps the joystick signal range (0-5V) to the analog to digital converter range (0-2.56V).
- Light sensor inputs are accepted from BNC connectors, with the negative terminal fed through a 10 k $\Omega$  resistor to ground. The light sensors are phototransistors, which behave as current sources; a 10 k $\Omega$  resistor converts the photocurrent to a voltage large enough to measure reliably but small enough to leave ample headroom for device and light source variation.

The pre-amplifier has a gain of 10x, and performs high-pass filtering with a time constant of several seconds to cancel DC bias. The system should be allowed to stabilize for one minute after being connected and powered, prior to use.

- The NeuraLynx handshaking signals are implemented using two dedicated ATmega output ports, for speed of manipulation by firmware. The ports chosen each map to contiguous blocks of 8 pins on the Arduino Mega header, which are wired to an appropriate 34-pin connector.

In 16-bit mode, bit 15 is the strobe bit, and bits 0-14 are data.

In 8-bit mode, bit 15 is still the strobe bit, bits 8-14 are data, and bits 0-7 are always zero. **NOTE:** The user sees this as an 8-bit interface. The argument to “NEU” is 0-127.

- A status LED is provided, to communicate diagnostic information. This is not presently used (it’s turned on when the I/O SynchBox boots and is left on).
- A reset switch is provided, for manual reset without power cycling. In practice, power cycling is likely the most convenient reset method.

- All traces and cables are assumed to be “electrically short” – that is, to have propagation delays very much smaller than the timescales relevant to the equipment being connected. This allows cable termination and driving impedance to be ignored.

A rule of thumb is that reflections propagate at a minimum of half the speed of light, and that ten round-trips is sufficient to stabilize signal levels. With a characteristic timescale of 0.1 ms, maximum cable length is about 750 m.

If connected equipment uses a sufficiently fast communications sampling rate (50 MHz or more), reflections may become significant.

A reverse-engineered schematic of the joystick is shown in Figure 5.2.



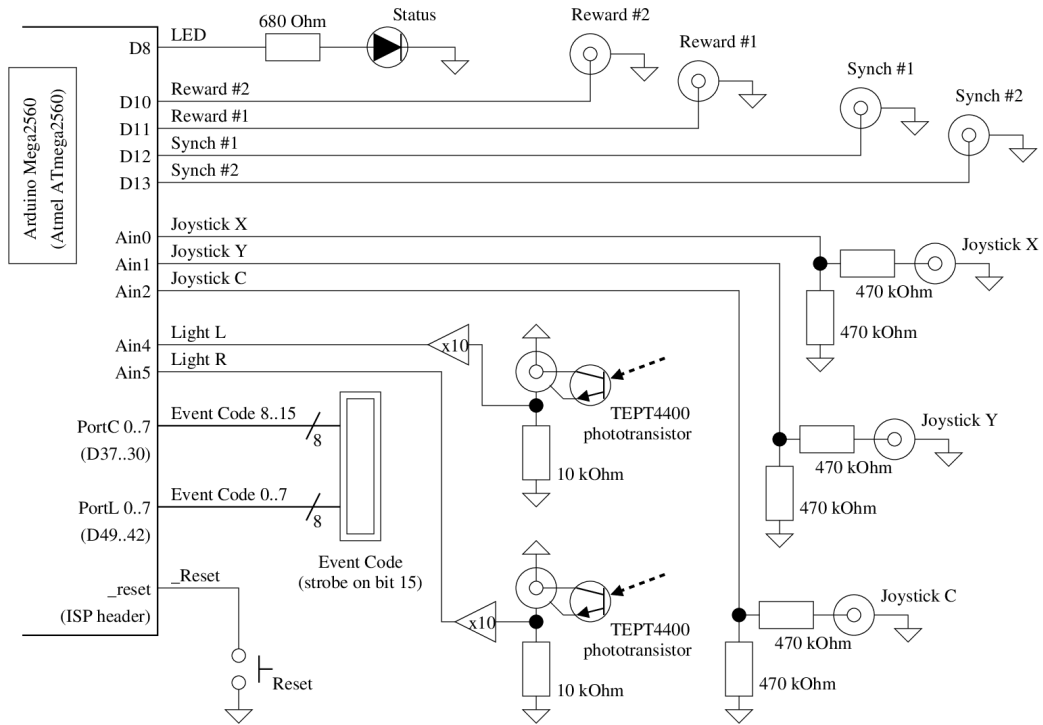


Figure 5.1: I/O SynchBox schematic.

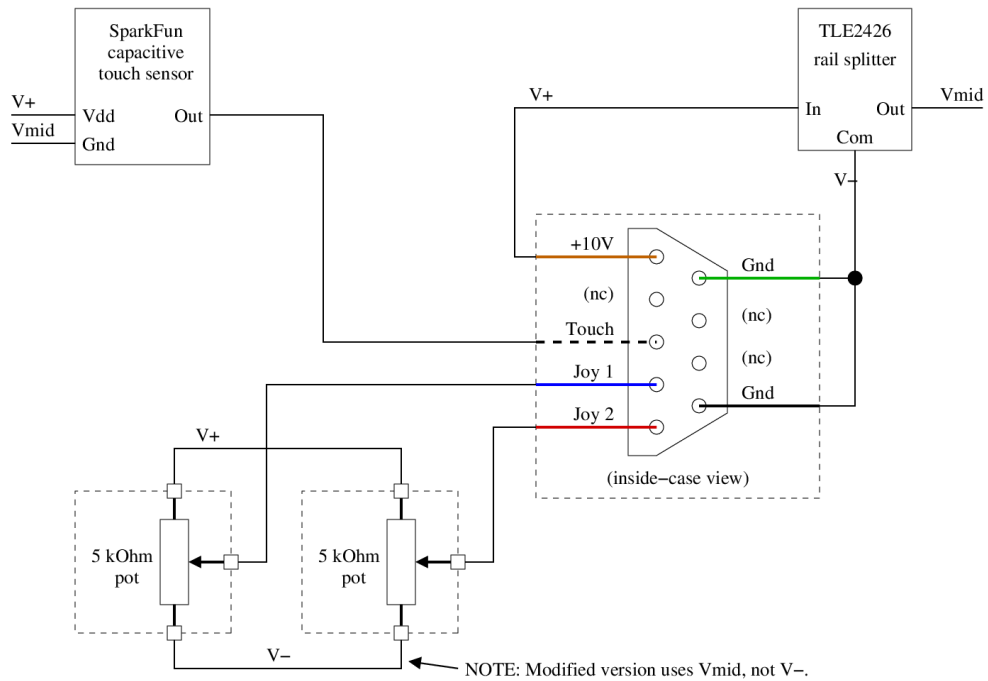


Figure 5.2: Joystick schematic.



# Chapter 6

## Firmware

The I/O SynchBox firmware consists of two polling loops that communicate via global variables. These are illustrated in Figure 6.1.

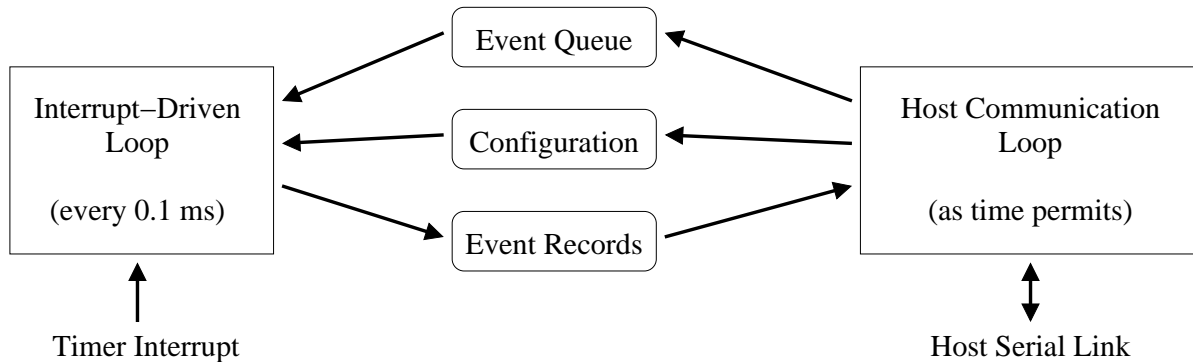


Figure 6.1: I/O SynchBox firmware block diagram.

The interrupt-driven loop is called at every measurement tick (0.1 ms intervals), and always occurs. During each loop, it sets output pin values as dictated by the event queue, and it attempts to read one analog input.

The host communication loop is called via the Arduino `loop()` function, and repeats as frequently as it can but with no time guarantees. During each loop, it polls the host serial link for inbound commands, queues events or alters internal state based on those commands, and reports events that have already occurred.

State information, including the event queue and records of events that have recently occurred, is stored in global variables. As these may be updated via interrupt service routines, they are flagged as `volatile` and are accessed from within `ATOMIC_BLOCK()` constructs when read or modified by the host communication loop (interrupts are already atomic).

The following aspects of the firmware should be kept in mind:

- Analog reads take slightly longer than 0.1 millisecond. The interrupt-driven loop initiates conversions

only when the analog to digital converter is not busy. With the default configuration, this means one conversion every 0.2 millisecond, or one full read of all analog inputs every 1.0 millisecond.

Analog inputs are read sequentially (round-robin order).

- It is possible to set the reporting rate high enough that the serial link cannot keep up with the requested rate. What occurs in this scenario is that the host communication loop reports the *most recent* set of events that occurred every time it is polled.

The result from the user's viewpoint is that records are silently dropped. This situation can be detected by paying close attention to the timestamps of reported events (these timestamps are always accurate).

# Chapter 7

## Project Files

**FIXME: This is out of date as of December 2018.**

The following folders contain the CAD files, code, and documentation for the I/O SynchBox project:

<b>sketch</b>	Arduino firmware working directory.
<b>hexfiles</b>	Arduino firmware binary releases.
<b>pcb</b>	Printed circuit board CAD files.
<b>manual</b>	User guide document files.
<b>schematics</b>	Schematic drawings. Needed for the manual.
<b>datasheets</b>	Datasheets for selected components.
<b>drawings</b>	Mechanical drawings.
<b>capture</b>	I/O SynchBox communications logs.

Relevant commentary is as follows:

- The “sketch” directory should be extracted (or symlinked) to a “neurarduino” project directory within Arduino’s “sketchbook”. See the “README” file in that directory for more commentary.
- Alternatively, the “sketch” directory may be built using the “neuravr” library instead of using the Arduino development environment. To do this, use “make -f Makefile.neuravr”. See the “README” file in the “sketch” directory for more commentary.
- The “pcb” directory contains two subdirectories (“plots” and “gerber”). The root directory contains the CAD file (edited with the “pcb” CAD program from the gEDA project). The “plots” directory contains an automatically-generated postscript file and manually-generated XCF and PNG files. The “gerber” directory contains automatically-generated GBR files, and a script (“pcb2seed.sh”) that converts these into a form SeeedStudio will accept (the ZIP archive in that directory).
- The “manual” directory contains LaTeX source files for the user manual. Some resource files are symbolic links to files that are elsewhere in the project.
- The “schematics” and “datasheets” directories contain human-readable information pertaining to the design of the I/O SynchBox system. Anyone redesigning the I/O SynchBox will likely want to read these.

- The “drawings” directory contains mechanical drawings of the faceplate for the I/O SynchBox enclosure. The “laser” file is separated into layers in such a way as to facilitate importing into laser-cutter software.
- The “capture” directory contains a sample conversation with the I/O SynchBox . Manually-extracted excerpts are used in the user manual. This file contains most types of traffic that will be seen when talking to the I/O SynchBox . It should be noted that echo was turned on during this conversation, which (prior to 2017) sometimes results in command characters being interleaved with event data.

# Appendix A

## BrainAmp Adapter

An adapter was constructed that allows connection of a BrainAmp unit in place of a NeuroLynx unit (connecting to the 26-pin “trigger” port). Data emitted over the NeuroLynx interface shows up as S-type and R-type markers in the BrainAmp data (with the least significant byte mapping to S values and the most significant byte mapping to R values).

Pin mappings are listed in Table A.1, with physical connector information shown in Figure A.1. A layout plot is provided in Figure A.2.

NeuroLynx pin	Function	BrainAmp pin	Function
1	D0	14	D00 (S 1)
3	D1	2	D01 (S 2)
5	D2	15	D02 (S 4)
7	D3	3	D03 (S 8)
9	D4	16	D04 (S 16)
11	D5	4	D05 (S 32)
13	D6	17	D06 (S 64)
15	D7	5	D07 (S 128)
17	D8	18	D08 (R 1)
19	D9	6	D09 (R 2)
21	D10	19	D10 (R 4)
23	D11	7	D11 (R 8)
25	D12	20	D12 (R 16)
27	D13	8	D13 (R 32)
29	D14	21	D14 (R 64)
–	(ground)	9	D15 (R128)
31	(strobe)	–	(not connected)

Table A.1: NeuroLynx to BrainAmp pin mappings.

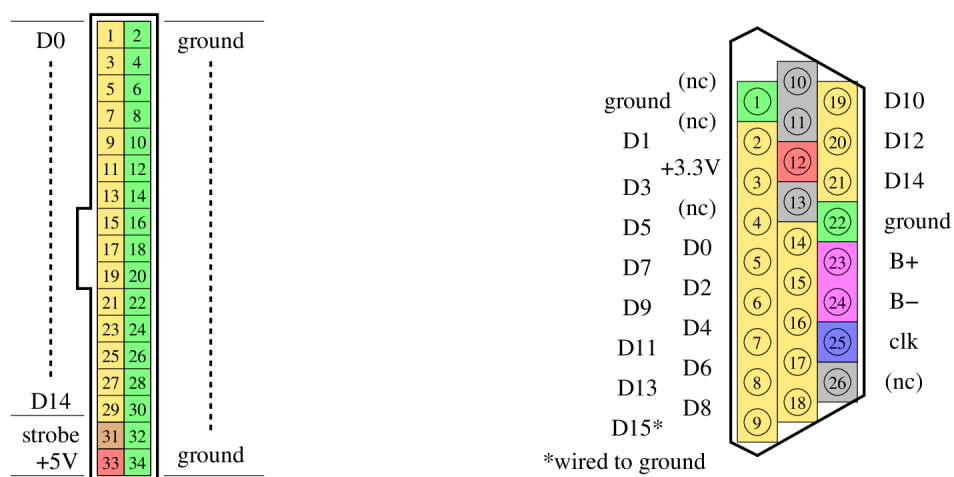


Figure A.1: NeuraLynx and BrainAmp connector pinouts.

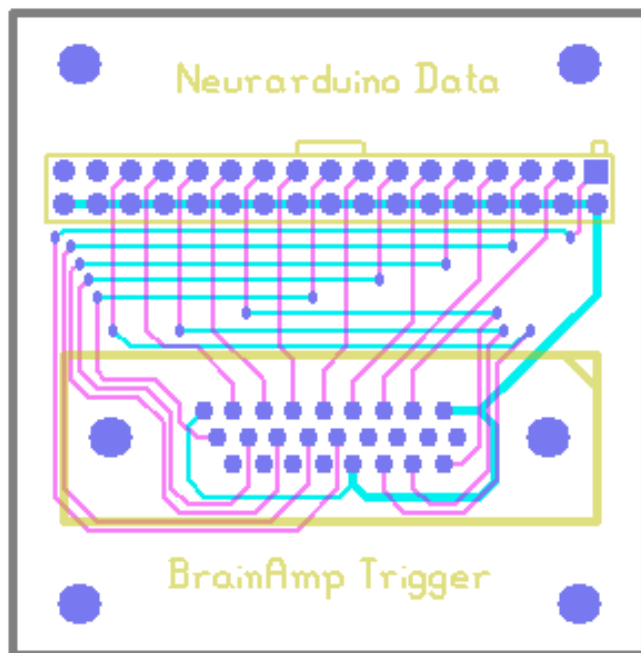


Figure A.2: BrainAmp adapter PCB layout.