Appendix C Pinout of the trigger socket (digital port)

The trigger socket for the input of external synchronization pulses such as trigger and reaction time markers is located on the front of the USB2 Adapter (labeled *Trigger In*). On the PCI Adapter Card, this is located next to the first fiber optic socket. This is a 26-pin HD D-Sub connector. The input ports are TTL-CMOS ports.

Table C-1 shows the pinout and the assignment of the 16 digital inputs to stimulus information (S markers) and response information (R markers) as interpreted by the Recorder for positive logic. If several bits are set simultaneously at the stimulus input, their values are added together. For example, if D01 (S 2) and D05 (S 32) are set simultaneously, this results in the stimulus S 34. The same applies for combinations of response bits. You will find details on configuration in the Recorder User Manual.

The third and fourth columns show which contacts on the connectors (25-pin D-Sub/LPT and BNC connectors) of standard trigger cables are connected to which contacts of the trigger socket (digital port).

Table C-1. Pinout of the trigger socket (digital port) on USB2 Adapter and PCI Adapter Card

Pin on 26-pin HD D-Sub trigger socket (digital port)	Function	25-pin D-Sub/LPT on trigger cable	BNC connector on trigger cable
1	Ground	25	Ground
2	D01 (S 2)	3	
3	D03 (S 8)	5	
4	D05 (S 32)	7	
5	D07 (S128)	9	
6	D09 (R 2)		
7	D11 (R 8)		
8	D13 (R 32)		
9	D15 (R128)		Signal
10	Unused		
11	Unused		
12	VCC +3.3 V		
13	Unused		
14	D00 (S 1)	2	
15	D02 (S 4)	4	
16	D04 (S 16)	6	
17	D06 (S 64)	8	
18	D08 (R 1)		
19	D10 (R 4)		

Table C-1. Pinout of the trigger socket (digital port) on USB2 Adapter and PCI Adapter Card

Pin on 26-pin HD D-Sub trigger socket (digital port)	Function	25-pin D-Sub/LPT on trigger cable	BNC connector on trigger cable
20	D12 (R 16)		
21	D14 (R 64)		
22	Ground		
23	Block+		
24	Block-		
25	5 kHz out		
26	Unused		