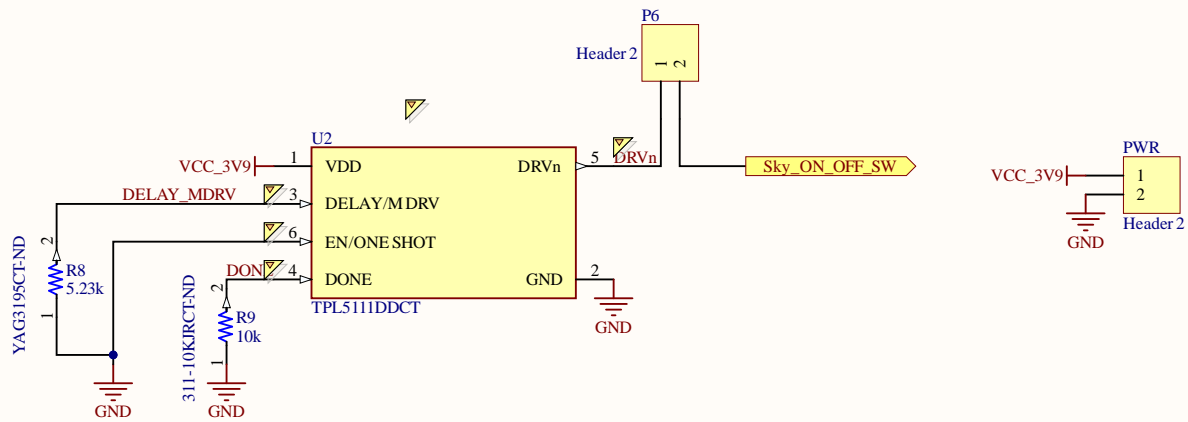


Title			
LTE ADAPTER BOARD			
Size	Number		Revision
Letter			0
Date:	1/05/2026	Sheet of	
File:	M:\Projects\...\LTE_block_diagram.SchDoc		



Title			
LTE Board Auto Start			
Size	Number		Revision
Letter			0
Date:	1/05/2026		Sheet of
File:	M:\Projects\..LTE_auto_start_Rev01.Sch Drawn By:		

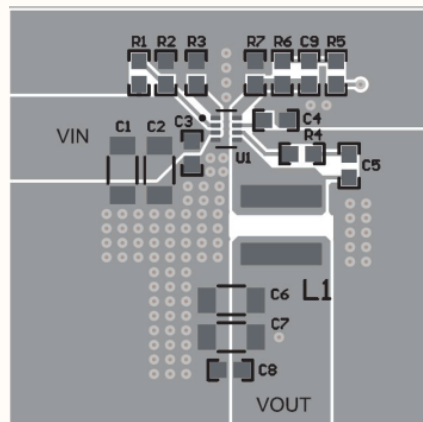
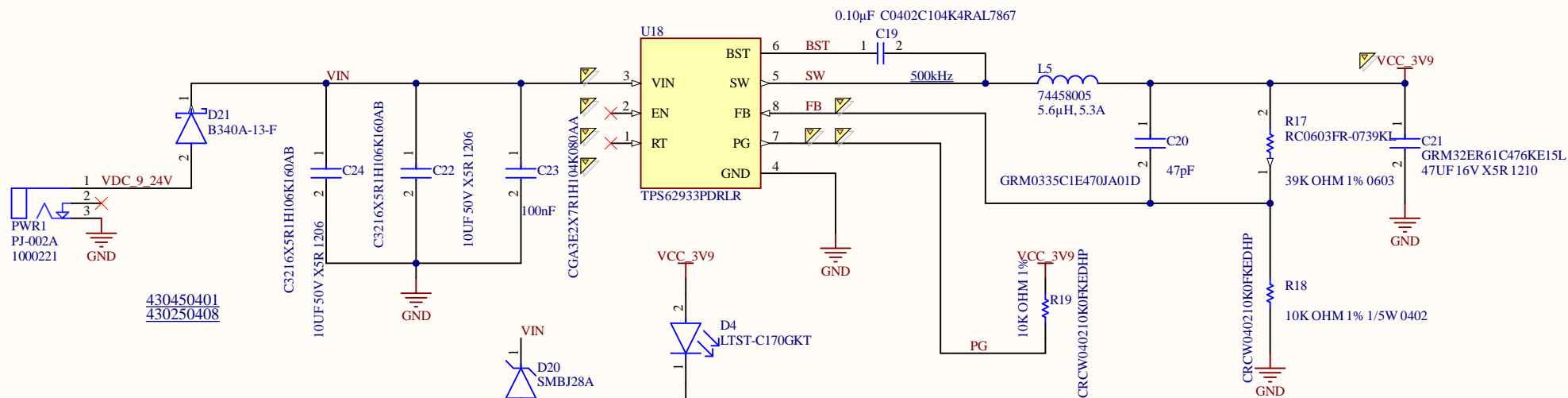


Figure 12-2. TPS62933 Top Layout Example

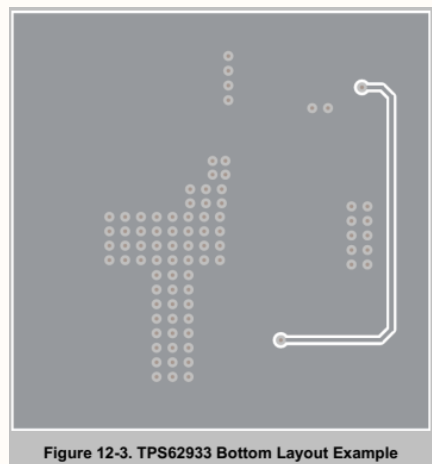
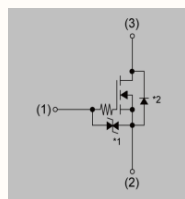
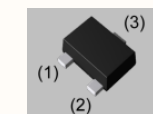
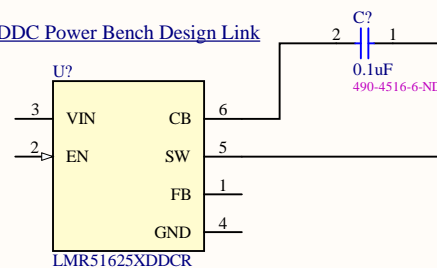


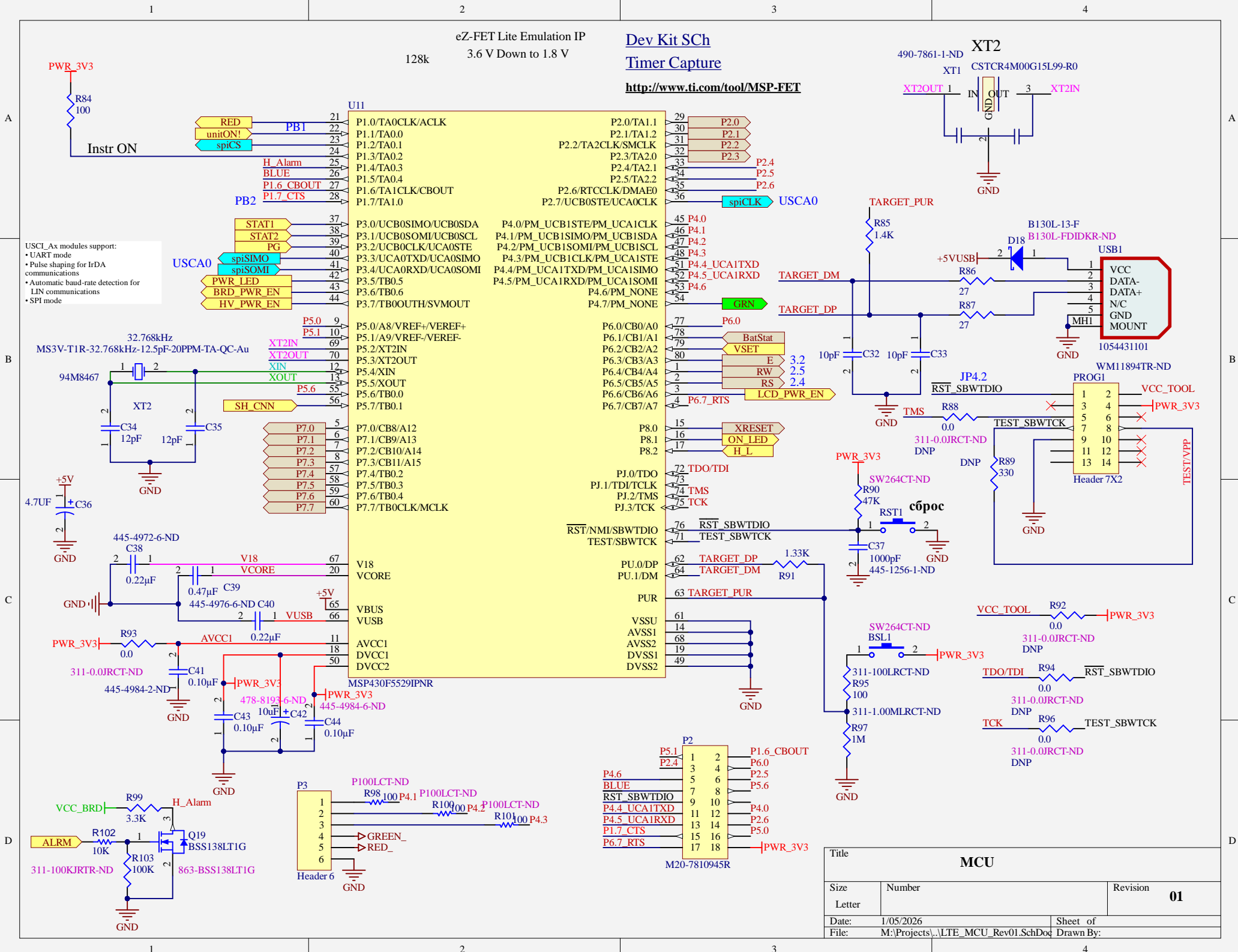
Figure 12-3. TPS62933 Bottom Layout Example



[LMR51625DDC Power Bench Design Link](#)



Title LTE BOARD POWER 9-24VDC to 3.9VDC		
Size	Number	Revision
Letter		0.1
Date:	1/05/2026	Sheet of
File:	M:\Projects\...\LTE_power_Rev01.SchDoc	Drawn By:



/1/ The vendor shall be UL approved to a minimum category of 94v2.
The vendor I.D. shall be silkscreened on the top side of the board.

3. BOARD MUST BE IDENTIFIED WITH APPROPRIATE VENDOR UL IDENTIFICATION MARK,DATE CODE

5. ALL DIMENSION TOLERANCE AS BELOW UNLESS OTHERWISE SPECIFIED.

6. PCB LAYERS : 4 LAYERs

7. PCB THICKNESS : 62 mil \pm 10%

8. TRACE WIDTHS TO BE FINISHED SIZES+/-1MIL

9. TOP SIDE SILKSCREEN : WHITE

BOTTOM SIDE SILKSCREEN: WHITE

10. SOLDER MASK : GREEN BOTH SIDES

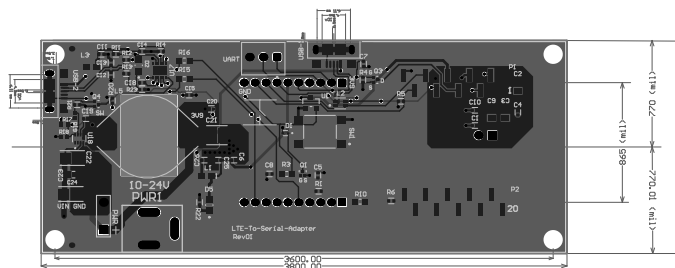
11. MATERIAL : HF FR4 TG: 150 CELSIUS

12. PLATING : IMMERSION GOLD (ENIG)

13. PCB Outline Dimension Tol: ROUTING : +/- 0.13mm

14. Fabricate per IPC-6012A CLASS 2

Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	0.40mil	3,5	
1	Top Layer	CF-004	1.38mil		
	Dielectric 1	PP-017	5.10mil	4,3	
	Dielectric 1a	PP-016	4.60mil	4,4	
2	Mid Layer 1 Sig	CF-004	1.38mil		
	Core	FR-4	39.00mil	4,8	
3	Mid Layer 2 GND	CF-004	1.38mil		
	Dielectric 2	PP-016	4.60mil	4,4	
	Dielectric 2	PP-017	5.10mil	4,3	
4	Bottom Layer	CF-004	1.38mil		
	Bottom Solder	Solder Resist	0.40mil	3,5	
	Bottom Overlay				



1/5/2026
11:35:03 AM

1/5/2026
11:35:03 AM

M:\Projects\Github\IoT-dev\IoT_Dev_v0.00\LTE-To-Serial-Adapter.PcbDoc

Template			
ENGINEER: Roman Chak	TITLE: PCBA, UUDI-360 LTE ADAPTER BOARD		
ENGINEER: XXXXXXXXXX PHONE:XXXX-XXXX-XXXX	PART NO: 29-9020	C	DATE: 1/5/2026
FILE NAME: LTE-To-Serial-Adapter.PcbDoc	LAYER: Top	Part: 29-9020	Rev: 1

PROJECT TITLE: .PRJ_Title	
DESIGNED FOR: .PRJ_Customer	
FILE NAME: LTE-To-Serial-Adapter_PcbDoc	
ENGINEER: .PRJ_Engineer	LAYOUT BY: .PCB_Layout
SCALE: 1.00	ALTUM DESIGNER VERSION: 25.6.2.33

