

Date 24/05/2023 Wednesday

pg # 22
(seventh class)

* COMBINATIONAL LOGIC ANALYSIS AND DESIGN:-

(OVERVIEW TOPICS TODAY)

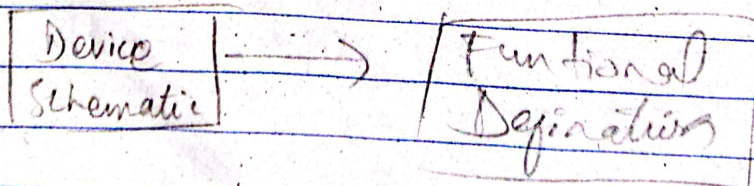
→ Summary of logic & analysis process
Steps for a combinational logic device.

1) ANALYSIS PROCESS:-

- * System Diagram
- * Output Expression
- * Truth Table
- * Functional Definition

2) DESIGN PROCESS:-

- * System Diagram
- * Truth Table
- * K-Map (FOR DESIGNING)
- * Output Expression
- * Schematic diagram



FORMULA OF COMMUNICATION