



VDD_A; OUT: Internal regulated supply 2.7-3.4V for analog circuit
VDD_RF;OUT: Internal regulated supply 2.7-5V, normally connected to VDD_PA (pin 4)

VIN, SUP:External supply input to chip (2.7 V to 5.5 V)

VDD_PA; INP: Supply for PA; normally connected externally to VDD_RF (pin 3)
VDD_I/O; INP: Pin 16 is used to supply the I/O interface pins (I/O_0 to I/O_7), IRQ, SYS_CLK, and DATA_CLK pins of the reader.

VDD_X; OUT:Internally regulated supply (2.7 V to 3.4 V) for digital circuit and external devices

EN: If EN=1 (EN2 dont care) system clock is configured in 0x09
EN2: Selection of Power down mode
MOD: direct access to the transmit modulator and receive signal
ASK/OOK: in Register 0x09, enable or disable. 1: OOK, 0=ASK







