

**Q1) Explain Binary Addition and Subtraction with rules and examples.**

**Answer:**

Binary arithmetic is mathematical operation performed on binary numbers.

**Binary Addition Rules:**

$$0 + 0 = 0$$

$$0 + 1 = 1$$

$$1 + 0 = 1$$

$$1 + 1 = 0 \text{ (carry 1)}$$

**Example:**

$$\begin{array}{r} 1011 \\ + 0101 \\ \hline \end{array}$$

$$\begin{array}{r} \\ \\ \\ \\ \hline 10000 \end{array}$$

**Binary Subtraction Rules:**

$$1 - 1 = 0$$

$$1 - 0 = 1$$

$$0 - 0 = 0$$

$$0 - 1 = 1 \text{ (borrow 1)}$$

**Example:**

$$\begin{array}{r} 1010 \\ - 0011 \\ \hline \end{array}$$

$$\begin{array}{r} \\ \\ \\ \\ \hline 0111 \end{array}$$

Binary addition and subtraction are performed inside ALU.

**Q2) Explain Binary Multiplication Algorithm with example.**

**Answer:**

Binary multiplication is based on:

$$0 \times \text{anything} = 0$$

$$1 \times \text{anything} = \text{same number}$$

It is performed using **shift-and-add algorithm**.

### **Example: Multiply $110 \times 101$**

$$\begin{array}{r} 110 \\ \times 101 \\ \hline 110 & (1 \times 110) \\ 000 & (0 \times 110 \text{ shifted left}) \\ + 110 & (1 \times 110 \text{ shifted twice}) \\ \hline 11110 \end{array}$$

Final result = **11110<sub>2</sub>**

### **Q3) Explain Binary Division Algorithm with example.**

**Answer:**

Binary division works like decimal division —  
Repeated subtraction + shifting.

Example:

$$1101_2 \div 10_2 (13 \div 2)$$

Outcome:

$$\text{Quotient} = 110_2$$

$$\text{Remainder} = 1$$

Division in processors uses **Restoring/Non-Restoring division algorithms**.

### **Q4) Explain Floating Point Number Representation and Arithmetic.**

**Answer:**

Floating point format stores **real/fractional numbers**.

General representation:

$$\text{Number} = \text{Mantissa} \times \text{Base}^{\text{Exponent}}$$

Mantissa holds significant bits

Exponent defines scaling

Example:

$$1.101 \times 2^3$$

Floating point operations include:

- Exponent alignment
- Addition/subtraction
- Normalization

Used in scientific computing, graphics, physics simulations.

### **Q5) What is Decimal Arithmetic Unit? Explain.**

**Answer:**

Decimal arithmetic is performed on **decimal digits**, mainly using BCD (Binary Coded Decimal).

Decimal arithmetic unit performs:

- Decimal Addition
- Decimal Subtraction
- Decimal Multiplication
- Decimal Division

Used in accounting, finance, calculators and business apps.

### **Q6) Explain Half-Adder and Full-Adder with functions.**

**Answer:**

#### **Half-Adder**

Performs addition of two 1-bit numbers

Inputs: A, B

- Sum = A XOR B
- Carry = A AND B

### **Full-Adder**

Adds three bits (A, B, Carry-in)

Outputs: Sum, Carry

- Sum = A XOR B XOR Cin
- Carry = AB + Cin(A XOR B)

→ Both circuits are part of ALU.

### **Rules of Binary Arithmetic**

Binary arithmetic 0 aur 1 par based hota hai. Basic rules:

#### **Addition Rules:**

$$0 + 0 = 0$$

$$0 + 1 = 1$$

$$1 + 0 = 1$$

$$1 + 1 = 0 \text{ (carry 1)}$$

#### **Subtraction Rules:**

$$0 - 0 = 0$$

$$1 - 0 = 1$$

$$1 - 1 = 0$$

$$0 - 1 = 1 \text{ (borrow 1)}$$

#### **Multiplication Rules:**

$$0 \times 0 = 0$$

$$0 \times 1 = 0$$

$$1 \times 0 = 0$$

$$1 \times 1 = 1$$

#### **Division Rules:**

$$0 \div 1 = 0$$

$$1 \div 1 = 1$$

$$1 \div 0 = \text{undefined (not possible)}$$

**Perform binary subtraction: 1011 – 0010**

1011 - 0010

----- 1001

Answer = **1001<sub>2</sub>**

### **Define Floating Point Representation**

Floating-point representation fractional numbers ko represent karne ka binary format hai.

Iska form hota hai:

$$N = \text{Mantissa} \times 2^{\text{Exponent}}$$

Isse large aur small decimal values store ki ja sakti hai.

### **Explain concept of BCD**

**BCD (Binary Coded Decimal)** ek technique hai jisme **har decimal digit 4 bits me represent hoti hai**.

Example:

5 = 0101

9 = 1001

Iska use calculator, digital clocks, and financial systems me hota hai.

### **Differentiate Half Adder & Full Adder**

<b>Half Adder</b>	<b>Full Adder</b>
2 inputs: A, B	3 inputs: A, B, Carry-in
Sum & Carry generate karta hai	Sum & Carry-out generate karta hai
Previous carry handle nahi karta	Previous carry process karta hai

## **Explain Shift-and-Add Multiplication**

Binary multiplication ko repeated shifting & adding se perform kiya jata hai.

Steps:

1. LSB check karo.
2. Agar bit 1 hai toh multiplicand add karo.
3. Result ko left shift karo.
4. Next bit process karo.

Ye CPU multipliers me use hota hai.

## **What is decimal arithmetic?**

Decimal arithmetic normal base-10 number system me operations perform karta hai — 0–9 digits ke sath addition, subtraction, multiplication, division.

## **Short answers**

### **What is binary arithmetic?**

Arithmetic operations using base-2 number system (0 & 1).

### **What are addition rules?**

Binary addition ke rules:

$0+0=0$ ,  $0+1=1$ ,  $1+0=1$ ,  $1+1=0$  with carry 1.

### **What is a floating-point number?**

Fractional value jisko mantissa aur exponent ke form me represent kiya jata hai.

### **Write floating point formula.**

$$N = \text{Mantissa} \times \text{Base}^{\text{Exponent}}$$

Binary me Base = 2 hota hai.

### **Write binary of $1 \div 0$**

**Undefined** (division by zero is not possible)

### **What is decimal arithmetic unit?**

Processor ka woh part jo base-10 operations perform karta hai — jaise calculator circuits.

**Write sum & carry expression for half adder.**

**Sum =  $A \oplus B$**

**Carry =  $A \cdot B$**

**Full adder sum formula kya hota hai?**

**Sum =  $A \oplus B \oplus Cin$**

Carry formula (extra info):

$$Cout = AB + BCin + ACin$$

### **MCQ TYPE QUESTIONS**

$$1 + 1 = ? \rightarrow 0 \text{ (carry 1)}$$

$$1 \times 101 = 101$$

Floating point = Mantissa  $\times$  Base $^{\wedge}$ Exponent

Half-adder carry =  $A \cdot B$

Full adder sum =  $A \oplus B \oplus Cin$