



Software Safety Requirements and Architecture Lane Assistance

Document Version:1.0



Document history

Date	Version	Editor	Description
27/11/2018	1.0	Atul Kumar	Draft version
29/11/2018	2.0	Atul Kumar	Adding more feature

Table of Contents

Document history

Table of Contents

Purpose

Inputs to the Software Requirements and Architecture Document

Technical safety requirements

Refined Architecture Diagram from the Technical Safety Concept

Software Requirements

Refined Architecture Diagram

Purpose

The purpose of the software safety requirements and architecture document is to identify new detailed requirements and allocate these software requirements to component level diagrams for the lane assistance functional safety project as pertain to the potential malfunctions of the electrical and electronic systems as defined by ISO 26262 standard or tailored version as per organization.

Inputs to the Software Requirements and Architecture Document

Technical safety requirements

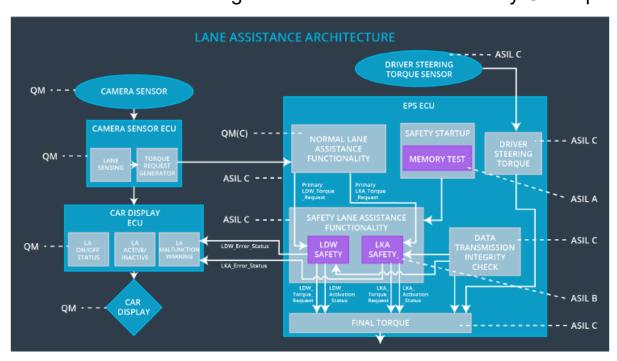
Technical Safety Requirements related to Functional Safety Requirement 01-01 are:

ID	Technical Safety Requirement	A S I L	Fault Tolerant Time Interval	Architecture Allocation	Safe State
Technical Safety Requirement 01	The LDW safety component shall ensure that the amplitude of the 'LDW_Torque_Request' sent to the 'Final electronic power steering Torque' component is below 'Max_Torque_Amplitude	С	50ms	LDW Safety block	Set lane departure warning torque to zero
Technical Safety Requirement 02	As soon as the LDW function deactivates the LDW feature, the 'LDW Safety' software block shall send a signal to the car display ECU to turn on a warning light.	С	50ms	LDW Safety block	Set lane departure warning torque to zero
Technical Safety Requirement 03	As soon as a failure is detected by the LDW function, it shall deactivate the LDW feature and the 'LDW_Torque_Request' shall be set to zero.	С	50ms	LDW Safety block	Set lane departure warning torque to zero
Technical Safety Requirement 04	The validity and integrity of the data transmission for 'LDW_Torque_Request' signal shall be ensured	Α	50ms	LDW Safety block	Set lane departure warning torque to zero
Technical Safety Requirement 05	Memory test shall be conducted at startup of the EPS ECU to check for any faults in memory.	Α	Ignition Cycle	Data Transmission Integrity Check	Set lane departure warning torque to zero

Technical Safety Requirement 06	The LDW safety component shall ensure that the fequency of the 'LDW_Torque_Request' sent to the 'Final electronic power steering Torque' component is below 'Max_Torque_Fequency	С	50ms	LDW Safety block	Set lane departure warning torque to zero
Technical Safety Requirement 07	The LKA safety component shall ensure that the duration of the lane keeping assistance torque applied is less than Max_Duration.	С	500ms	LKA Safety block	Set lane keeping assistance torque to zero
Technical Safety Requirement 08	As soon as the LKA function deactivates the LKA feature, the 'LKA Safety' software block shall send a signal to the car display ECU to turn on a warning light	С	500ms	LKA Safety block	Set lane keeping assistance torque to zero
Technical Safety Requirement 09	As soon as a failure is detected by the LKA function, it shall deactivate the LKA feature and the 'LKA_Torque_Request' shall be set to zero.	С	500ms	LKA Safety block	Set lane keeping assistance torque to zero
Technical Safety Requirement 10	The validity and integrity of the data transmission for 'LKA_Torque_Request' signal shall be ensured	С	500ms	LKA Safety block	Set lane keeping assistance torque to zero
Technical Safety Requirement 11	Memory test shall be conducted at start Up of the EPS ECU to check for any faults in memory.	Α	Ignition Cycle	Data Transmission Integrity Check	Set lane keeping assistance torque to zero
Technical Safety Requirement 12	The LKA safety component shall ensure that the loss of camera sensor torque request transmission will deactivate the LKA feature and the	С	500ms	LKA Safety block	Set lane keeping assistance torque to zero

'LKA_Torque_Request' shall be set to zero.		

Refined Architecture Diagram from the Technical Safety Concept



Software Requirements

Lane Departure Warning (LDW) Amplitude Malfunction Software Requirements:

[Instructions: Fill in the software safety requirements for the LDW amplitude malfunction technical safety requirements. We have provided the associated technical safety requirements. Hint: The software safety requirements were discussed in the text from the software and hardware lesson.

OPTIONAL:

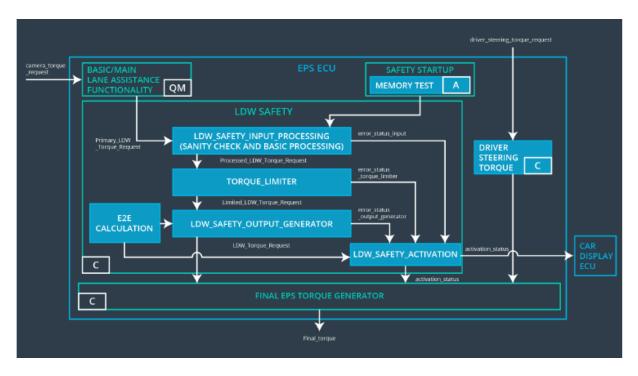
CHALLENGE ONE

Develop software safety requirements for the Lane Departure Warning (LDW) frequency function and modify the system architecture as needed.

CHALLENGE TWO

Develop software safety requirements for the Lane Keeping Assistance (LKA) function and modify the system architecture as needed.

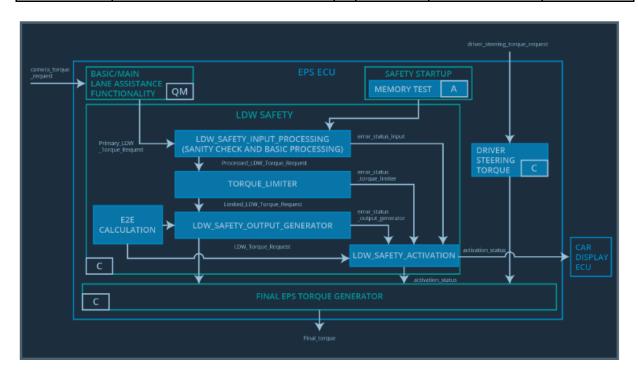
ID	Technical Safety Requirement	ASIL	Fault Tolerant Time Interval	Allocation to Architecture	Safe State
Technical Safety Requirement 01	The LDW safety component shall ensure that the amplitude of the LDW_Torque_Request sent to the Final Electronic Power Steering Torque component is below Max_Torque_Amplitude	С	50ms	LDW Safety block	Set lane departure warning torque to zero



ID	Software Safety Requirement	A % L	Allocation Software Elements	Safe State
Software Safety Requirement	The input signal "Primary_LDW_Torq_Req" shall be read and pre-processed to	С	LDW_SAFETY_INPUT_P ROCESSING	Not Applicable

01-01	determine the torque request coming from the "Basic/Main LAFunctionality" SW Component. Signal"processed_LDW_Torq_R eq"shall be generated at the end of the processing			
Software Safety Requirement 01-02	In case the "processed_LDW_Torq_Req" signal has a value greater than "Max_Torque_Ampltide_LD W"(maximum allowed safe torque), the torque signal "limited_LDW_Torq_Req" shall be set to 0, else "limited_LDW_Torq_Req" shall take the value of "processed_LDW_Torq_Req"	С	TORQUE_LIMITER	"limited_LDW_T orq_Req" = 0(Nm=Newtonm eter)
Software Safety Requirement 01-03	The "limited_LDW_Torq_Req" shall be transformed into a signal "LDW_Torq_Req" which is suitable to be transmitted outside of the LDW Safety component ("LDW Safety") to the "Final EPS Torque" component.	С	LDW_SAFETY_OUTPUT _GENERATOR	LDW_Torq_Req = 0 (Nm)

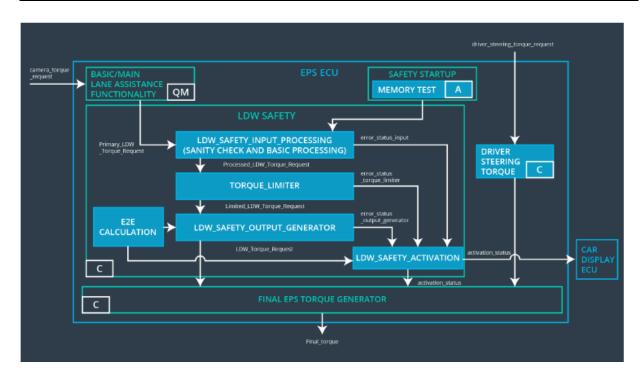
ID	Technical Safety Requirement	A S I L	Fault Tolerant Time Interval	Allocation to Architecture	Safe State
Technical Safety Requirement 02	As soon as a failure is detected by the LDW function, it shall deactivate the LDW feature and the LDW_Torque_Request shall be set to zero	С	50ms	LDW Safety block	Set lane departure warning torque to zero



ID	Software Safety Requirement	A S I L	Allocation Software Elements	Safe State
Software Safety Requirement 02-01	Each of the SW elements shall output a signal to indicate any error which is detected by the element. Error signal = error_status_input(LDW_SAFET Y_INPUT_PROCESSING), error_status_torque_limiter(TOR	С	All	Not Applicable

	C All N/A 8 QUE_LIMITER), error_status_output_gen(LDW_ SAFETY_OUTPUT_GENERAT OR)			
Software Safety Requirement 02-02	A software element shall evaluate the error status of all the other software elements and in case any 1 of them indicates an error, it shall deactivate theLDW feature("activation_status"=0)	С	LDW_SAFETY _ACTIVATION	Activation_statu s = 0 (LDW function deactivated)
	In case of no errors from the software elements, the status of the LDW feature shall be set to activated ("activation_status"=1)		LDW_SAFETY _ACTIVATION	Not Applicable
	In case an error is detected by any of the software elements, it shall set the value of its corresponding torque to 0 so that "LDW_Torq_Req" is set to 0		ALL	LDW_Torq_Re q= 0 (Nm)
	Once the LDW functionality has been deactivated, it shall stay deactivated till the time the ignition is switched from off to on again.	С	LDW_SAFETY _ACTIVATION	Activation_statu s = 0 (LDW function deactivated)

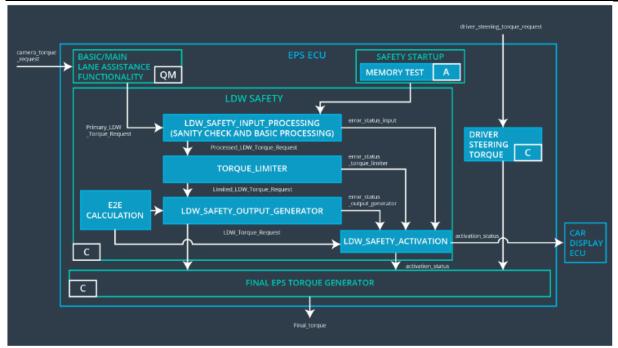
ID	Technical Safety Requirement	A S I L	Fault Tolerant Time Interval	Allocation to Architecture	Safe State
Technical Safety Requirement 03	As soon as the LDW function deactivates the LDW feature, the LDW Safety software block shall send a signal to the car display ECU to turn on a warning light	С	50ms	LDW Safety block	Set lane departure warning torque to zero



ID	Software Safety Requirement	A S I L	Allocation Software Elements	Safe State
Software Safety Requirement 03-01	When the LDW function is deactivated (activation_status set to 0), the activation_status shall be sent to the car displayECU.	С	LDW_SAFET Y_ACTIVATIO N, CarDisplay ECU	Not Applicable

Technical Safety Requirement A Fault Allocation to Safe State

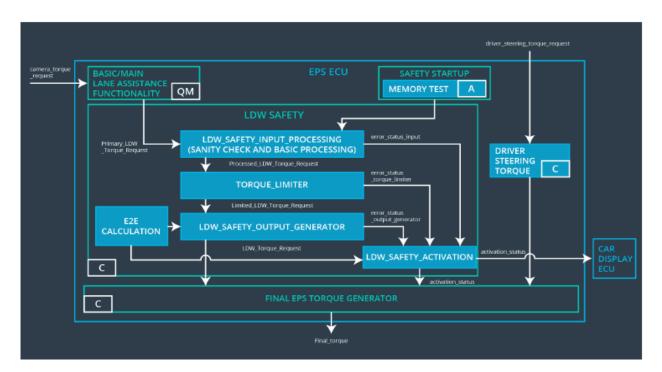
		SIL	Tolerant Time Interval	Architecture	
Technical Safety Requirement 04	As soon as the LDW function deactivates the LDW feature, the LDW Safety software block shall send a signal to the car display ECU to turn on a warning light	С	50ms	LDW Safety block	Set lane departure warning torque to zero



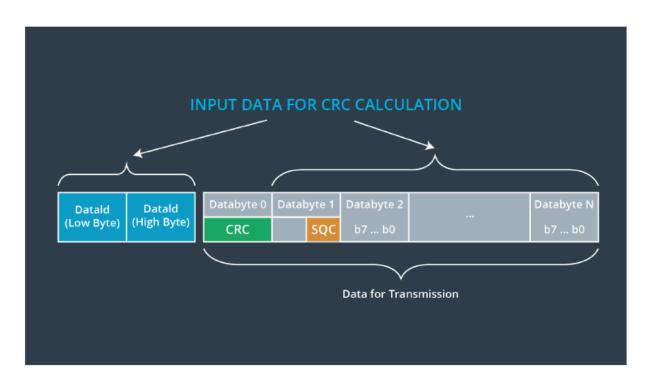
ID	Software Safety Requirement	A S I L	Allocation Software Elements	Safe State
Software Safety Requirement 04-01	When the LDW function is deactivated (activation_status set to 0), the activation_status shall be sent to the car displayECU	С	LDW_SAFET Y_ACTIVATIO N,CarDisplay ECU	Not Applicable

ID Technical Safety Requirement			Allocation to Architecture	Safe State
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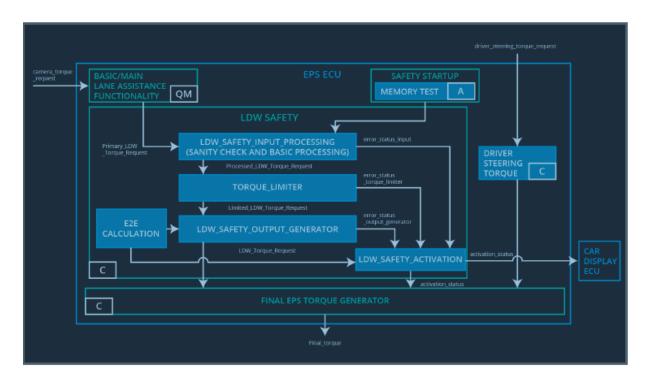
Technical Safety Requirement 04-01	The validity and integrity of the data transmission for LDW_Torque_Request signal shall be ensured	С	50ms	Data Transmission Integrity Check	Not Applicable
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ID	Software Safety Requirement	A S I L		Safe State
Software Safety Requirement 04-01	Any data to be transmitted outside of the LDW Safety component ("LDW Safety")including "LDW_Torque_Req" and "activation_status" shall be protected by an End2End protection mechanism	С	E2ECalc	LDW_Torq_Req=0(Nm)
	The E2E protection protocol shall contain and attach the control data: alive counter (SQC) and CRC to the data to be transmitted.	С	E2ECalc	LDW_Torq_Req=0(Nm)



ID	Technical Safety Requirement	ASIL	Fault Tolerant Time Interval	Allocation to Architecture	Safe State
Technical Safety Requirement 05	Memory test shall be conducted at startup of the EPS ECU to check for any faults in memory	Α	Ignition Cycle	Data Transmission Integrity Check	Set lane departure warning torque to zero

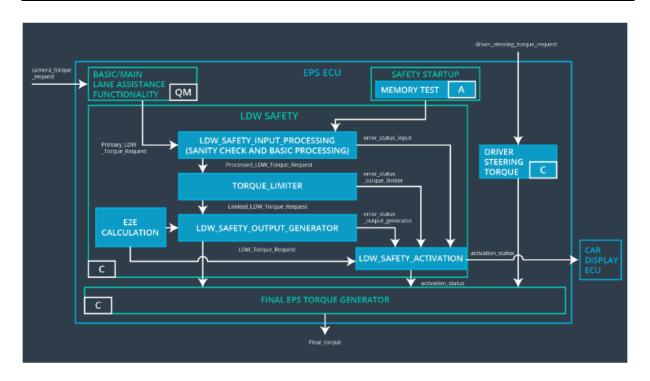


ID	Software Safety Requirement	A S I L	Allocation Software Elements	Safe State
Software Safety Requirement 04-01	A CRC verification check over the software code in the Flash memory shall be done every time the ignition is switched from off to on to check for any corruption of content.	Α	MEMORYTES T	Activation_status=0
Software Safety Requirement 04-01	Standard RAM tests to check the data bus, address bus and device integrity shall be done every time the ignition is switched from off to on.	A	MEMORYTES T	Activation_status=0
Software Safety Requirement 04-01	The test result of the RAM or Flash memory shall be indicated to the LDW_Safety component via the "test_status" signal	Α	MEMORYTES T	Activation_status=0
Software Safety Requirement 04-01	In case any fault is indicated via the "test_status" signal the INPUT_LDW_PROCESSING shall set an error on error_status_input (=1) so that	Α	LDW_SAFET Y_PROCESSI NG	Activation_status=0

the LDW functionality is deactivated and the LDWTorque is set to 0		

Lane Departure Warning(LDW) Frequency Malfunction Software Requirements:

ID	Technical Safety Requirement	A S I L	Fault Tolerant Time Interval	Allocation to Architecture	Safe State
Technical Safety Requirement 05	The LDW safety component shall ensure that the frequency of the LDW_Torque_Request sent to the Final Electronic Power Steering Torque component is below Max_Torque_Frequency	С	50 ms	LDW safety block	Set lane departure warning torque to zero



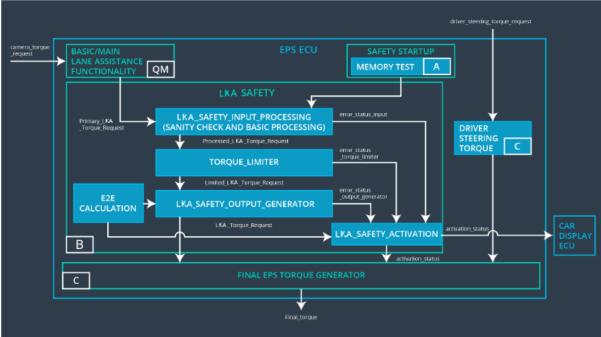
ID Software Safety Requirement	A Allocation Safe State Software I Elements	
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Software Safety Requirement 04-01	The input signal "Primary_LDW_Torq_Req" shall be read and pre-processed to determine the torque request coming from the "Basic/Main LA Functionality" SW Component. Signal "processed_LDW_Torq_R eq"shall be generated at the end of the processing.	С	LDW_SAFET Y_INPUT_PR OCESSING	Not Applicable
Software Safety Requirement 04-02	In case the "processed_LDW_Torq_Req" signal has a value greater than "Max_Torque_Frequency_L DW" (maximum allowed safe torque), the torque signal "limited_LDW_Torq_Req" shall be set to 0, else "limited_LDW_Torq_Req" shall take the value of "processed_LDW_Torq_Req".	С	TORQUE_LIM ITER	"limited_LDW_T orq_Req" = 0(Nm=Newtonmeter)
Software Safety Requirement 04-03	The "limited_LDW_Torq_Req"shall be transformed into a signal "LDW_Torq_Req" whichis suitable to be transmitted outside of the LDW Safetycomponent ("LDW Safety") to the "Final EPS Torque"component.	С	LDW_SAFET Y_OUTPUT _GENERATO R	LDW_Torq_Req = 0 (Nm)

Lane Keeping Assistance(LKA) sensor Malfunction Software Requirements:

ID	Technical Safety Requirement	A S I L	Fault Tolerant Time Interval	Allocation to Architecture	Safe State
Technical Safety Requirement 05	The LKA safety component shall ensure that the loss of camera sensor torque request transmission will deactivate the LKA feature and the 'LKA_Torque_Request' shall be	В	50ms	LDW Safety block	Set Lane keeping assistance torque to zero

set to zero.



ID	Software Safety Requirement	ASIL		Safe State
Software Safety Requirement 05-01	The input signal "Primary_LKA_Torq_Req" shall be read and pre-processed to determine the torque request coming from the "Basic/Main LA Functionality" SW Component. Signal "processed_LKA_Torq_R e q" shall be generated at the end of the processing.	В	LKA_SAFETY _INPUT_PR OCESSING	Not Applicable
Software Safety Requirement 05-02	In case the "processed_LKA_Torq_Req" signal has an invalid Alive counter (SQC), the camera sensor ECU is no longer detecting lane lines, the torque signal "limited_LKA_Torq_Req" shall be set to 0, else"limited_LKA_Torq_Req" shall take the value of	В	TORQUE_LIM ITER	"limited_LKA_To rq_Req" = 0(Nm=Newtonmeter)

	"processed_LKA_Torq_Req".			
Software Safety Requirement 05-03	The "limited_LKA_Torq_Req"shall be transformed into a signal "LKA_Torq_Req" whichis suitable to be transmitted outside of the LKA Safetycomponent (" LKA Safety") to the "Final EPS Torque"component.	В	LKA_SAFETY _OUTPUT_ GENERATOR	LKA_Torq_Req= 0 (Nm)

Refined Architecture Diagram

