


Microprocessor 8085 - [Code Editor]

File Edit View Converter Options Simulate Help



Microprocessor 8085 Simulator Software Kit 1.0

Ln 8, Col 6

TStates : 227 | MCycles : 64 | Time of Execution : 227 %s

Flags : S Z AC P OV

General Regs.:

A	B	C	D	E	H	L	M	SP	PC
06	03	06	00	00	00	00	00	0000	8000

MVI A 12h
MVI B 03h
MVI C 00h

START: CMP B
JC NEXT
INR C
SUB B
JNZ START

NEXT: STA 5555h
MOV A C
STA 5554h
HLT

Starting Address : 8000

Program Divide

Comments :

Address	Data
5550	00
5551	00
5552	00
5553	00
5554	06
5555	00
5556	00
5557	00

Address	Data
8000	3E
8001	12
8002	06
8003	03
8004	0E
8005	00
8006	B8
8007	DA
8008	0F
8009	80
800A	0C
800B	90
800C	C2
800D	06
800E	80
800F	32
8010	55
8011	55

1 Data Transfer Group :

MOV	MVI	LDA	STA
LHLD	SHLD	LXI	XCHG
LDAX	STAX	IN	OUT

2 Arithmetic Group :

ADD	ADI	ADC	ACI	DAA
SUB	SUI	SBB	SBI	DAD
INR	DCR	INX	DCX	

3 Logical Group :

ANA	ANI	XRA	XRI	CMA
ORA	ORI	CMP	CFI	CMC
RLC	RRC	RAL	RAR	STC

4 Branch and Machine Control Group

JMP	JL	CALL	CH
RET	RL	EI	DI
PCHL	RST	RIM	SIM
XTHL	SPHL	NOP	HLT

☒ Auto Insert

Instruction Information :

Jump unconditionally