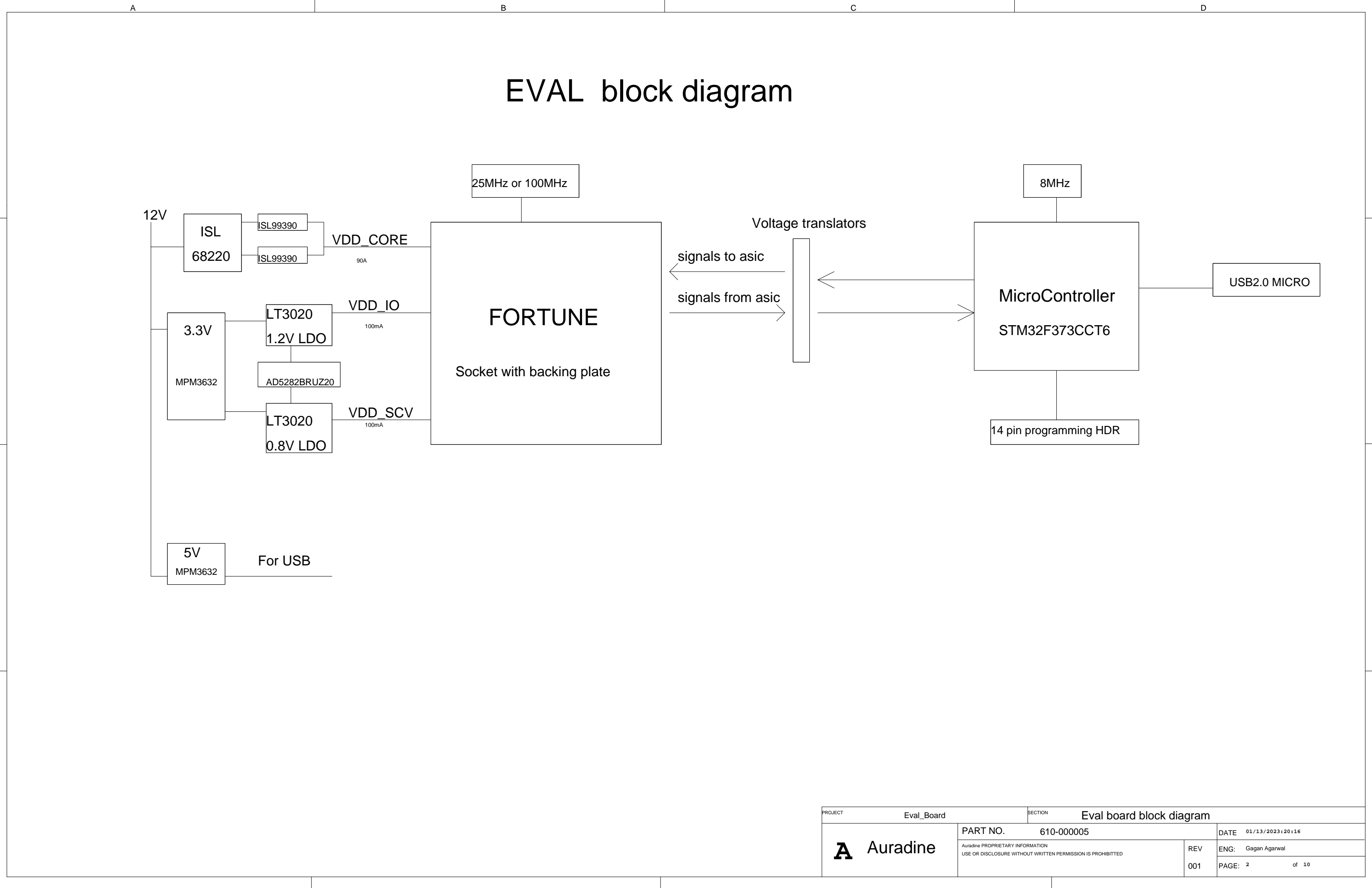


EVAL board Index

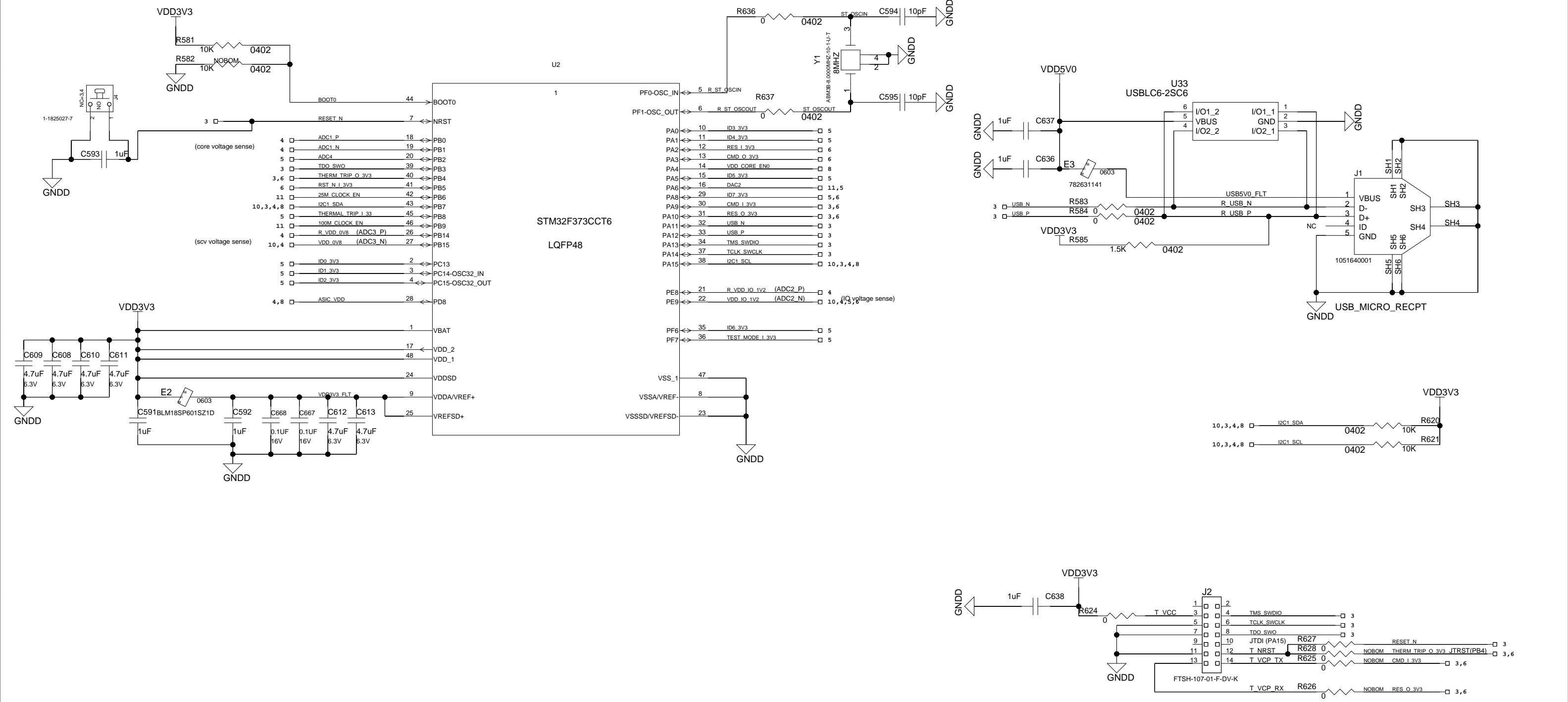
- 1. Index
- 2. Block_diagram
- 3. STM32F3 Microcontroller
- 4. ASIC and LDO
- 5. clock and voltage translator
- 6. voltage translators
- 7. VDD5V0 and VDD3V3
- 8. VDD_CORE_CTRL
- 9. VDD_CORE_PHASE[0:1]
- 10. temp sensor
- 11. clock
- 12. opamp_vss_negative 2V5

PROJECT		Eval_Board		SECTION		EVAL board Index			
<div><div>A</div><div>Auradine</div></div>		PART NO.		610-000005		DATE 01/13/2023:21:31			
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						001		PAGE: 1 of 10	



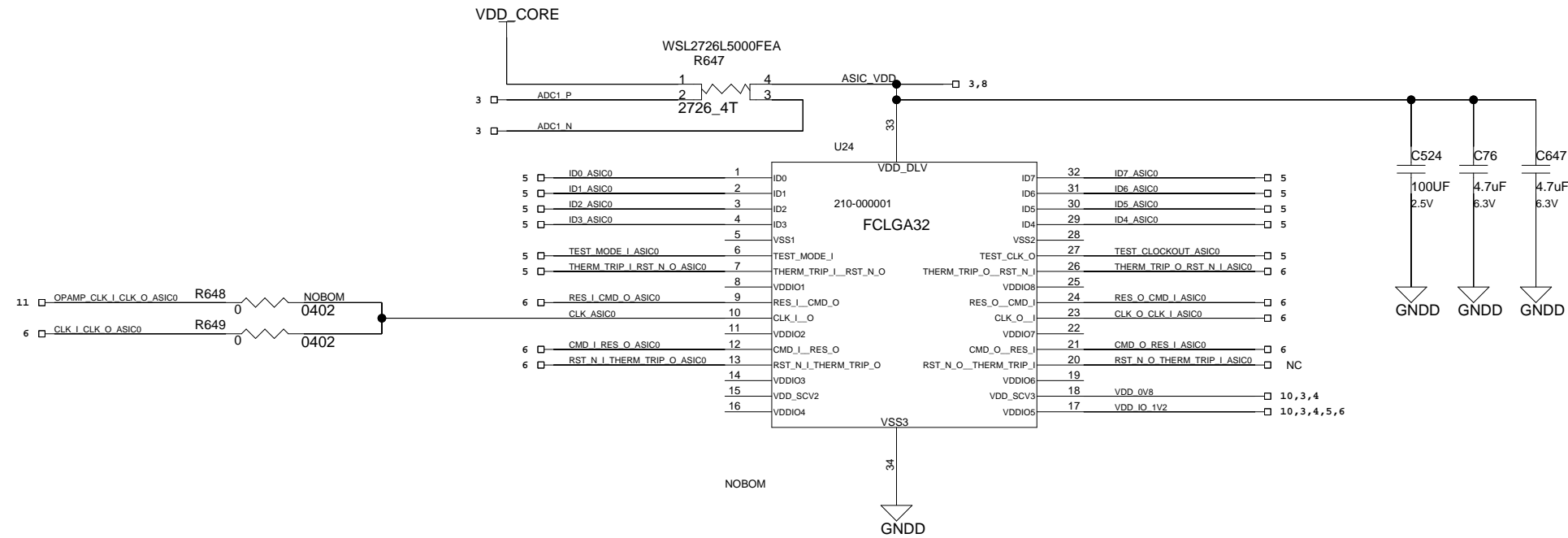
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			PAGE: 2 of 10

STM32F3 Microcontroller

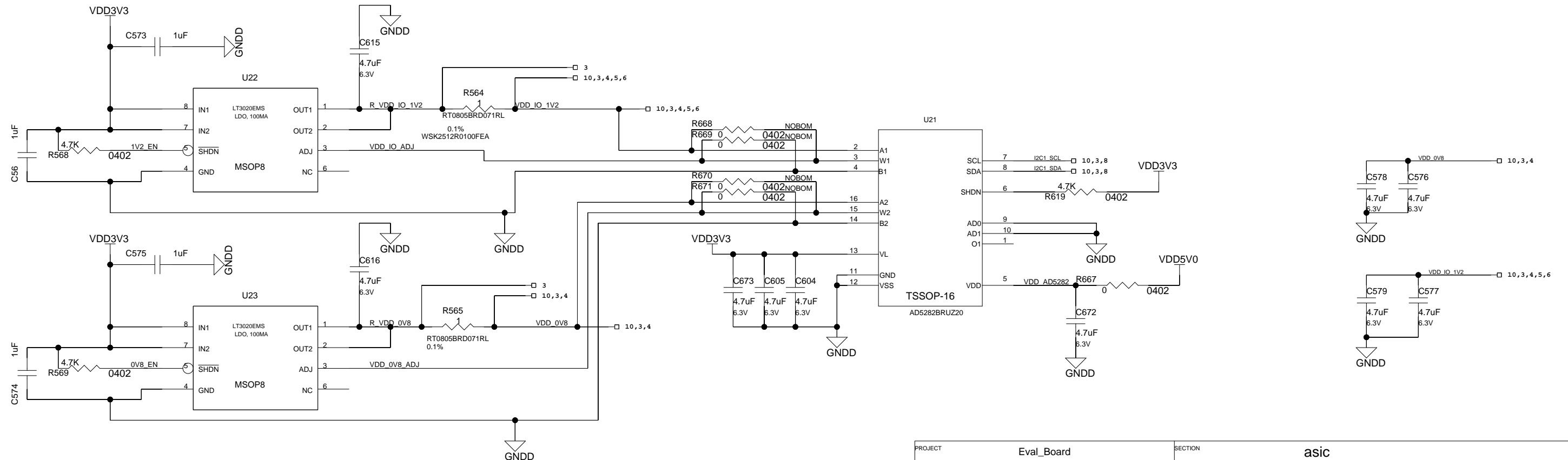


PROJECT		SECTION	
Eval_Board		STM32F3 Microcontroller	
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	001		PAGE: 3 of 10

Auradine Asic

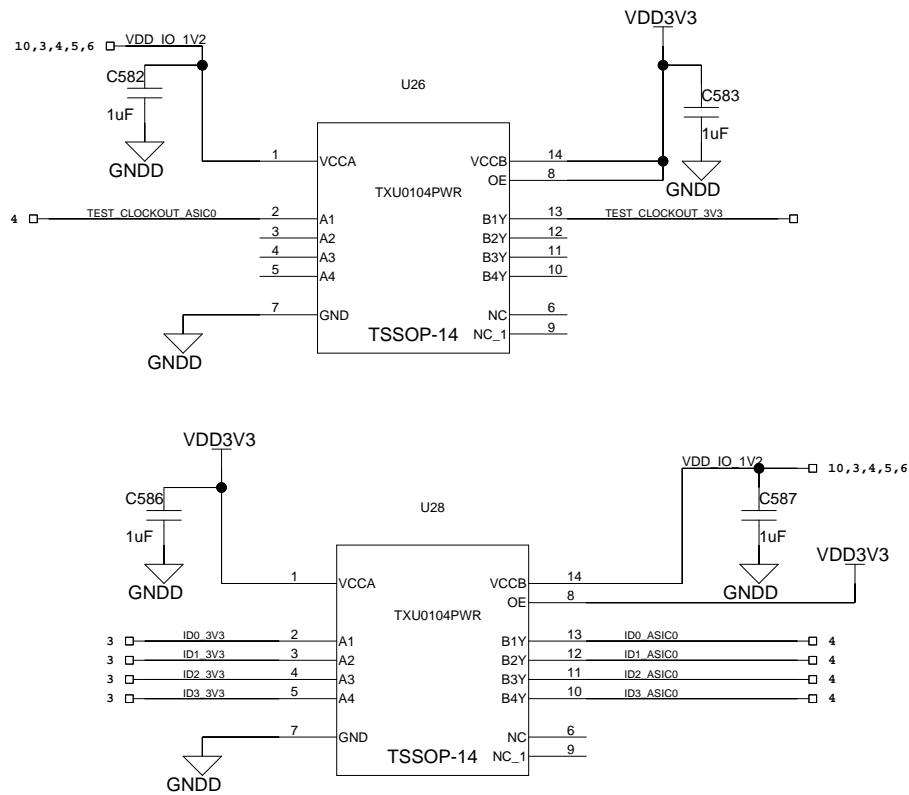
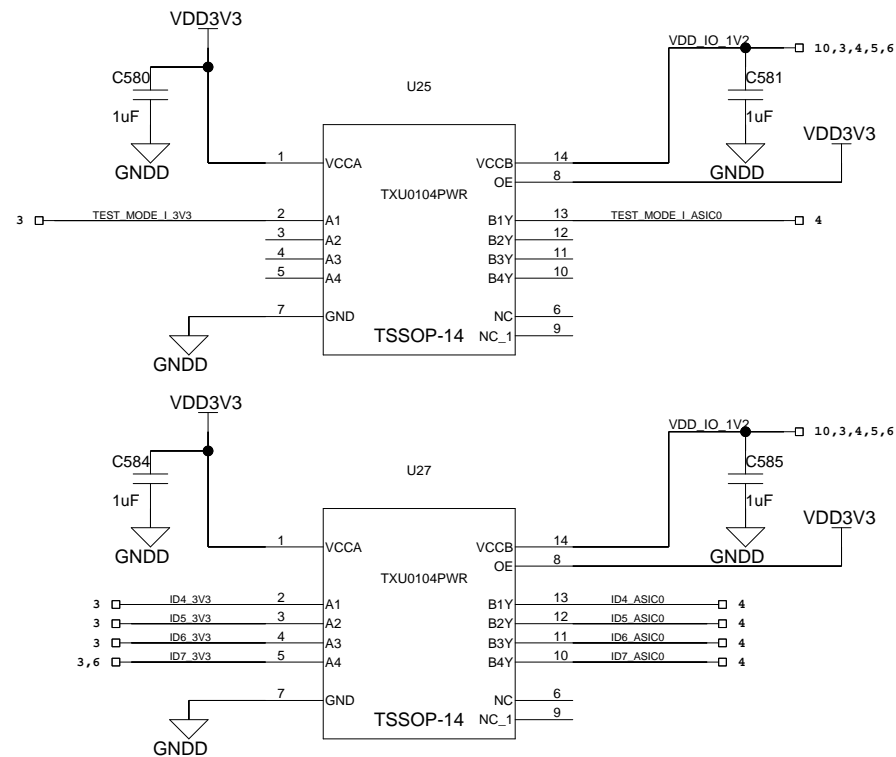


I/O and SCV power for ASIC 1.2V/0.8V

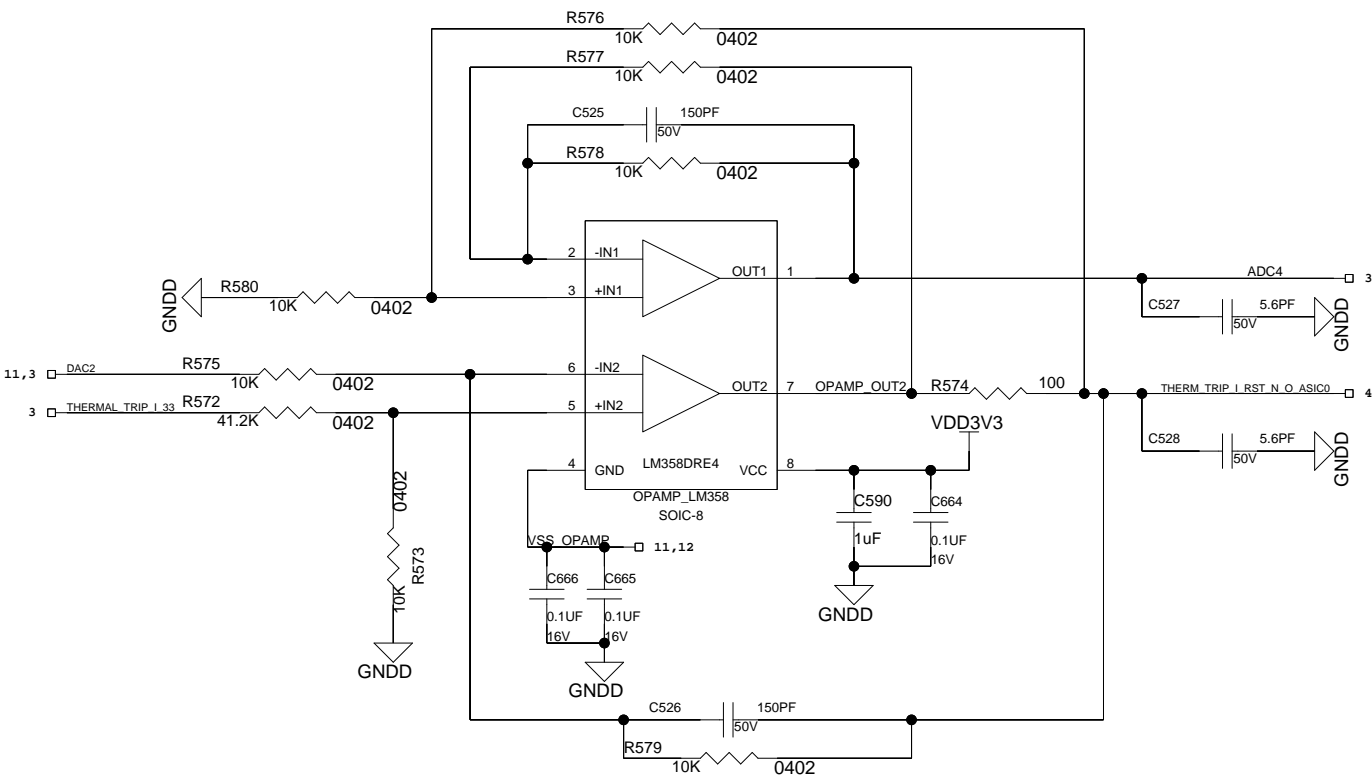


PROJECT		Eval_Board		SECTION		asic	
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						PAGE: 4 of 10	

Voltage translators

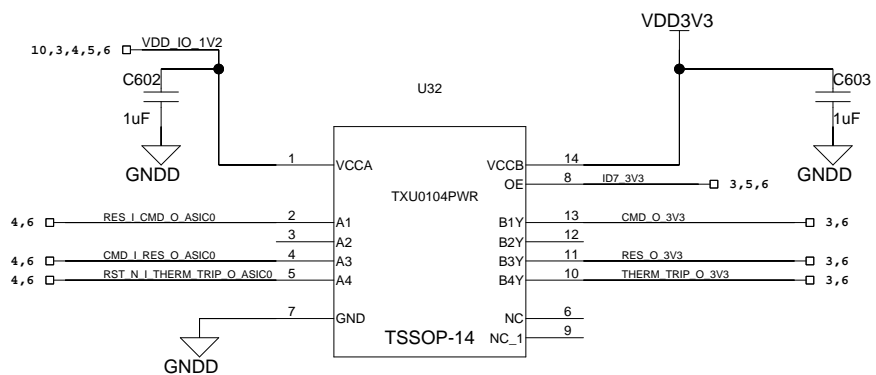
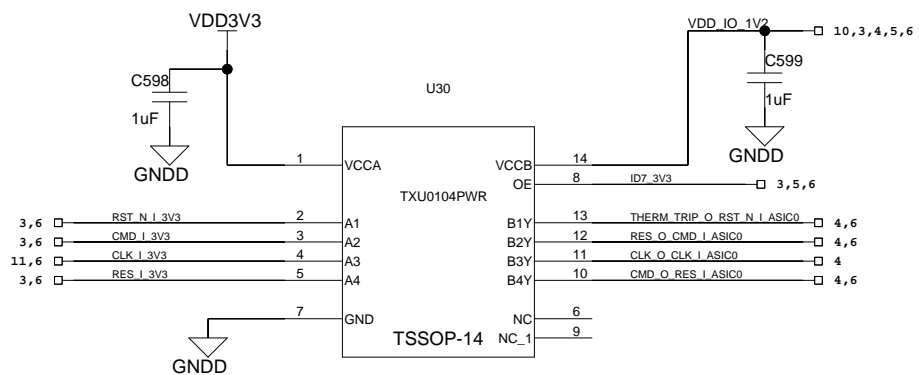
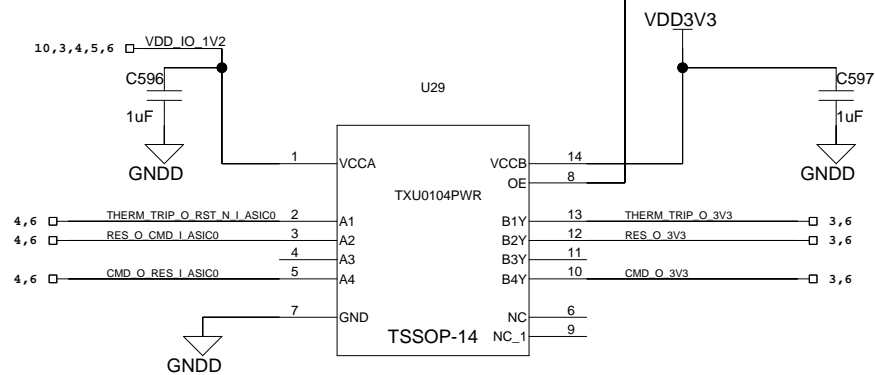
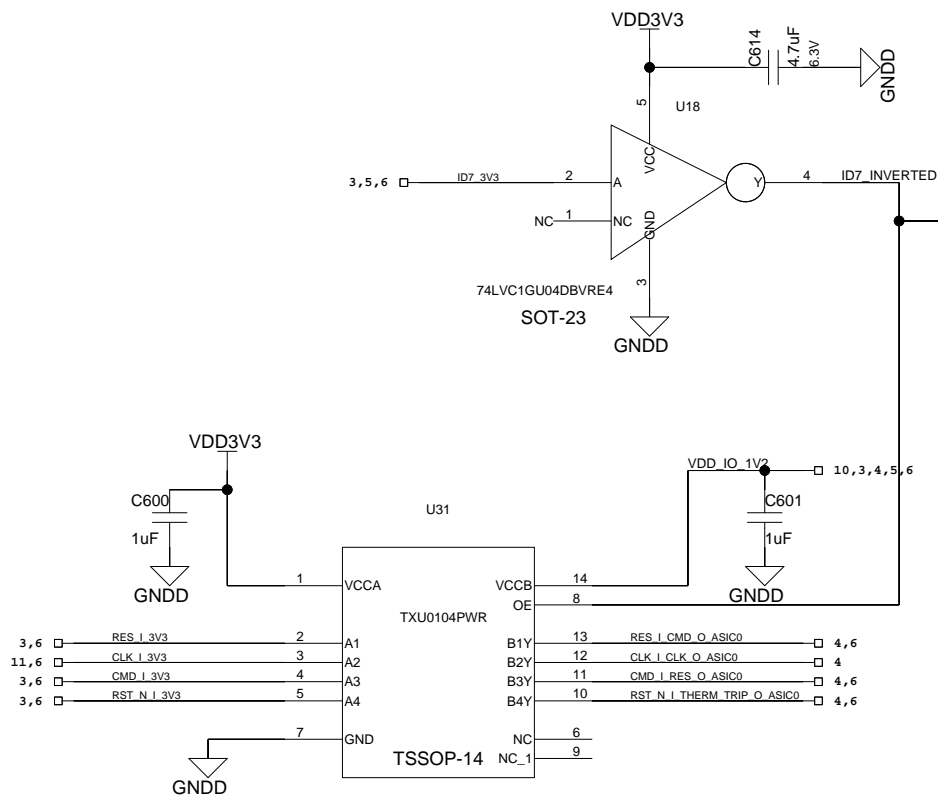


current sensing on the Thermal_trip line



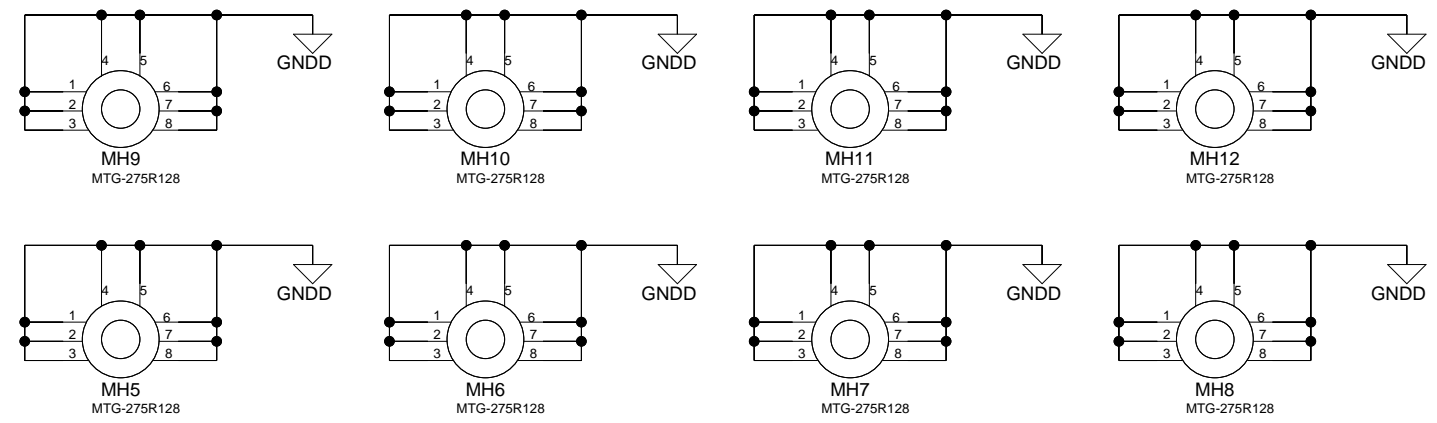
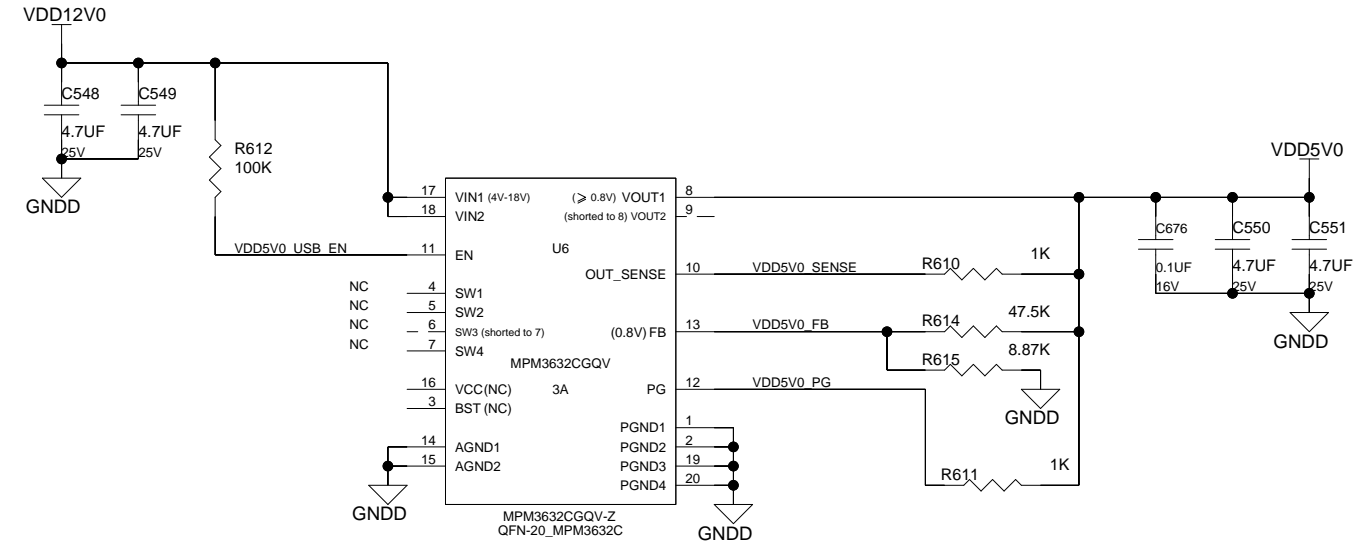
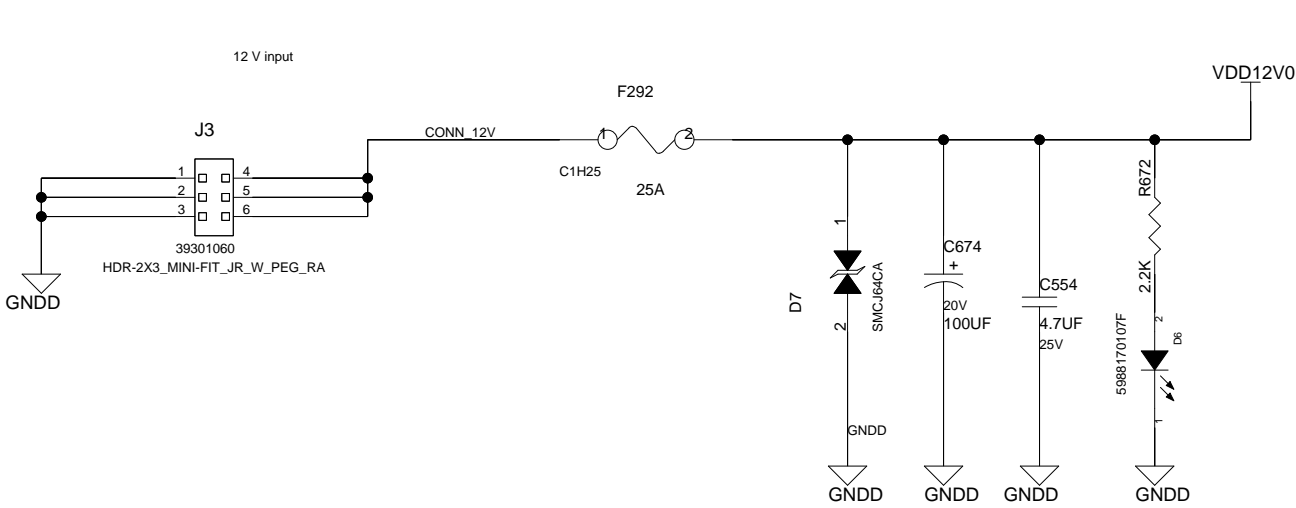
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A Auradine		PART NO.		610-000005		DATE	
		Auradine PROPRIETARY INFORMATION USE OR DISCLOSURE WITHOUT WRITTEN PERMISSION IS PROHIBITED		REV		ENG: Gagan Agarwal	
		001		PAGE: 5		of 10	

Voltage translators

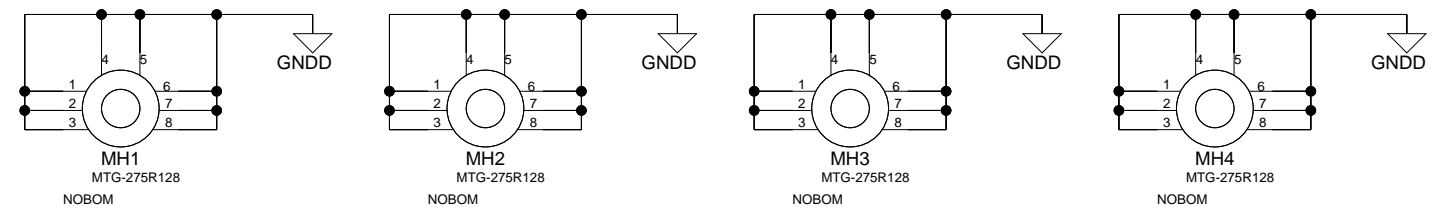


PROJECT		Eval_Board		SECTION		voltage translator	
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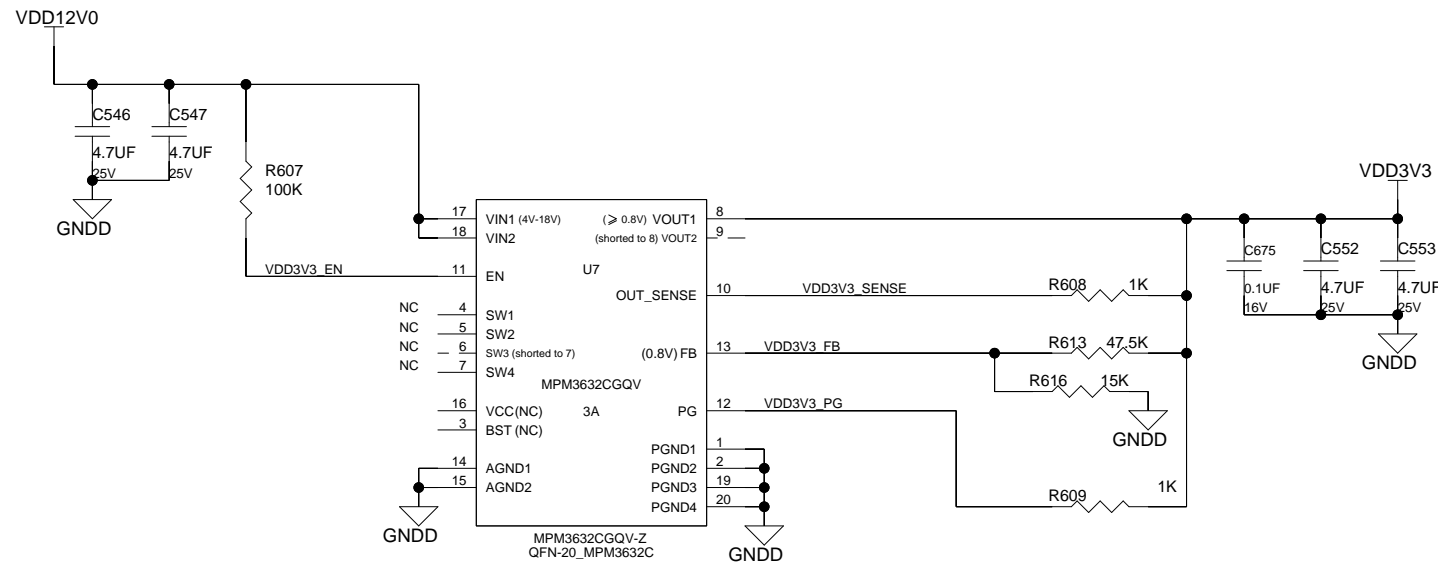
Board power 12V, 5V, 3.3V



4 Mounting holes with pem nut

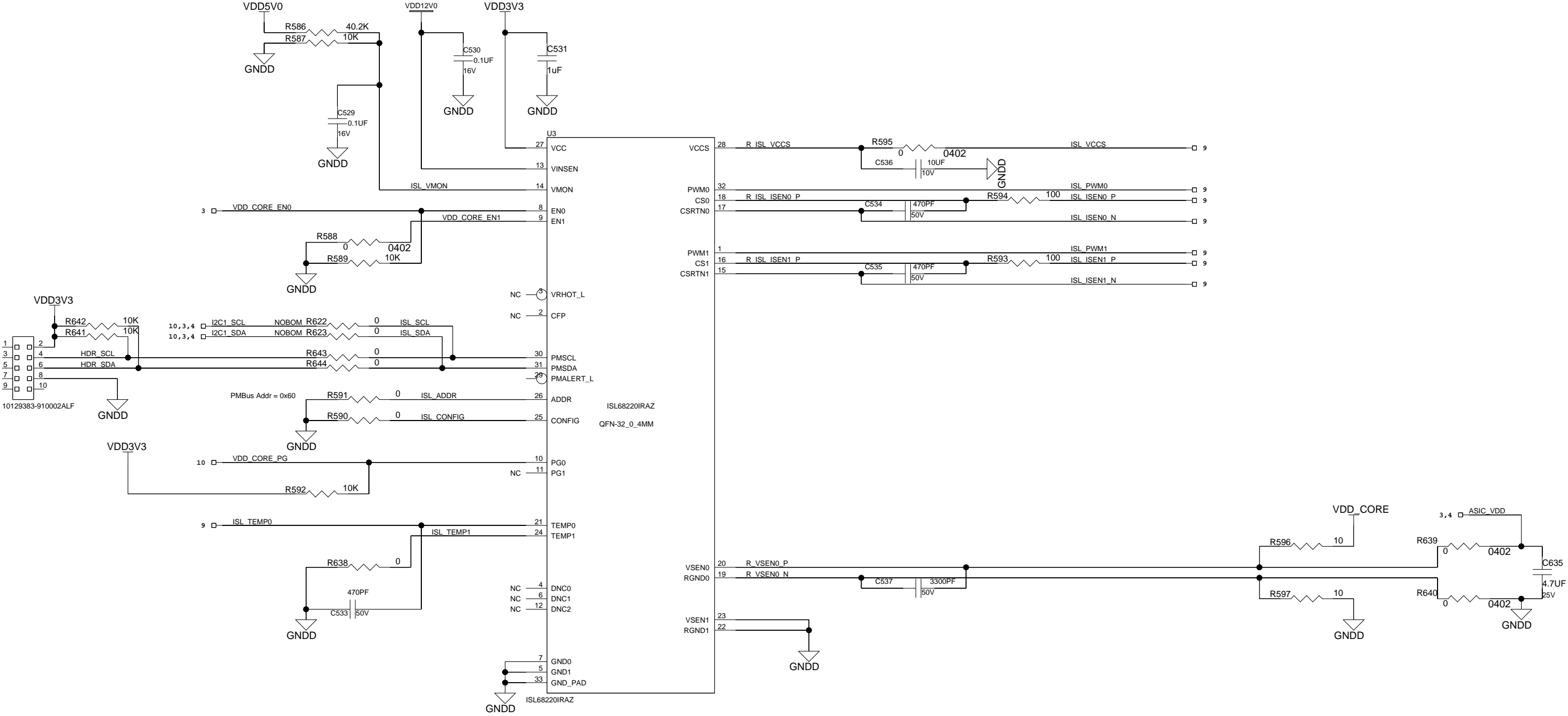


4 Mounting holes with pem nut



PROJECT		Eval_Board		SECTION		VDD5v0 and VDD3v3	
A Auradine		PART NO.		610-000005		DATE	
		Auradine PROPRIETARY INFORMATION USE OR DISCLOSURE WITHOUT WRITTEN PERMISSION IS PROHIBITED		REV		ENG: Gagan Agarwal	
		001		PAGE: 7		of 10	

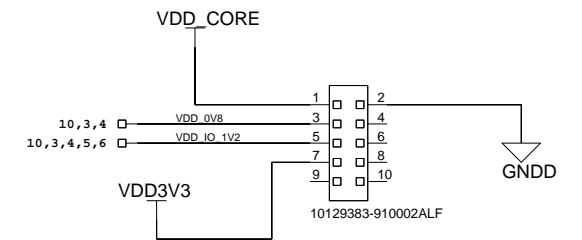
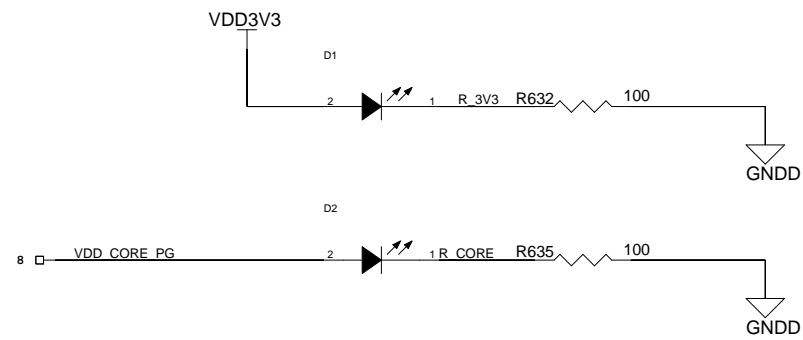
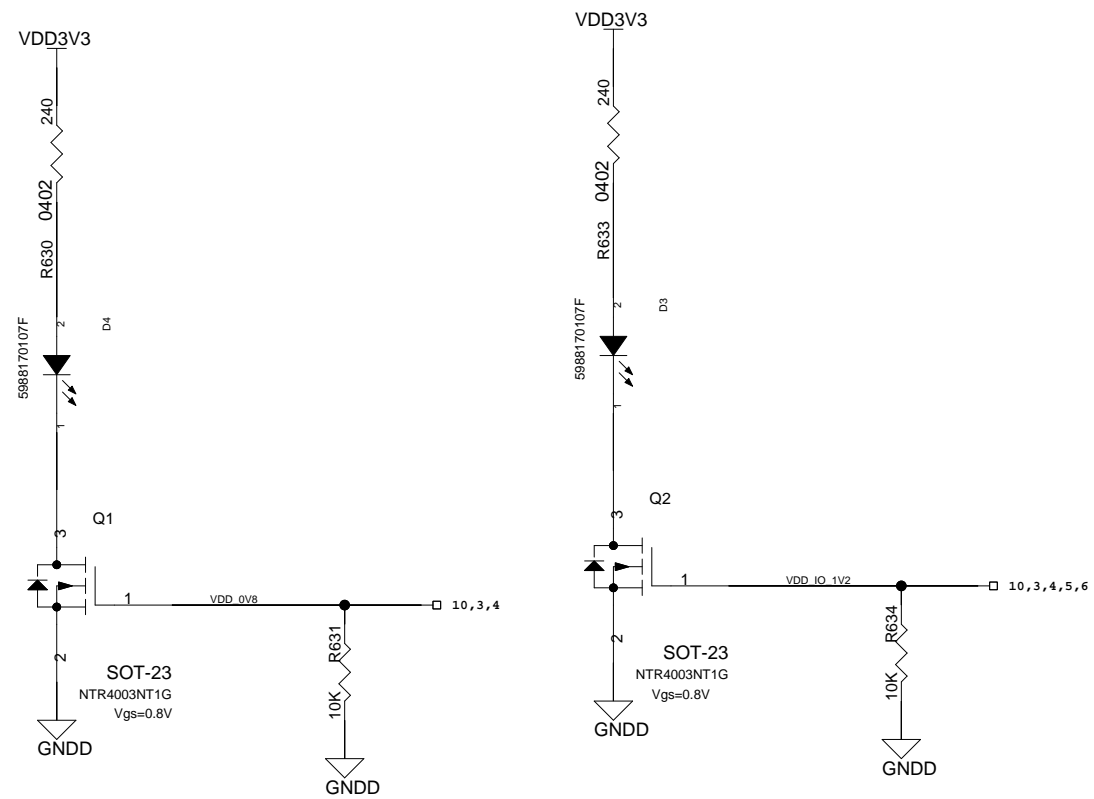
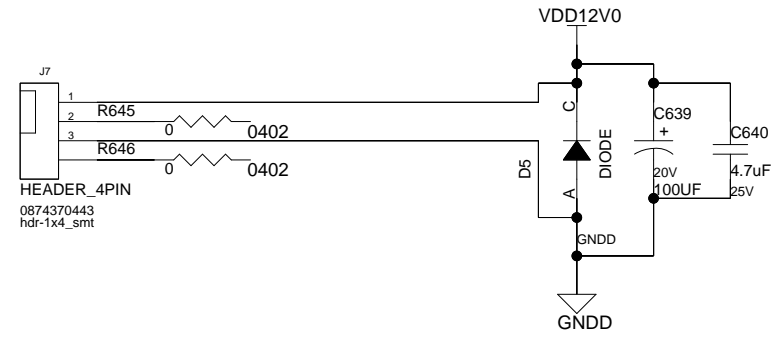
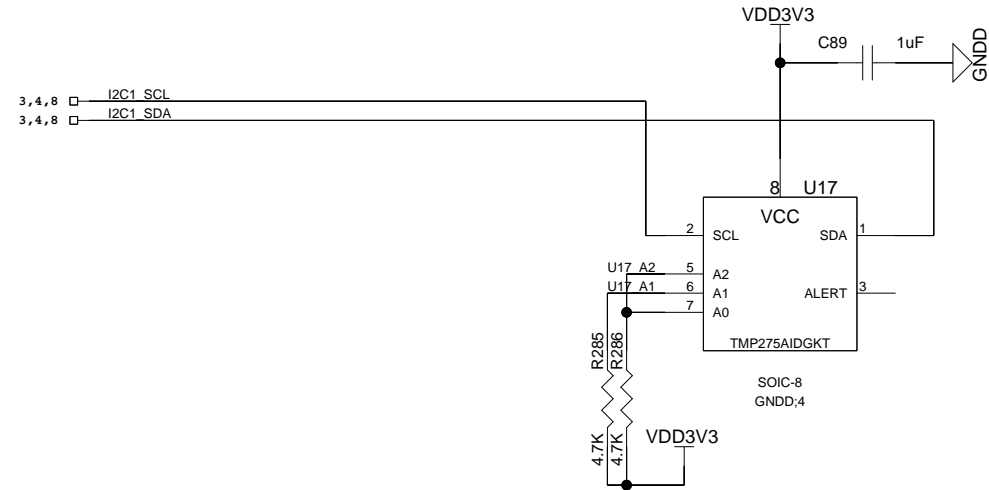
CORE power - ISL 68220




PROJECT		Eval_Board		SECTION		Renesas ISL68220	
A Auradine		PART NO.		610-000005		DATE	
		Auradine PROPRIETARY INFORMATION USE OR DISCLOSURE WITHOUT WRITTEN PERMISSION IS PROHIBITED		REV		ENG: Gagan Agarwal	
		001		PAGE: 8		of 10	

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4



PROJECT		Eval_Board		SECTION		Temp sensor	
 Auradine		PART NO. 610-000005				DATE 01/14/2023:18:45	
		Auradine PROPRIETARY INFORMATION USE OR DISCLOSURE WITHOUT WRITTEN PERMISSION IS PROHIBITED				REV ENG: Gagan Agarwal	
						001 PAGE: 10 of 10	

clock

voltage shift option on the clock line

