Software Construction

Makefile.simple

Simple makefile

```
game : main.o graphics.o world.o

____gcc -o game main.o graphics.o world.o

main.o : main.c graphics.h world.h
____gcc -c main.c

graphics.o : graphics.c world.h
____gcc -c graphics.c

world.o : world.c world.h
____gcc -c world.c

clean:
____rm -f game main.o graphics.o world.o
```

make0.pl

Simple Perl implementation of "make".

It parses makefile rules and stores them in 2 hashes.

Building is done with a recursive function.

```
$makefile_name = "Makefile";
if (@ARGV >= 2 && $ARGV[0] eq "-f") {
  <u>shift @ARGV;</u>
  $makefile_name = shift @ARGV;
parse_makefile($makefile_name);
push @ARGV, $first_target if !@ARGV;
build($_) foreach @ARGV;
exit 0;
sub parse_makefile {
<u>my ($file) = @_;</u>
 open MAKEFILE, $file or die "Can not open $file: $!";
  while (<MAKEFILE>) {
     my ($target, $dependencies) = /(\S+)\s*:\s*(.*)/ or next;
   $first_target ||= $target;
     $dependencies{$target} = $dependencies;
  while (<MAKEFILE>) {
         <u>last if !/^\t/;</u>
    $build_command{$target} .= $_;
sub build {
<u>my ($target) = @_;</u>
my $build_command = $build_command{$target};
 die "*** No rule to make target $target\n" if !$build command && !-e $target;
return if !$build command;
  my $target build_needed = ! -e $target;
foreach $dependency (split /\s+/, $dependencies{$target}) {
      build($dependency);
    $target build needed | = -M $target > -M $dependency;
return if !$target_build_needed;
 print $build command;
  system $build command;
}.
```

Makefile.variables

Simple makefile with variables & a comment

make1.pl

Add a few lines of code to make 0.pl and we can handle variables and comments.

A good example of how easy some tasks are in Perl.

```
$makefile_name = "Makefile";
if (@ARGV >= 2 && $ARGV[0] eq "-f") {
  shift @ARGV;
  $makefile name = shift @ARGV;
parse_makefile($makefile_name);
push @ARGV, $first_target if !@ARGV;
build($_) foreach @ARGV;
exit 0;
sub parse makefile {
<u>my ($file) = @_;</u>
 open MAKEFILE, $file or die "Can not open $file: $!\n";
  while (<MAKEFILE>) {
  s/#.*//<u>;</u>
     s/\$\((\w+)\)/$variable{$1}||''/eg;
     if (/^\s*(\w+)\s*=\s*(.*)$/) {
         <u> $variable{$1} = $2;</u>
     <u>next;</u>
  my ($target, $dependencies) = /(\S+)\s*:\s*(.*)/ or next;
   <u> $first_target ||= $target;</u>
   $dependencies{$target} = $dependencies;
     while (<MAKEFILE>) {
  s/#.*//;
       <u>s/\$\((\w+)\)/$variable{$1}||''/eg;</u>
         last if !/^\t/;
         $build command{$target} .= $ ;
 ____}}
_____}<u>}</u>
sub build {
<u>my ($target) = @_;</u>
my $build_command = $build_command{$target};
  die "*** No rule to make target $target\n" if !$build command && !-e $target;
 return if !$build_command;
my $target_build_needed = ! -e $target;
 foreach $dependency (split /\s+/, $dependencies{$target}) {
      build($dependency);
     $target_build_needed | = -M $target > -M $dependency;
return if !$target_build_needed;
 print $build command;
 <u>system $build_command;</u>
}
```

Makefile.builtin variables

Simple makefile with builtin variables

Makefile.implicit

Simple makefile with builtin variables relying on implict rules

make2.pl

Add a few lines of code to make1.pl and we can handle some builtin variables and an implicit rule.

Another good example of how easy some tasks are in Perl.

```
$makefile name = "Makefile";
<u>if (@ARGV >= 2 && $ARGV[0] eq "-f")</u> {
  <u>shift @ARGV;</u>
  <u> $makefile_name = shift @ARGV;</u>
%variable = (CC => 'cc', CFLAGS => '');
parse_makefile($makefile_name);
push @ARGV, $first_target if !@ARGV;
build($_) foreach @ARGV;
exit 0;
sub parse makefile {
<u>my ($file) = @_;</u>
 open MAKEFILE, $file or die "Can not open $file: $!";
  while (<MAKEFILE>) {
 s/#.*//;
     <u>s/\$\((\w+)\)/$variable{$1}||''/eg;</u>
     <u>if (/^\s*(\w+)\s*=\s*(.*)$/)</u> {
         <u> $variable{$1} = $2;</u>
     <u>next;</u>
      my ($target, $dependencies) = /(\S+)\s*:\s*(.*)/ or next;
     $first_target = $target if !defined $first_target;
    $dependencies{$target} = $dependencies;
     while (<MAKEFILE>) {
  s/#.*//;
       <u>s/\$\((\w+)\)/$variable{$1}||''/eg;</u>
         <u>last if !/^\t/;</u>
         $build command{$target} .= $;
 ____}}.
_____}<u>}</u>
sub build {
<u>my ($target) = @_;</u>
my $build command = $build command{$target};
  <u>if (!\$build command && \$target =~ /(.*)\setminus.o/) {</u>
  $build_command = "$variable{CC} $variable{CFLAGS} -c \$< -o \$@\n";</pre>
die "*** No rule to make target $target\n" if !$build command && !-e $target;
return if !$build command;
my $target_build_needed = ! -e $target;
  foreach $dependency (split /\s+/, $dependencies($target)) {
<u>build($dependency);</u>
      $target_build_needed | = -M $target > -M $dependency;
return if !$target_build_needed;
my %builtin_variables;
$builtin_variables{'@'} = $target;
 $builtin variables{'^'} = $dependencies{$target};
($builtin_variables{'<'} = $dependencies{$target}) =~ s/\s.*//;</pre>
  $build_command =~ s/\$(.)/$builtin_variables{$1}||''/eg;
 print $build command;
   system $build command;
}
```

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