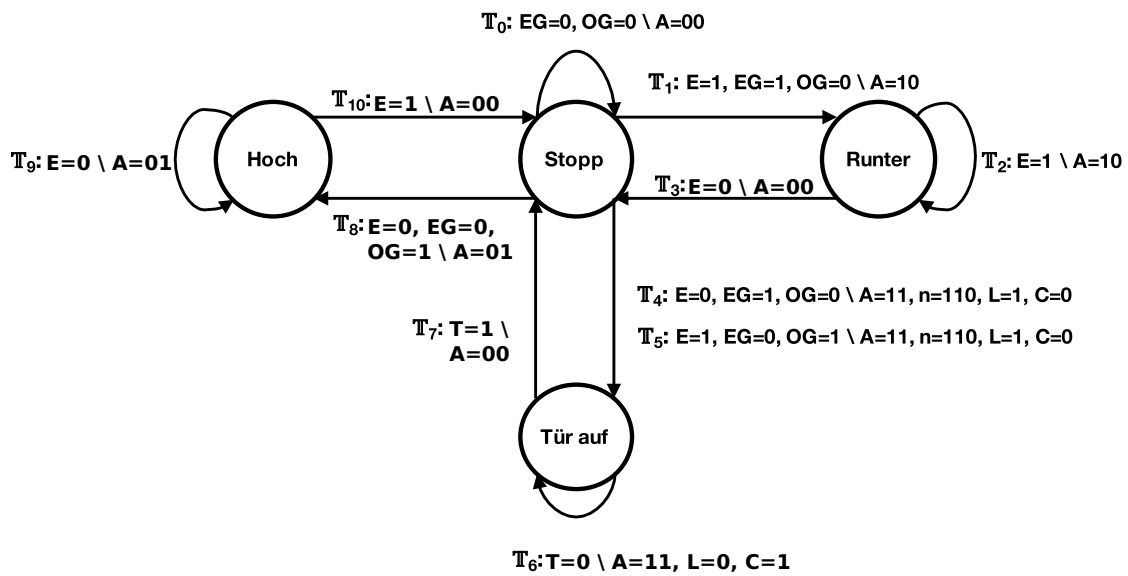


Grundlagen der Rechnerarchitektur

Tim Luchterhand, Paul Nykiel (Abgabegruppe 117)

28. Januar 2019

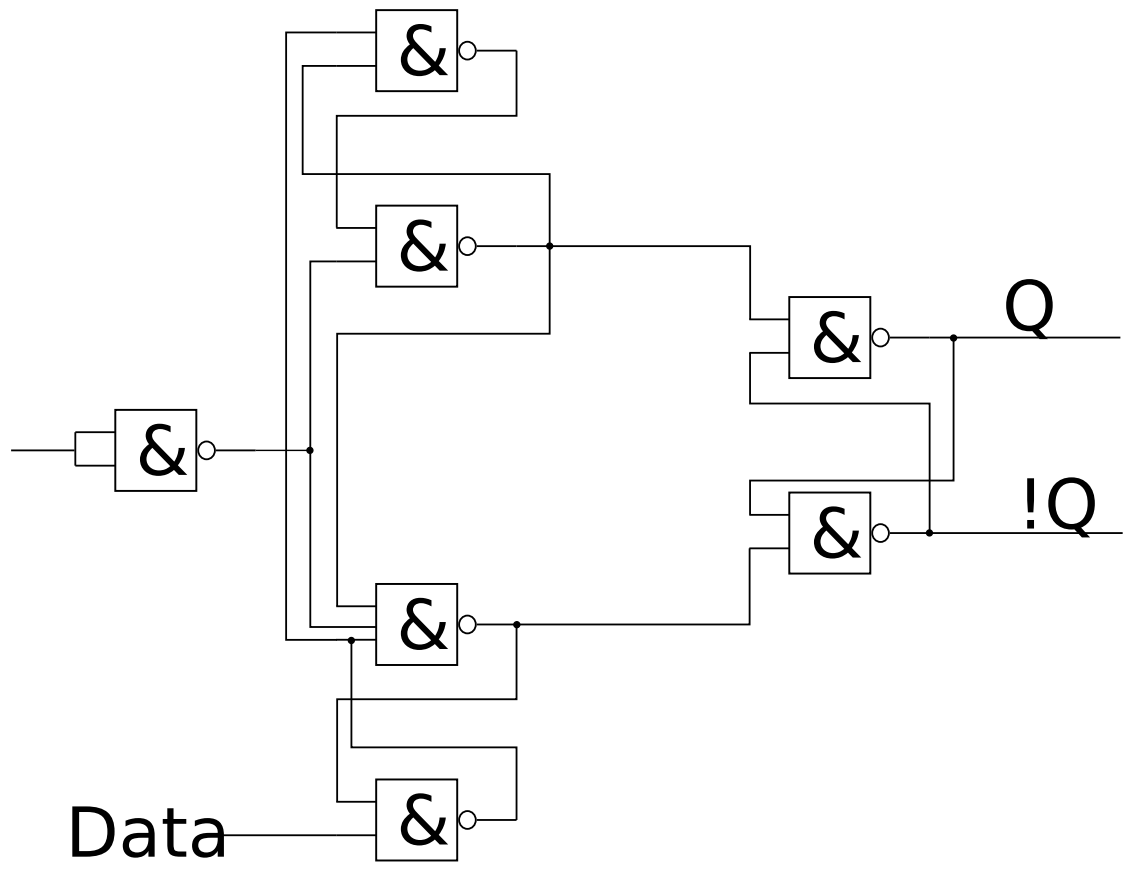
1



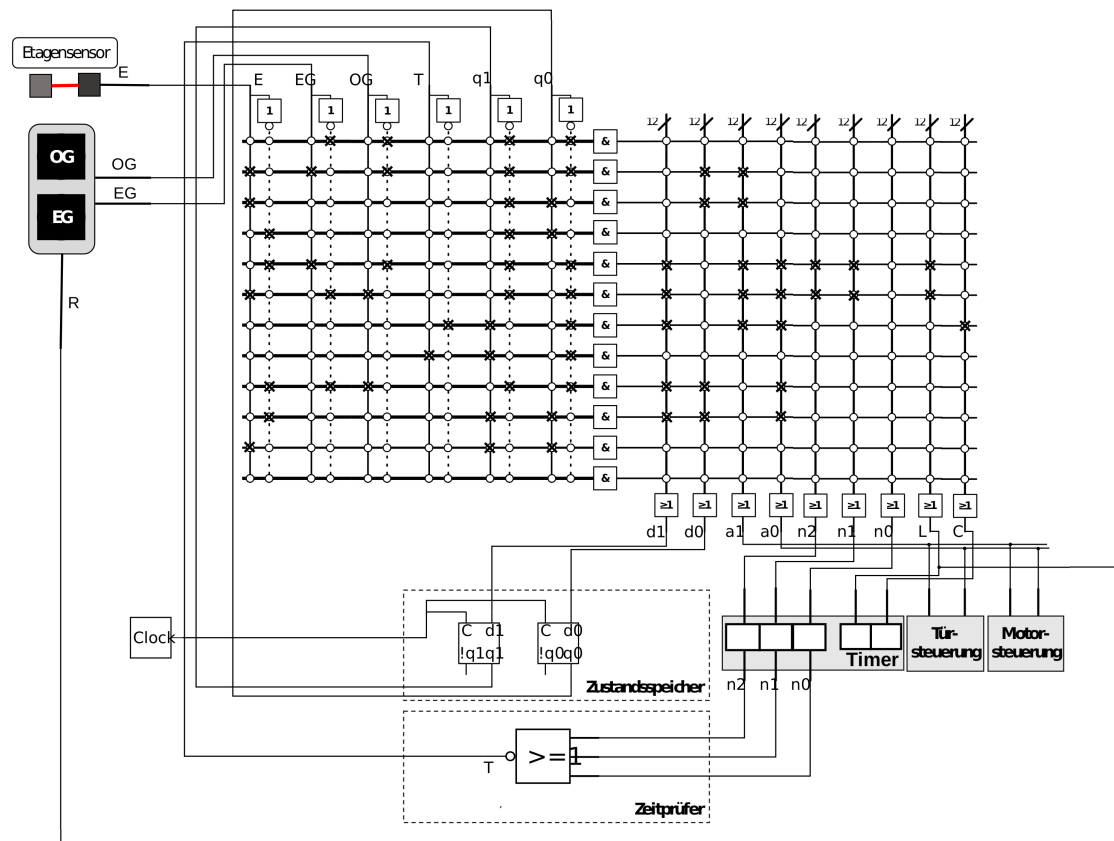
Transition T	Etagen- sensor E	Fahrstuhl- panel		Zeit- Prüfer T	Zustand		Folge- zustand	
		EG	OG		q ₁	q ₀	d ₁	d ₀
T ₀	d	0	0	d	0	0	0	0
T ₁	1	1	0	d	0	0	0	1
T ₂	1	d	d	d	0	1	0	1
T ₃	0	d	d	d	0	1	0	0
T ₄	0	1	0	d	0	0	1	0
T ₅	1	0	1	d	0	0	1	0
T ₆	d	d	d	0	1	0	1	0
T ₇	d	d	d	1	1	0	0	0
T ₈	0	0	1	d	0	0	1	1
T ₉	0	d	d	d	1	1	1	1
T ₁₀	1	d	d	d	1	1	0	0

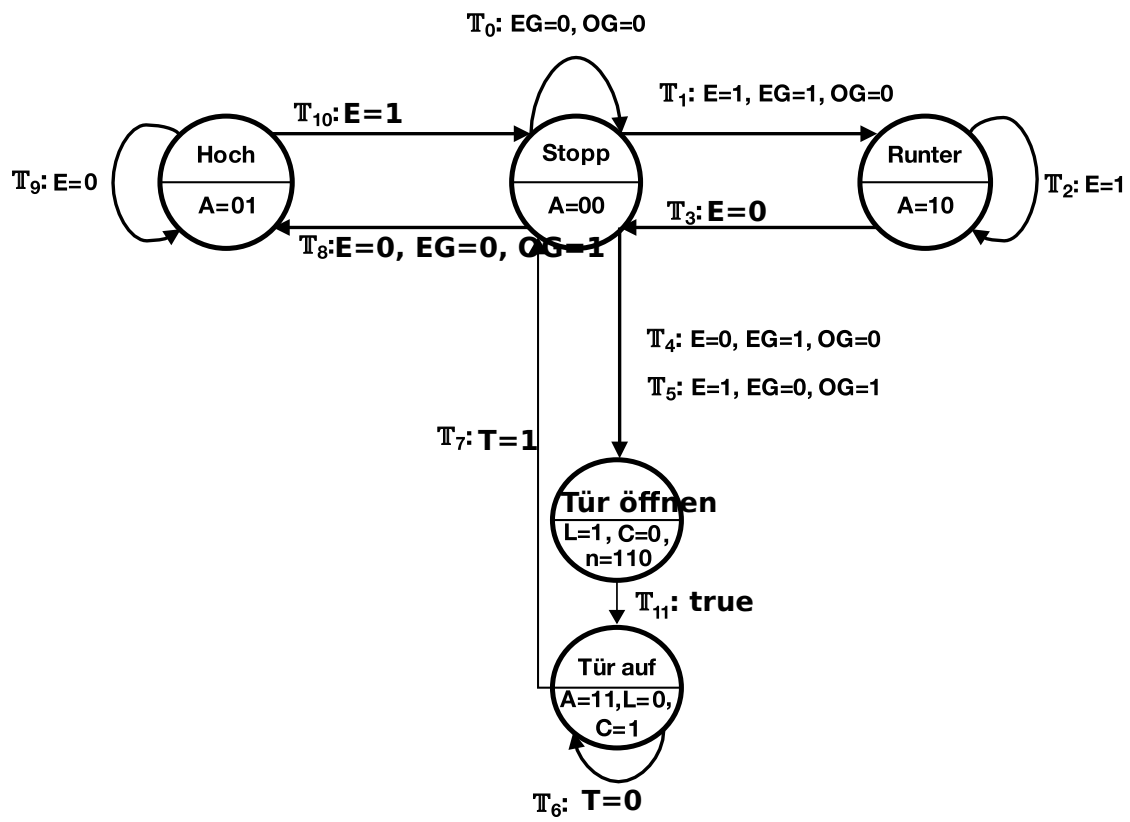
Ausgang		Timer					
a ₁	a ₀	n ₂	n ₁	n ₀	L	C	
0	0	d	d	d	d	d	
1	0	d	d	d	d	d	
1	0	d	d	d	d	d	
0	0	d	d	d	d	d	
1	1	1	1	0	1	0	
1	1	1	1	0	1	0	
1	1	d	d	d	0	1	
0	0	d	d	d	d	d	
0	1	d	d	d	d	d	
0	1	d	d	d	d	d	
0	0	d	d	d	d	d	

3



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Transition T	Etagen- sensor E	Fahrstuhl- panel		Zeit- Prüfer T	Zustand		Folge- zustand	
		EG	OG		q ₁	q ₀	d ₁	d ₀
T ₀	d	0	0	d	0	0	0	0
T ₁	1	1	0	d	0	0	0	1
T ₂	1	d	d	d	0	1	0	1
T ₃	0	d	d	d	0	1	0	0
T ₄	0	1	0	d	0	0	1	0
T ₅	1	0	1	d	0	0	1	0
T ₆	d	d	d	0	1	0	1	0
T ₇	d	d	d	1	1	0	0	0
T ₈	0	0	1	d	0	0	1	1
T ₉	0	d	d	d	1	1	1	1
T ₁₀	1	d	d	d	1	1	0	0

Ausgang		Timer					
a ₁	a ₀	n ₂	n ₁	n ₀	L	C	
0	0	d	d	d	d	d	
1	0	d	d	d	d	d	
1	0	d	d	d	d	d	
0	0	d	d	d	d	d	
1	1	1	1	0	1	0	
1	1	1	1	0	1	0	
1	1	d	d	d	0	1	
0	0	d	d	d	d	d	
0	1	d	d	d	d	d	
0	1	d	d	d	d	d	
0	0	d	d	d	d	d	