

VILNIUS UNIVERSITY
FACULTY OF MATHEMATICS AND INFORMATICS

AMIVA Smart Saver

PSI II second lab work

Authors: SE year II 5th group students:

Andrius Pukšta

Auksė Levonaitė

Ignas Bujevičius

Modestas Gražys

Vytautas Lėveris

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1. UML 4+1

1.1. Logical view

1.1.1. Information model

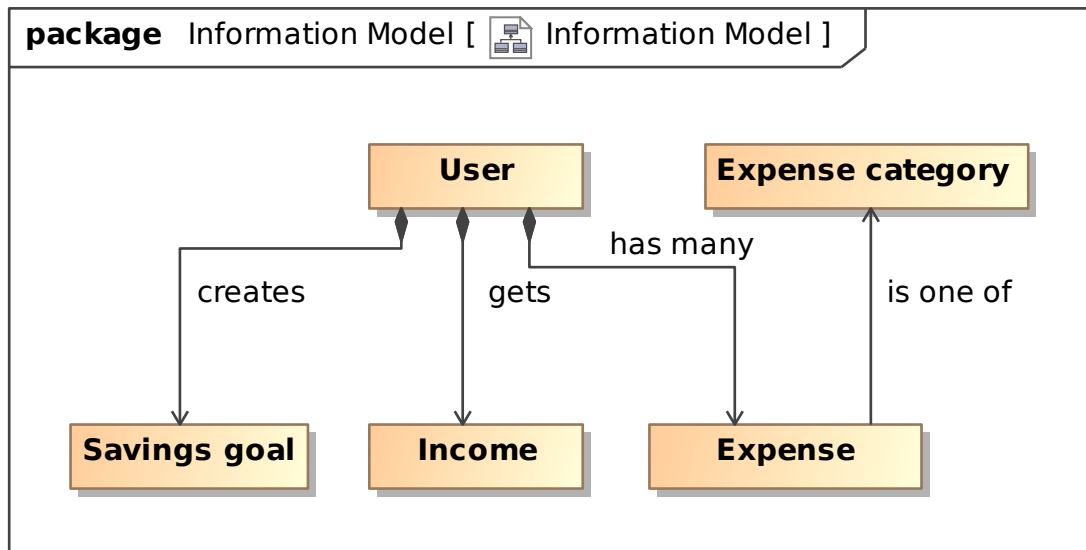


Figure 1.

Server class diagram

1.1.2. Class diagrams

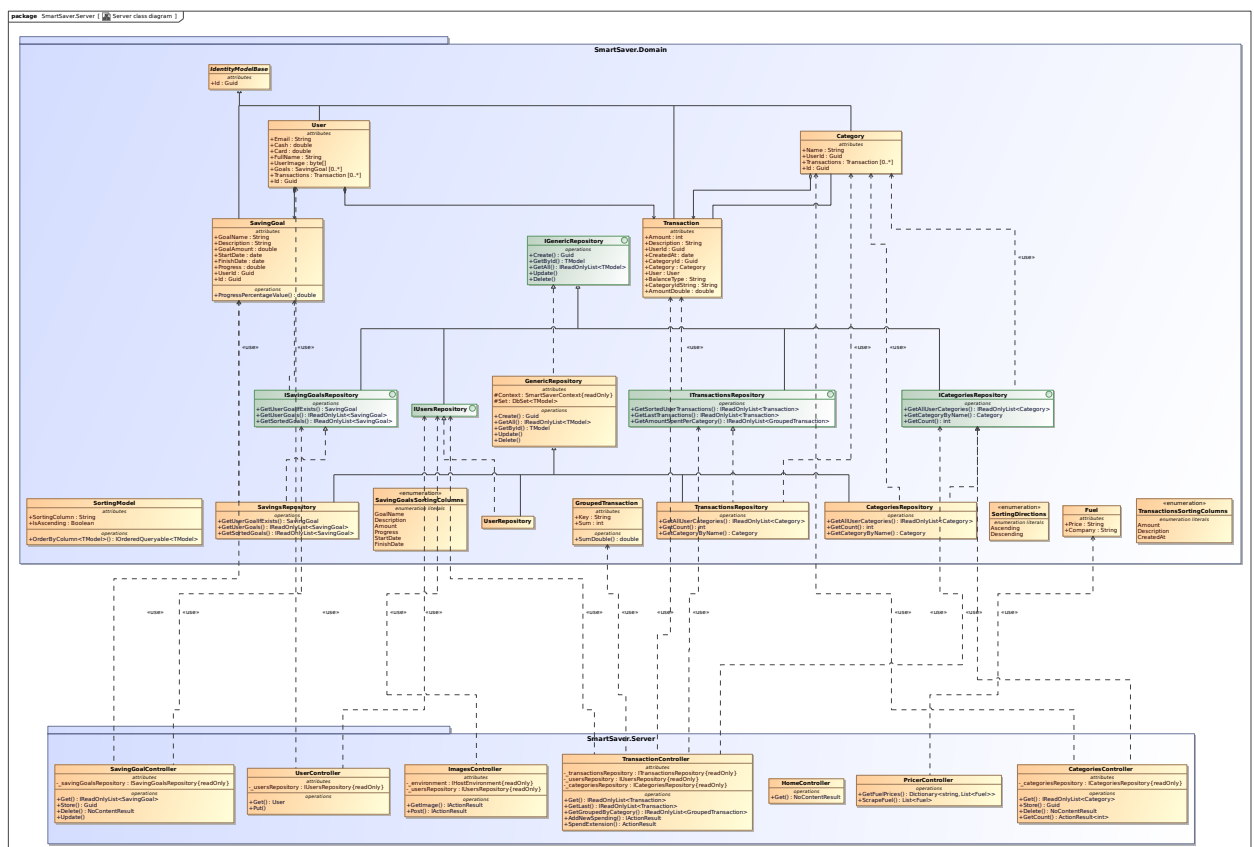
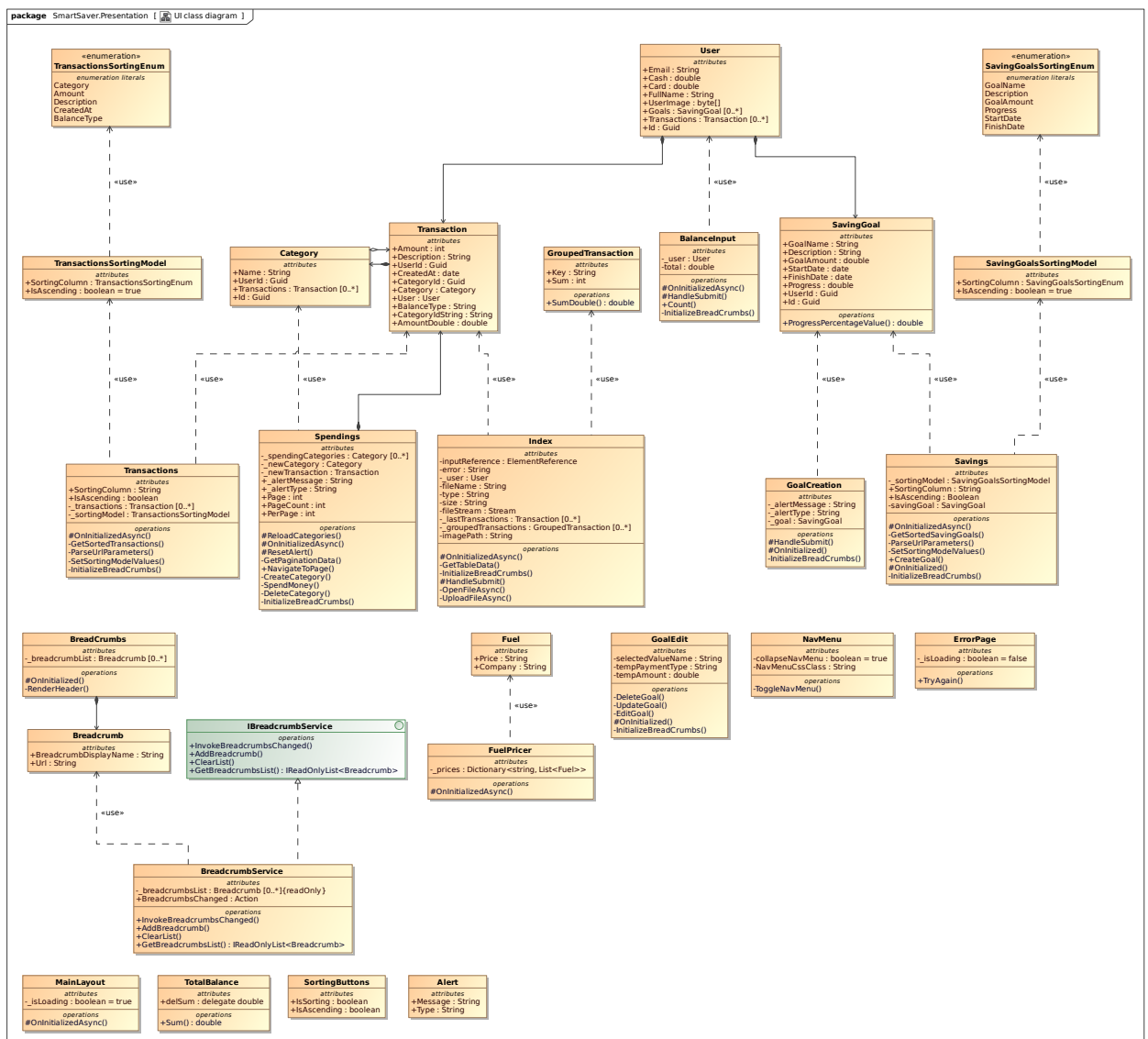


Figure 2. Server class diagram



1.1.3. Sequence diagrams

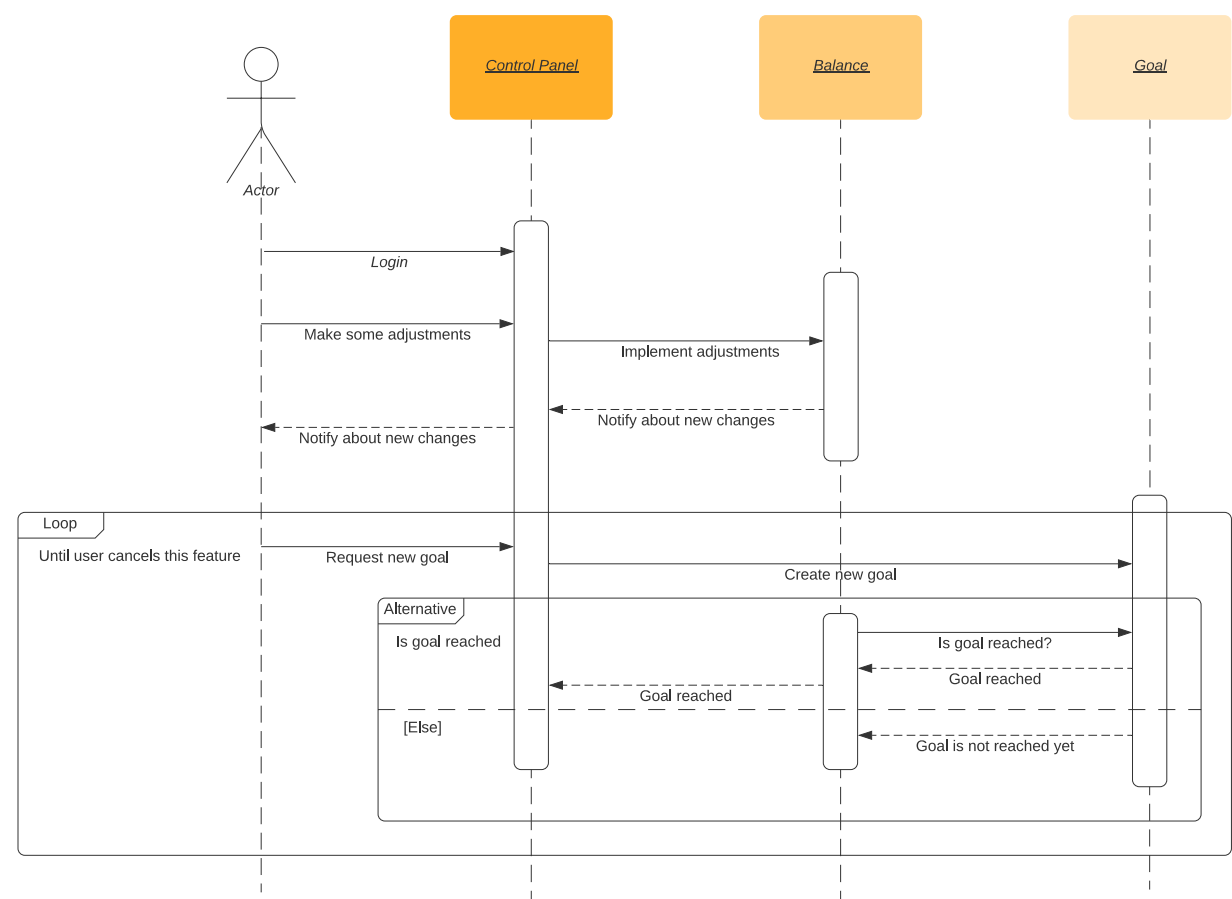


Figure 4. desc

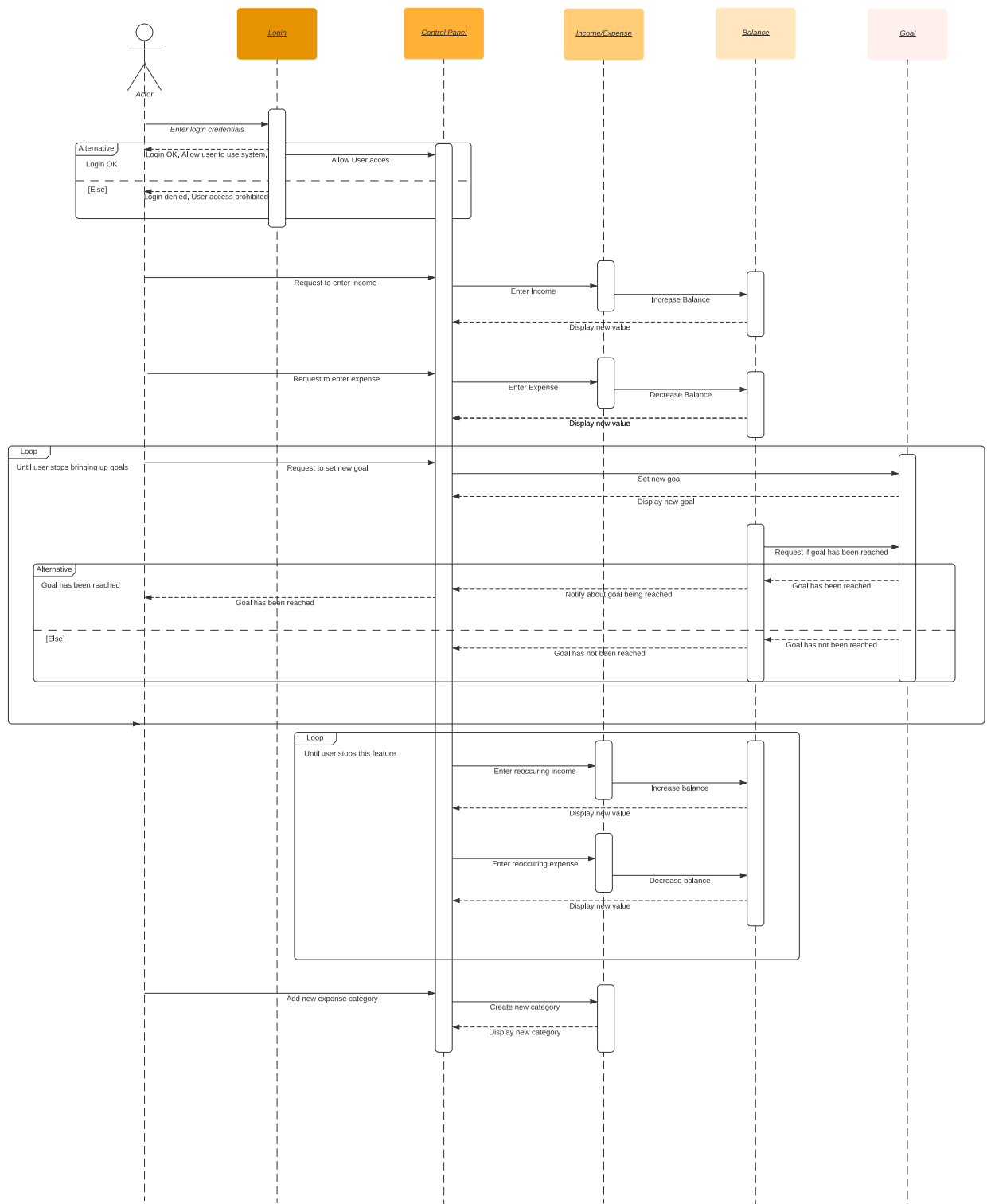


Figure 5. desc

1.1.4. Collaboration diagrams

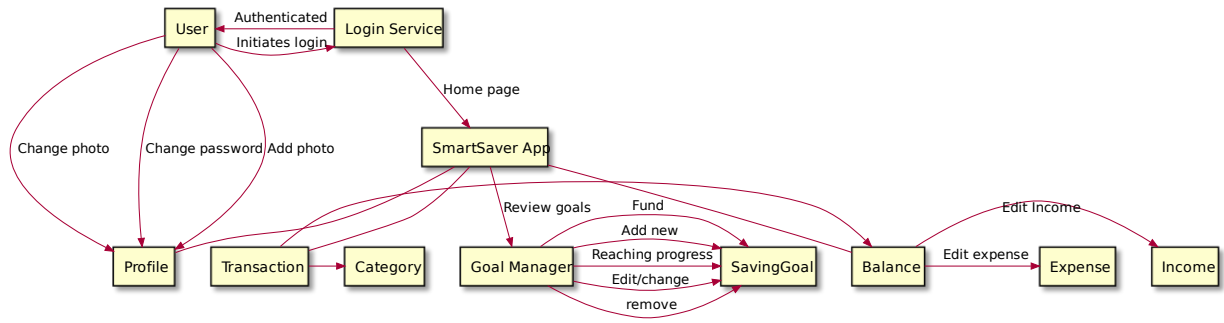


Figure 6. desc

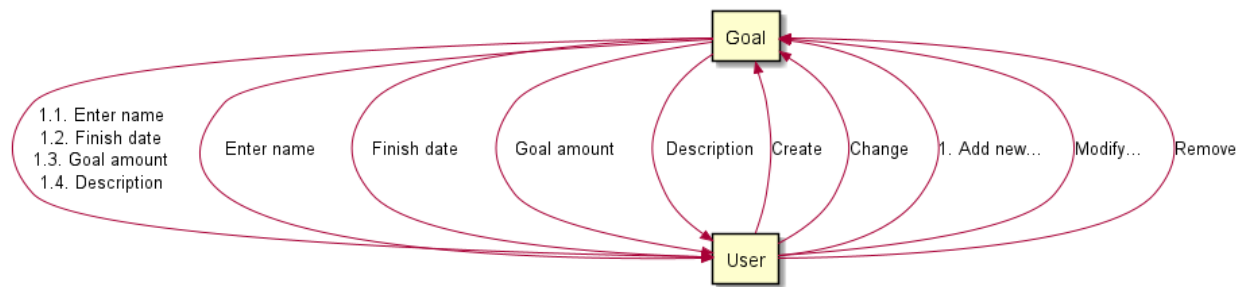


Figure 7. desc

TODO: convert to svg

1.1.5. Activity diagrams

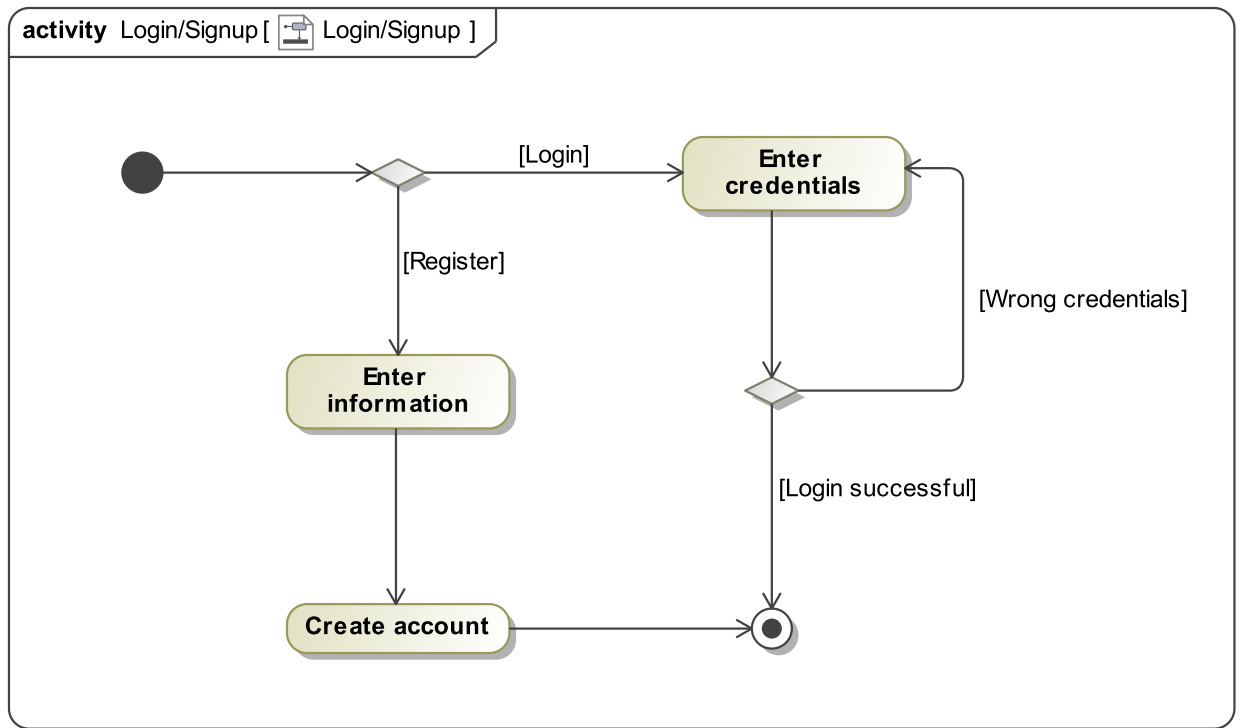


Figure 8. desc

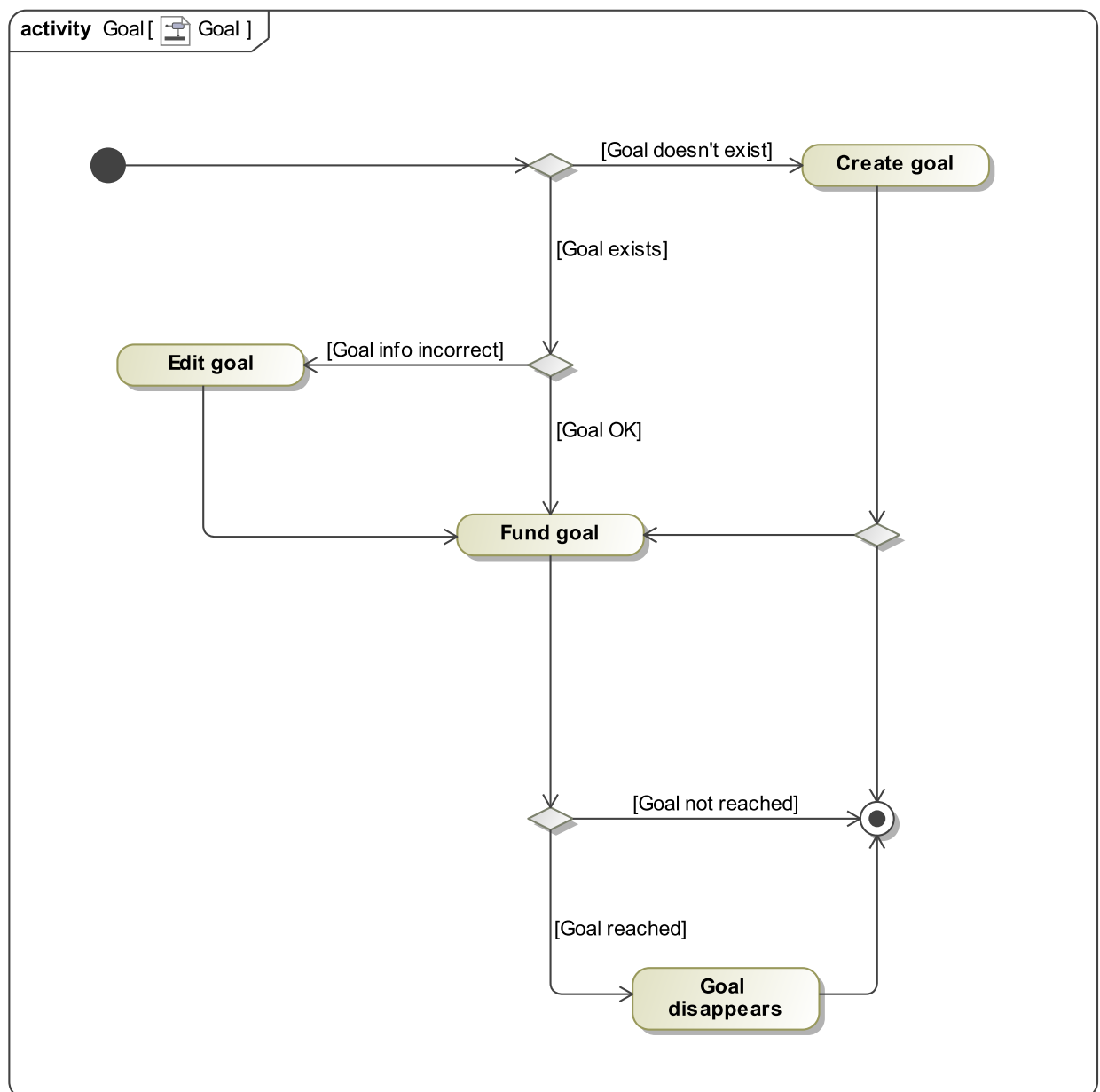


Figure 9. desc

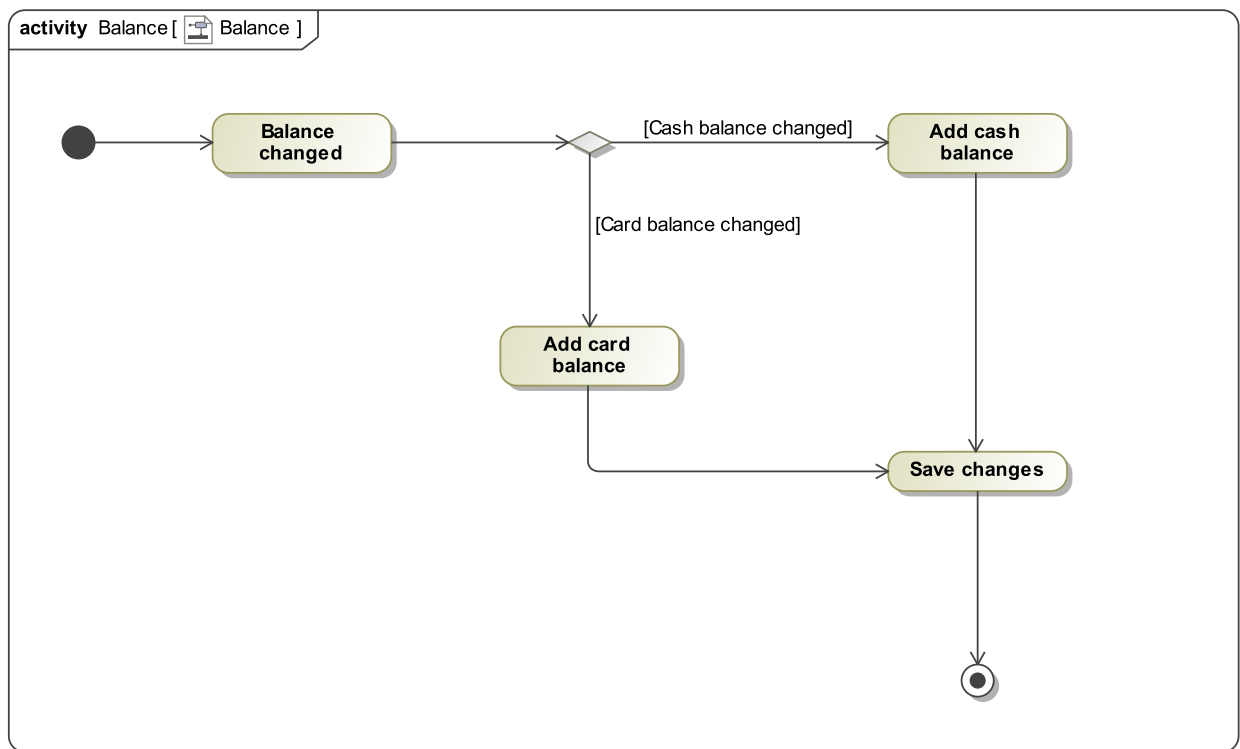


Figure 10. desc

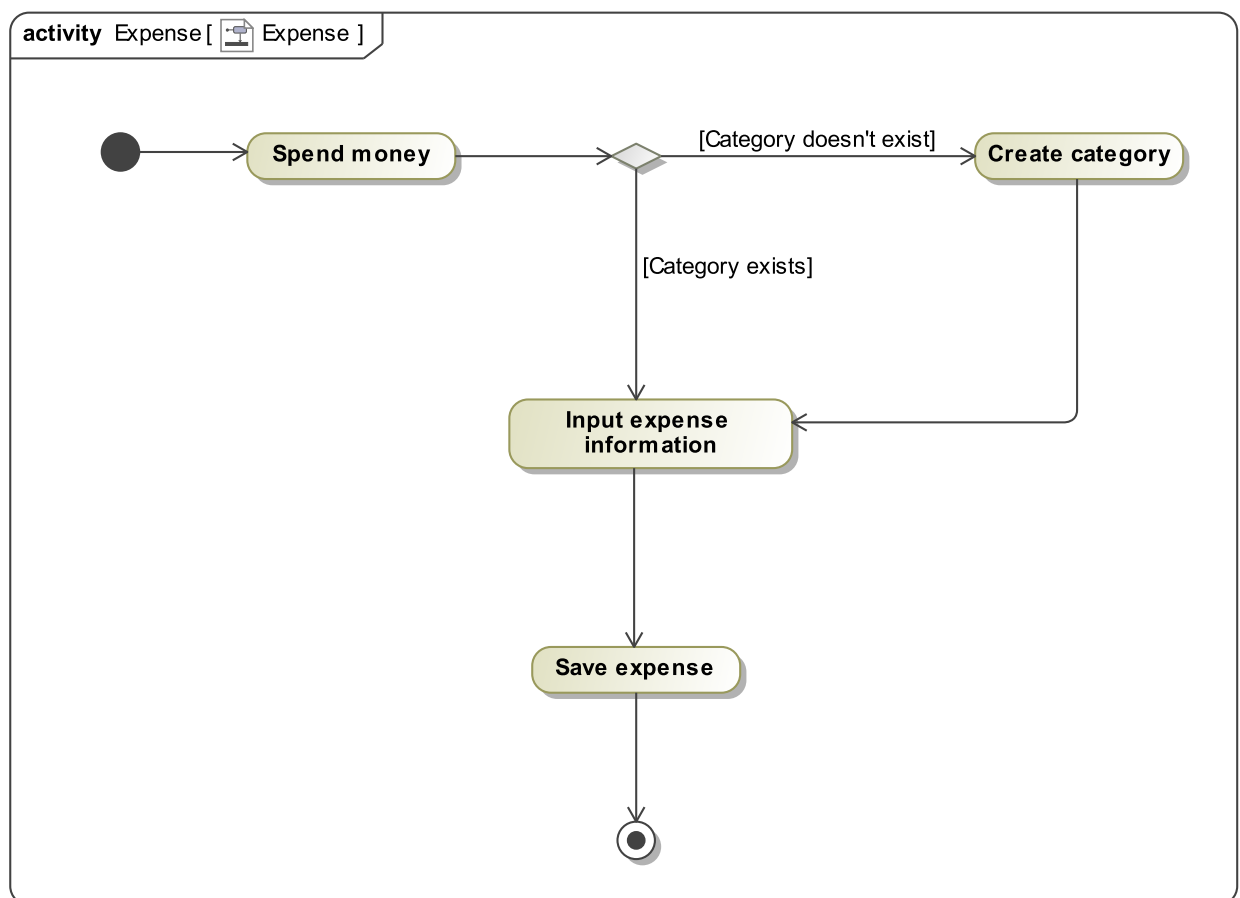


Figure 11. desc

1.1.6. State machine diagrams

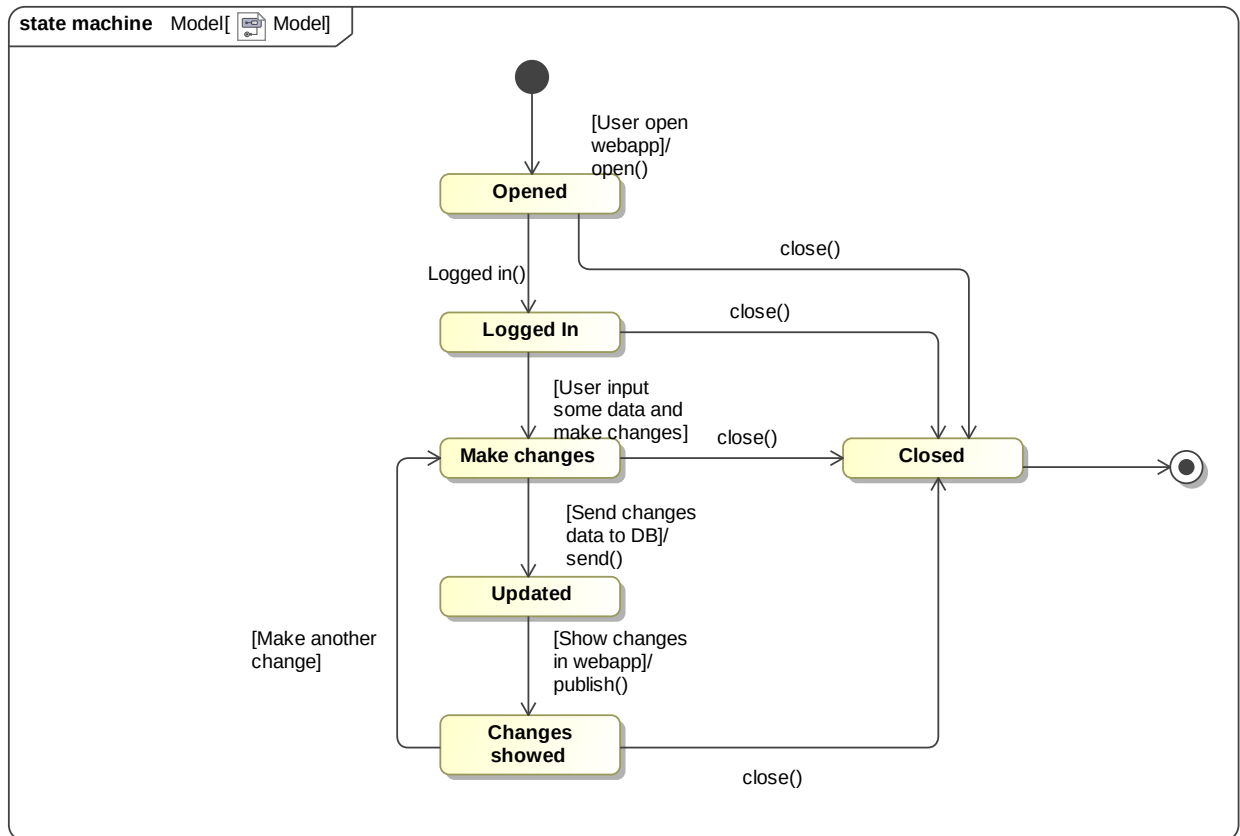


Figure 12. desc

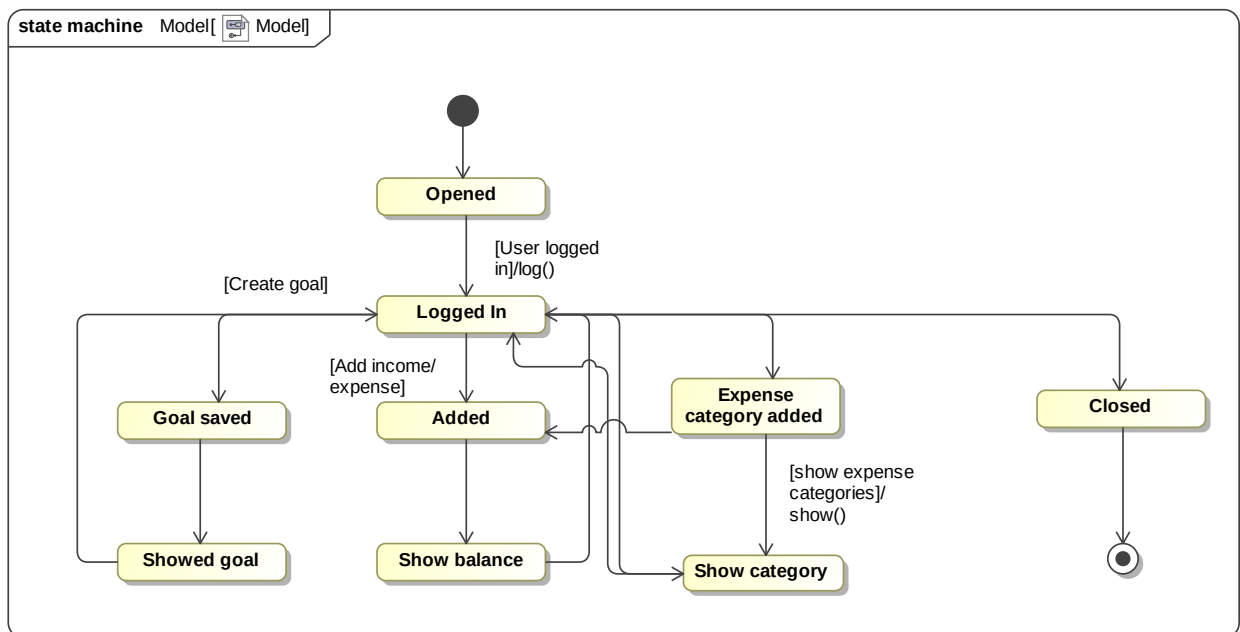


Figure 13. desc

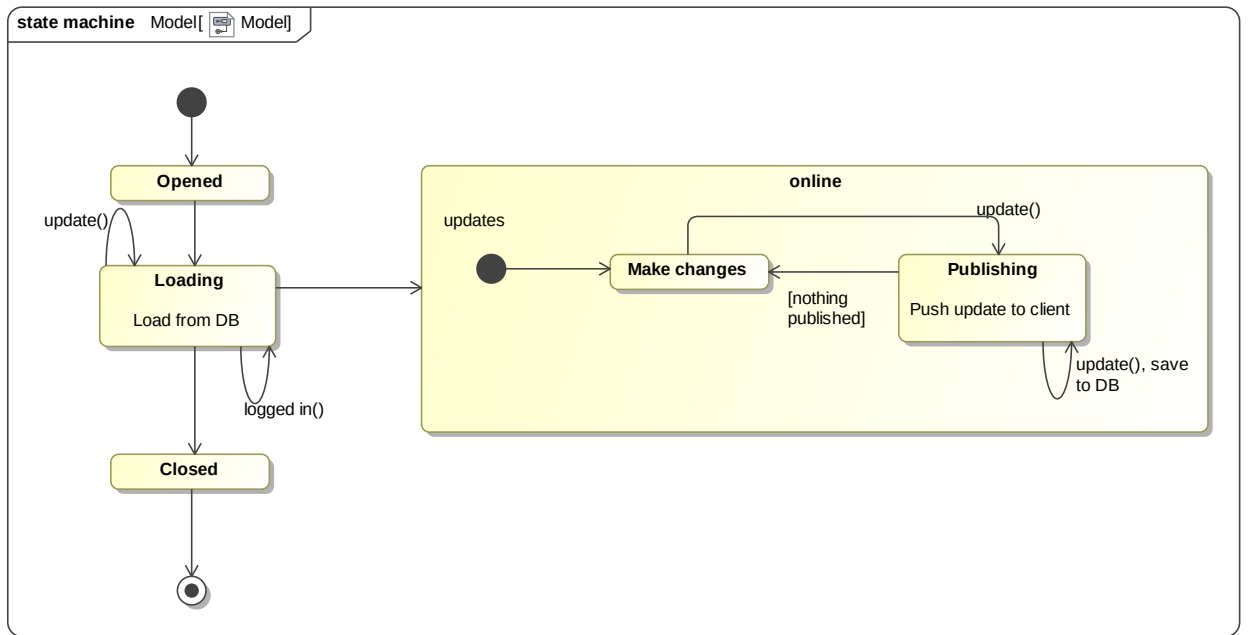


Figure 14. desc

1.2. Development view

1.2.1. Component diagrams

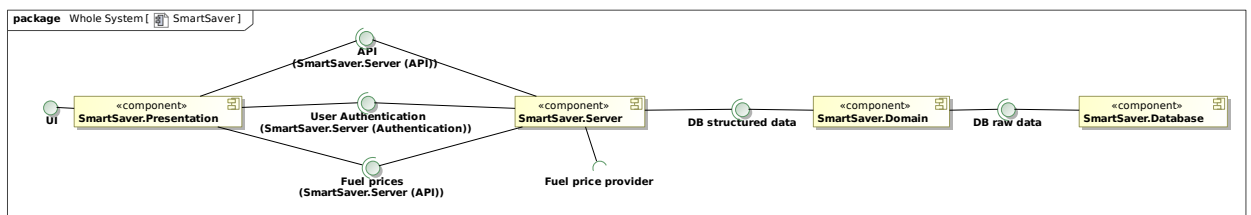


Figure 15. desc

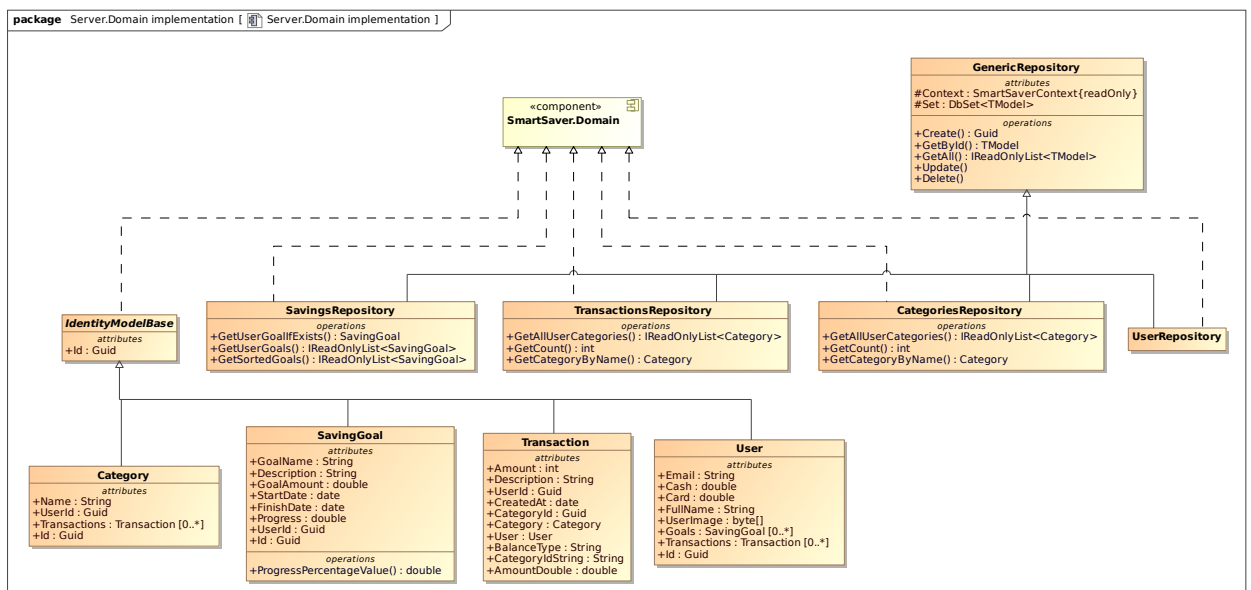


Figure 16. desc

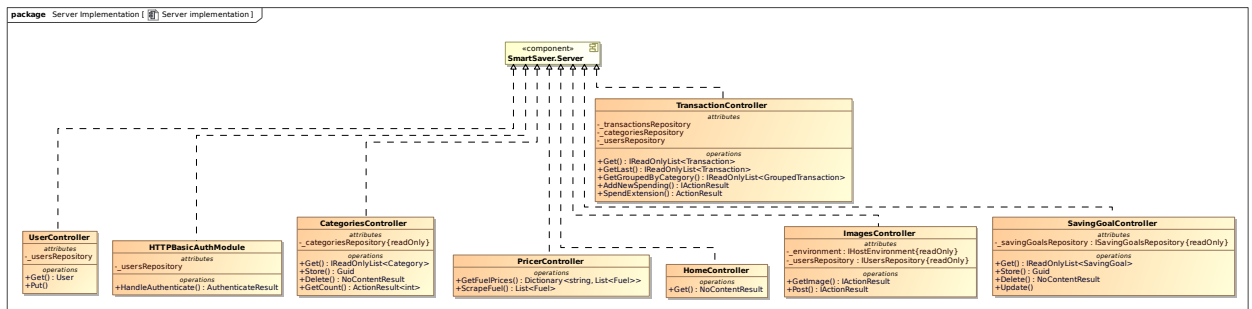


Figure 17. desc

1.2.2. Class diagrams

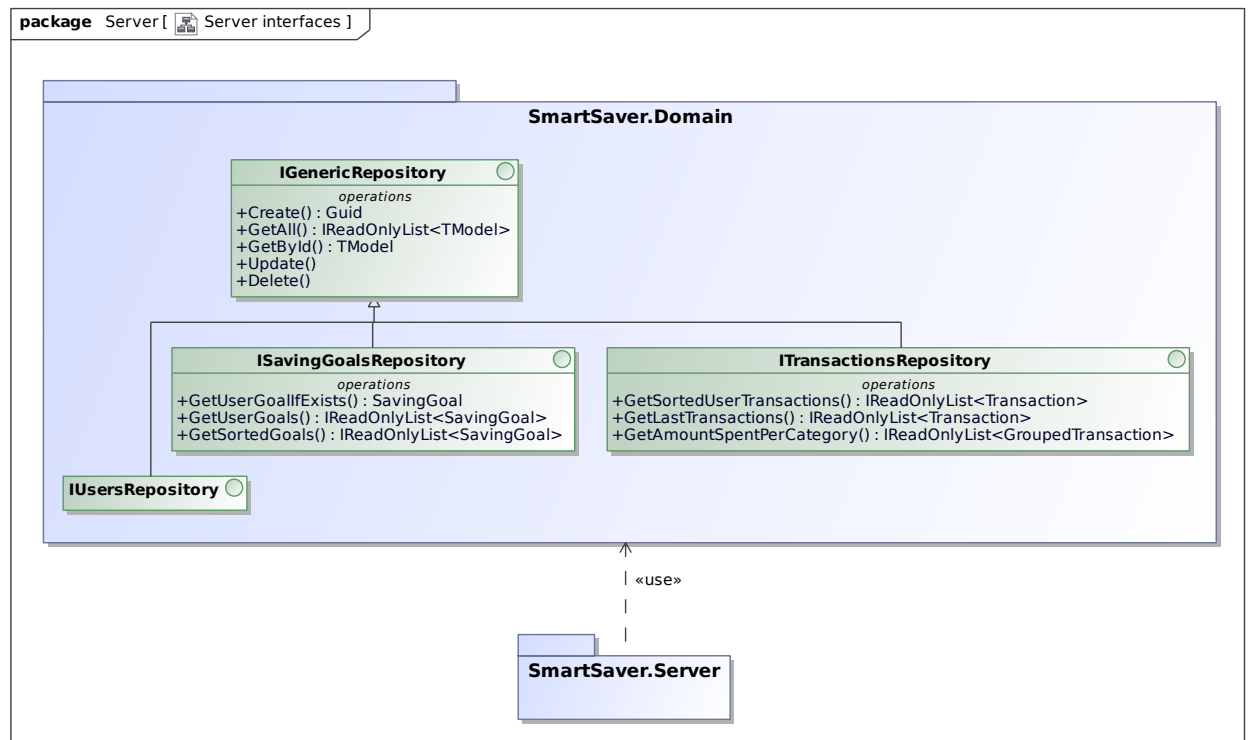


Figure 18. desc

1.2.3. Composite structure diagrams

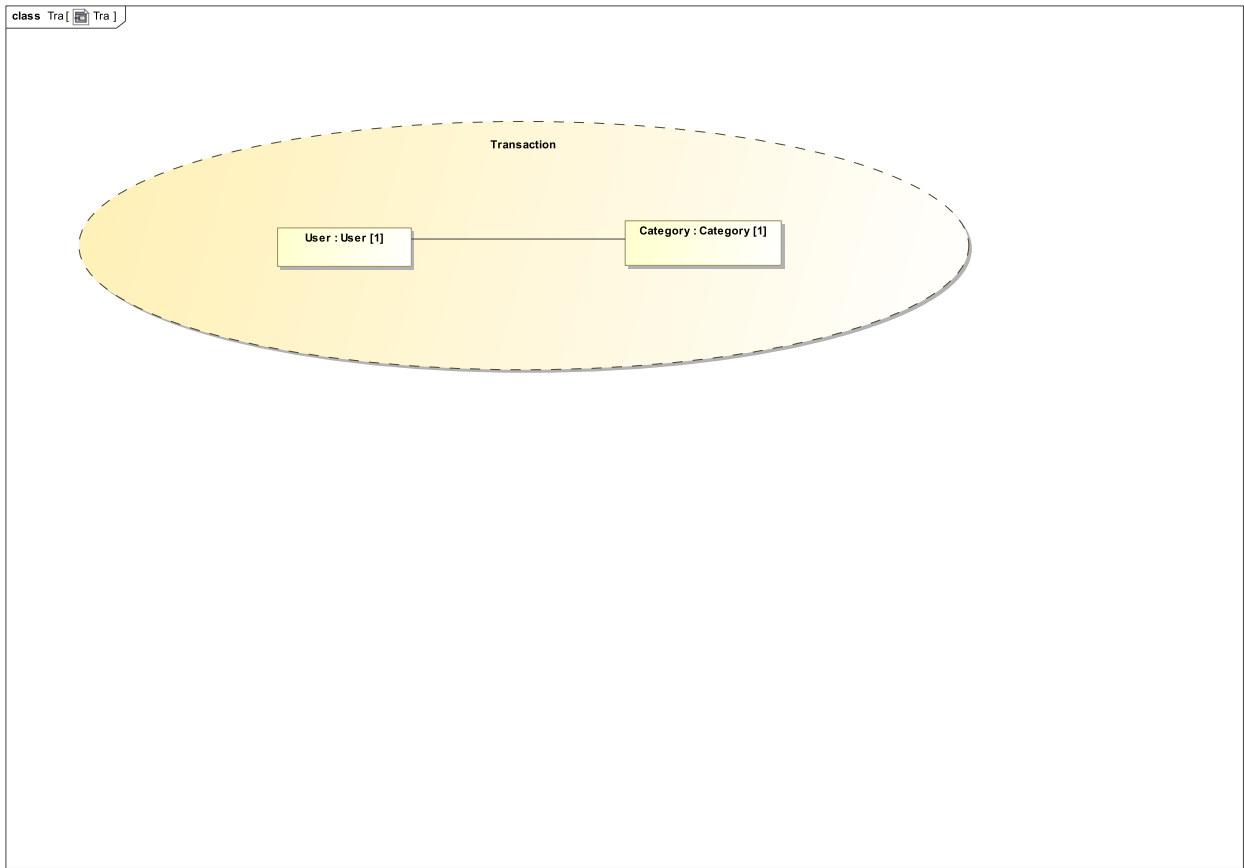


Figure 19. desc

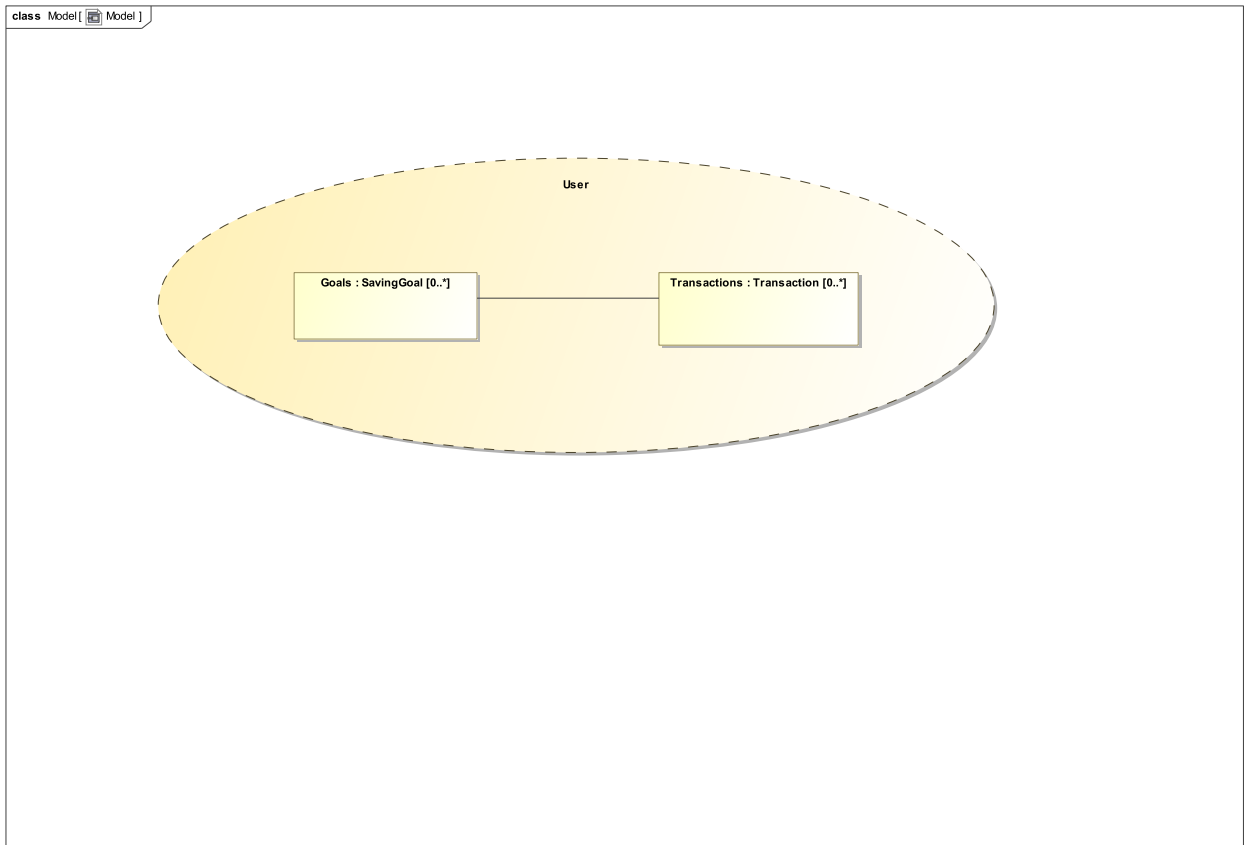


Figure 20. desc

1.2.4. Package diagrams

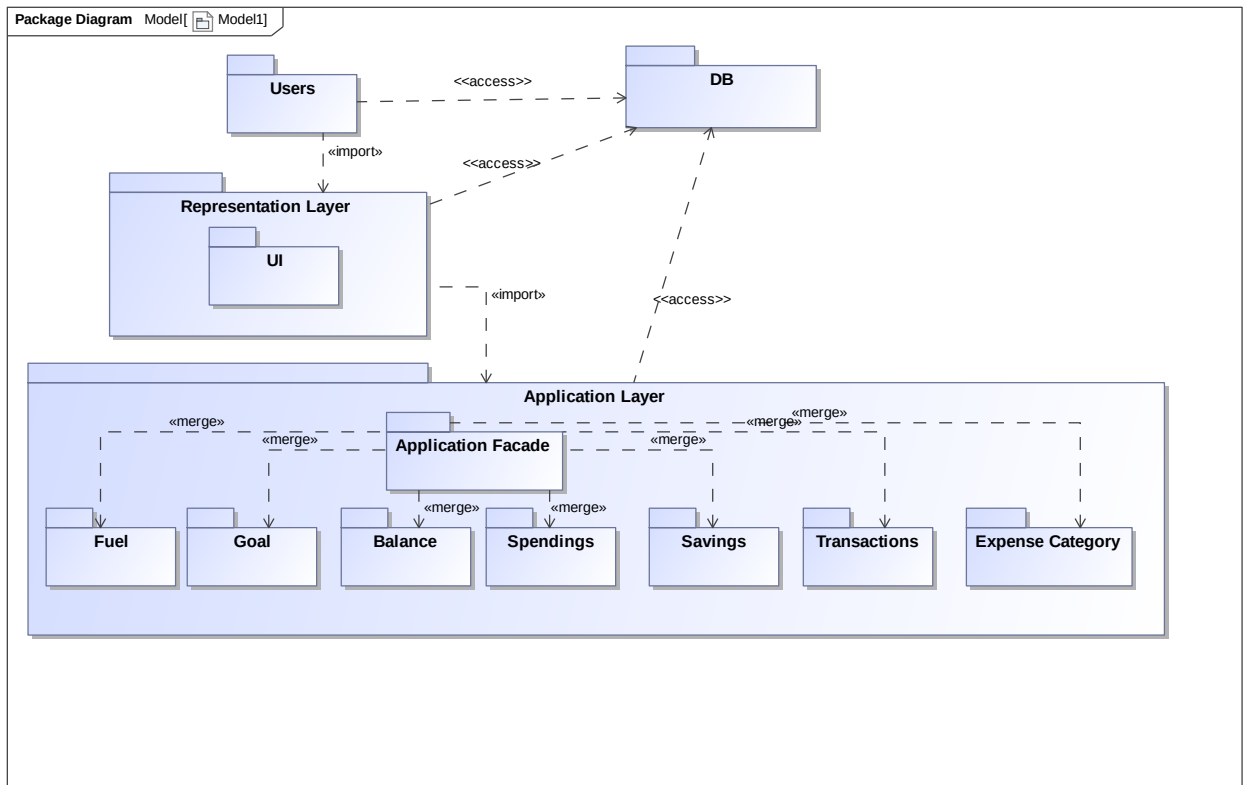


Figure 21. desc