PLAZA, ELMO L. BSCpE 3-A

21. VHDL CODE FOR RELAY INTERFACE

```
library IEEE;
use IEEE.STD_LOGIC_1164.ALL;


entity relay is
    Port (
        sw  : in  STD_LOGIC;     -- Switch input
        rl1 : out STD_LOGIC;     -- Relay output
        led : out STD_LOGIC      -- LED output
    );
end relay;
```

PIN ASSIGNMENT

XC3128TQ144		
Connector	Device pin	Property
P 18/3	41	Sw
P 18/5	43	Rl1
P 18/21	62	Led

XC2S100TQ144-5		
Connector	Device pin	Property
P 18/3	1	Sw
P 18/5	5	Rl1
P 18/21	23	Led

```
architecture Behavioral of relay is
begin
    rl1 <= sw;
    led <= sw;
end Behavioral;
```