```
PLAZA, ELMO L.

21. VHDL CODE FOR RELAY INTERFACE

library IEEE;

use IEEE.STD_LOGIC_1164.ALL;

entity relay is

Port (

sw : in STD_LOGIC; -- Switch input

rl1: out STD_LOGIC; -- Relay output

led: out STD_LOGIC -- LED output

);

end relay;

architecture Behavioral of relay is

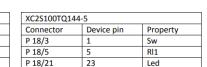
begin

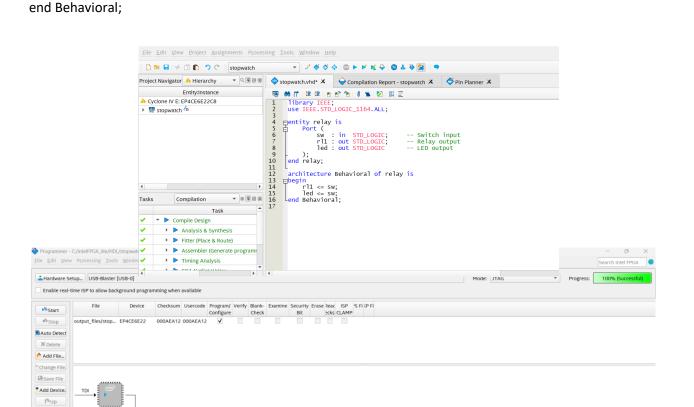
rl1 <= sw;

led <= sw;
```

EP4CE6E22

BSCpE 3-A





PIN ASSIGNMENT

Device pin

41

43

62

Property

RI1

Led

XC3128TQ144 Connector

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