IPV4 PACKET TRANSMITTER

lex_checksum_baseval(15:0) transmit_data_output_bus(7:0) flex wraddr(5:0) end of frame O flex wrdata(7:0) ransmit data input bus(7:0) transmit_data_length(15:0) source ready clk 125MHz start of frame O flex wren rst transmit_start_enable usr data trans phase on

IPV4_PACKET_TRANSMITTER