

# 18-642: Critical Systems

These tutorials are a simplified introduction, and are not sufficient on their own to achieve system safety. You are responsible for the safety of your system.



ASSC; 22 May 2019

"Never tell me the odds!"

— Han Solo



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# **Critical Systems**



#### Anti-Patterns for Critical Systems:

- You haven't characterized worst case failures
- You haven't assigned SILs to system hazards
- Validation plan doesn't match fleet exposure

#### Critical systems require low failure rates

- SIL = Safety Integrity Level
  - Higher level of integrity needed for higher risk
- Safety critical:

Loss of life, injury, environmental damage

- Special care must be taken to avoid deaths
- Mission critical:

Brand tarnish, financial loss, company failure

Consider a safety critical approach

#### Knight Capital Says Trading Glitch Cost It \$440 Million

By NATHANIEL POPPER AUGUST 2, 2012 9:07 AM 356 Comments

Runaway Trades Spread Turmoil Across Wall St.



Errant trades from the Knight Capital Group began hitting the New York Stock Exchange almost as soon as the opening bell rang on Wednesday. Brendan McDermid/Reuters

The <u>Knight Capital Group</u> announced on Thursday that it lost \$440 million when it sold all the stocks it accidentally bought Wednesday morning because a computer glitch. https://goo.gl/7dH0j0

## What Is The Worst Case Failure?



#### Worst case might not be obvious

- Aircraft software can cause a crash
- Thermostats/HVAC software can freezing plumbing
  - Can rarely! also kill small children due to overheating

#### **Key thought experiment:**

- What's the worst that can happen if ...
  - ... your system intentionally tried to cause harm?
- This identifies system hazards to mitigate

#### Failure consequence varies, typically:

- Multiple fatalities (e.g., plane crash)
- Single fatality (e.g., single-vehicle car crash)
- Severe injuries
- Minor injuries
- Can consider analogies for mission-critical goals



Malfunctioning heater leads to Fort Worth toddler's death



WFAA Channel 8 https://goo.gl/rFd8gW Takeaway: get a baby monitor with temperature sensor

# **Safety Integrity Level (SIL)**



#### **SIL represents:**

- The risk presented by a system-level hazard
- The engineering rigor applied to mitigate the risk
- The permissible residual probability after mitigation

#### **Example: DO-178 (aviation flight hours)**

- DAL A (Catastrophic): 10<sup>9</sup> hrs/failure = 114077 years
- DAL B (Hazardous): 10<sup>7</sup> hrs/failure = 1141 years
- DAL C (Major): 10<sup>5</sup> hrs/failure = 11 years
- DAL D (Minor): 10<sup>3</sup> hrs/failure = 42 days

#### **Example: IEC 61508 (industrial controls)**

- SIL 4: 108 hrs/dangerous failure = 11408 years
- SIL 3: 10<sup>7</sup> hrs/dangerous failure = 1141 years
- SIL 2: 10<sup>6</sup> hrs/dangerous failure = 114 years
- SIL 1: 10<sup>5</sup> hrs/dangerous failure = 11 years







https://en.wikipedia.org/wiki/ Bhopal\_disaster

1984: Bophal Chemical Plant Thousands of deaths (not software related: pre-dates IEC 61508)

https://goo.gl/GGHWRn

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## **Higher SIL Invokes More Engineering Rigor**



Exa	mp	le:
<b>IEC</b>	61	508

- HR = Highly
   Recommended
- R = Recommended
- NR = Not
   Recommended
   (don't do this)
- SIL 1: lowest integrity level (low risk)
- SIL 4: highest integrity level (unacceptable risk)

	Technique/Measure*	Ref	SIL1	SIL2	SIL3	
1	Fault detection and diagnosis	C.3.1		R	HR	Ι
2	Error detecting and correcting codes	C.3.2	R	R	R	
3a	Failure assertion programming	C.3.3	R	R	R	
3b	Safety bag techniques	C.3.4		R	R	$\Gamma$
3c	c Diverse programming		R	R	R	
3d	Recovery block	C.3.6	R	R	R	
3e	Backward recovery	C.3.7	R	R	R	T
3f	Forward recovery	C.3.8	R	R	R	Τ
3g	Re-try fault recovery mechanisms	C.3.9	R	R	R	7
3h	Memorising executed cases	C.3.10		R	R	
4	Graceful degradation	C.3.11	R	R	HR	I
5	Artificial intelligence - fault correction	C.3.12		NR	NR	T
6	Dynamic reconfiguration			NR	NR	Ţ
7a	Structured methods including for example, JSD, MASCOT, SADT and Yourdon.	C.2.1	HR	HR	HR	F
7b	Semi-formal methods		R	R	HR	
7c	Formal methods including for example, CCS, CSP, HOL, LOTOS, OBJ, temporal logic, VDM and Z	C.2.4		R	R	K
8	Computer-aided specification tools [IEC 61508]	B.2.4	R	R	HR	十

# Fleet Exposure & Probability



#### **Bigger fleets have increased exposure**

- 250 Million US vehicles @ 1 hour/day = 2.5 \* 108 hrs/day exposure
- If "unlikely" failures happen every million hours... that's:  $2.5 * 10^8$  hrs /  $10^6$  hrs per event
  - → 250 events every day
- This is why 108 to 1010 hrs is a typical goal

#### Hardware components fail at ~10<sup>5</sup>-10<sup>6</sup> hrs

- Need two independently failing components to get to 10<sup>9</sup> hours!
  - This motivates redundancy for life-critical applications (SIL 3 & SIL 4)

#### For mission-critical systems, consider:

- Fleet exposure = # units \* operational hours/unit
- Number of acceptable failures
- Compute failure rate = failures / hours; pick an appropriate SIL

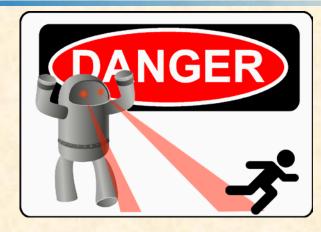


### **Best Practices For Critical Systems**



#### Characterize worst case failure scenarios

- Assign SIL based on relevant safety standard
- Use engineering rigor for software SIL
- Use redundancy for ultra-low failure rates
- Consider fleet exposure, not just single unit



#### Pitfalls:

- Software redundancy is difficult, and diversity is usually impracticable
- Designer's intuition about "realistic" faults usually optimistic
  - At 10<sup>-9</sup>/hr, random chance is a close approximation of a malicious adversary
- Going through the motions not enough for SIL-based process