

Name	Value
Out[31:0]	0000000b
clk	1
rst	0
InstrIn[31:0]	49813000
WriteData[31:0]	0000000b
ReadSelect1[4:0]	01
ReadSelect2[4:0]	06
WriteSelect[4:0]	0c
WriteEnable	1
Reset	0
Clk	1
ReadData1[31:0]	0000000a
ReadData2[31:0]	00000001
w_reg_to_outmux[31:0,31:0,31:0,31:0]	[00000000, 00000000, 00000000, 00000000]
[31,31:0]	00000000
[30,31:0]	00000000
[29,31:0]	00000000
[28,31:0]	00000000
[27,31:0]	00000000
[26,31:0]	00000000
[25,31:0]	00000000
[24,31:0]	00000000
[23,31:0]	00000000
[22,31:0]	00000000
[21,31:0]	00000000
[20,31:0]	00000000
[19,31:0]	00000000
[18,31:0]	00000000
[17,31:0]	00000000
[16,31:0]	00000000
[15,31:0]	00000000
[14,31:0]	00000000
[13,31:0]	00000000
[12,31:0]	0000000b
[11,31:0]	0000000a
[10,31:0]	0000aab4

