

## Using SDF Files with Questa Sim

- Change your test bench to use the architecture name given in the output from Quartus. Compile the .vho file you wrote from Quartus and your new test bench which references the Altera architecture name.
- In the directory where you created your Altera output (.vho and .sdo files), type vsim& in the terminal to start Questa Sim as a background process.
- Select Simulate -> Start Simulation from the pulldown menu
- Change the default resolution of the simulator, do that by selecting the correct units (ps) from the Resolution popup men in the bottom right hand portion of the window.
- Click on the SDF tab in the Start Simulation window.
- Click on Add...
- Navigate to and select your .sdo file.
- Click on Open.
- You now need to tell the simulator which region of the design has an SDF file. If you are using a test bench, you want to give the label of the entity you are testing (e.g., DUT, U1, U2, etc.). You can also choose min, typ, or max delay with the popup menu provided.
- Click OK.
- Click on the Design tab from the pulldown menu and proceed as usual with Questa Sim.
- Be aware that if you type run 200, it is now 200 ps and not 200 ns. If you want to run for 200 ns, just type run 200 ns.