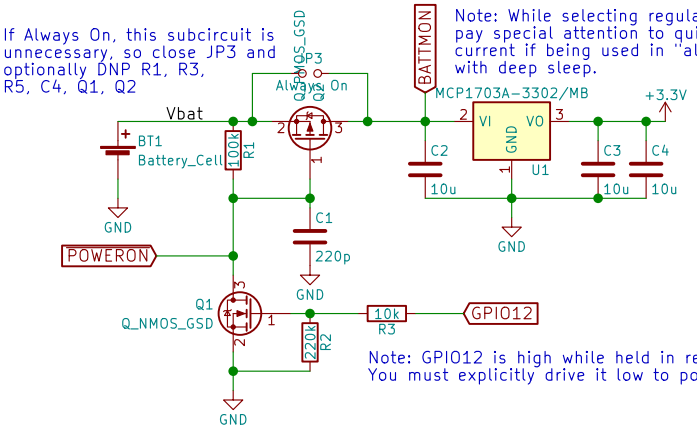
Based on
https://hackaday.io/project/12866-esp8266-power-latch

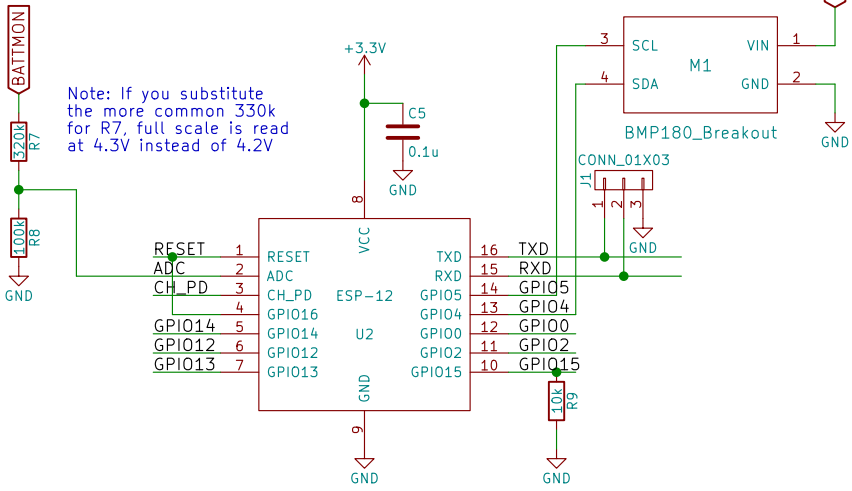
If Always On, this subcircuit is unnecessary, so close JP3 and optionally DNP R1, R3, R5, C4, Q1, Q2

Note: While selecting regulator, pay special attention to quiescent current if being used in "always on" with deep sleep.

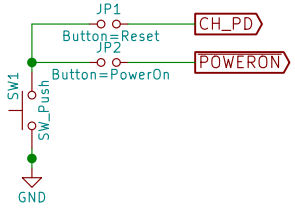


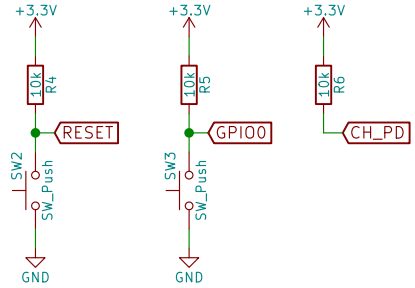
Note: GPIO12 is high while held in reset.
You must explicitly drive it low to power off.

Note: If you substitute the more common 330k for R7, full scale is read at 4.3V instead of 4.2V

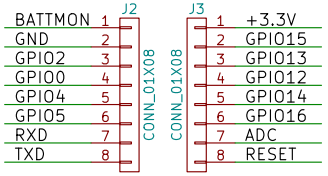


What Should the Big Button Do?





Wemos D1 Mini Headers



OSHW
P1

Sheet: /
File: esp8266_button.sch

Title: ESP8266 Button (multipurpose dev board)

Size: A4	Date: 2017-07-25	**Rev:** v1
KiCad E.D.A.	kicad 4.0.6-e0-634953ubuntu14.04.1	Id: 1/1