MSP432P4xx Pin Attributes

SIGNAL NAME	GPIO	ADC	Timer	UART	I2C	SPI	Clock	Port Mapper	Comparato	LCD	Debug
P1.0/UCA0STE/L19	P1.0					UCA0STE				L19	
P1.1/UCA0CLK/L18	P1.1					UCA0CLK				L18	
P1.2/UCA0RXD/UCA0SOMI/L17	P1.2			UCA0RX	D	UCA0SON	ı			L17	
P1.3/UCA0TXD/UCA0SIMO/L16	P1.3			UCA0TXI	þ	UCA0SIM0				L16	
P1.4/UCB0STE/L15	P1.4					UCB0STE				L15	
P1.5/UCB0CLK/L14	P1.5					UCB0CLK				L14	
P1.6/UCB0SDA/UCB0SIMO/L13	P1.6				UCB0SD.	AUCB0SIM0				L13	
P1.7/UCB0SCL/UCB0SOMI/L12	P1.7				UCB0SC	LUCB0SON	I			L12	
P2.0/PM_UCA1STE/L11	P2.0							PM_UCA1STE		L11	
P2.1/PM_UCA1CLK/L10	P2.1							PM_UCA1CLK		L10	
P2.2/PM_UCA1RXD/PM_UCA1SOM	/L 9 P2.2							PM_UCA1RXD/PM_UCA1SO	MI	L9	
P2.3/PM_UCA1SIMO/PM_UCA1TXD	/L8P2.3							PM_UCA1SIMO/PM_UCA1T	X D	L8	
P2.4/PM_TA0.1/L23	P2.4							PM_TA0.1		L23	
P2.5/PM_TA0.2/L22	P2.5							PM_TA0.2		L22	
P2.6/PM_TA0.3/L21	P2.6							PM_TA0.3		L21	
P2.7/PM_TA0.4/L20	P2.7							PM_TA0.4		L20	
P3.0/PM_UCA2STE/L7	P3.0							PM_UCA2STE		L7	
P3.1/PM_UCA2CLK/L6	P3.1							PM_UCA2CLK		L6	
P3.2/PM_UCA2RXD/PM_UCA2SOM	/L 5 P3.2							PM_UCA2RXD/PM_UCA2SO	MI	L5	
P3.3/PM_UCA2SIMO/PM_UCA2TXD	/L4P3.3							PM_UCA2SIMO/PM_UCA2T	X D	L4	
P3.4/PM_UCB2STE/L3	P3.4							PM_UCB2STE		L3	
P3.5/PM_UCB2CLK/L2	P3.5							PM_UCB2CLK		L2	
P3.6/PM_UCB2SDA/PM_UCB2SIMC	/L1P3.6							PM_UCB2SDA/PM_UCB2SII	ИО	L1	
P3.7/PM_UCB2SCL/PM_UCB2SOMI	/L0P3.7							PM_UCB2SCL/PM_UCB2SC	MI	L0	
P4.0/A13/L13	P4.0	A13								L13	
P4.1/A12/L12	P4.1	A12								L12	
P4.2/A11/TA2CLK/ACLK	P4.2	A11	TA2CLK				ACLK				
P4.3/A10/MCLK/RTCCLK	P4.3	A10					MCLK				F
P4.4/A9/HSMCLK/SVMHOUT	P4.4	A9					HSMCL	k			
P4.5/A8	P4.5	A8									
P4.6/A7	P4.6	A7									
P4.7/A6	P4.7	A6									
P5.0/A5	P5.0	A5									
P5.1/A4	P5.1	A4									
P5.2/A3	P5.2	А3									
P5.3/A2	P5.3	A2									
P5.4/A1	P5.4	A1									
P5.5/A0	P5.5	A0									
P5.6/TA2.1/C1.7/VREF+	P5.6		TA2.1						C1.7		
P5.7/TA2.2/C1.6/VREF-	P5.7		TA2.2						C1.6		
P6.0/A15/L15	P6.0	A15								L15	
P6.1/A14/L14	P6.1	A14								L14	
P6.2/UCB1STE/C1.5/L27	P6.2					UCB1STE			C1.5	L27	
P6.3/UCB1CLK/C1.4/L26	P6.3					UCB1CLK			C1.4	L26	

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P6.4/UCB1SDA/UCB1SIMO/C1.3/L25						UCB1SIMO			C1.3	L25	
P6.5/UCB1SCL/UCB1SOMI/C1.2/L24						UCB1SOM			C1.2	L24	.
P6.6/TA2.3/UCB3SDA/UCB3SIMO/C1.1P6.6			TA2.3			UCB3SIMC			C1.1		
P6.7/TA2.4/UCB3SCL/UCB3SOMI/C1	1.0P6.7		TA2.4		UCB3SCI	UCB3SOM	1		C1.0		
P7.0/PM_DMAE0/PM_SMCLK/R03	P7.0							PM_DMAE0/PM_SMCLK		R03	
P7.1/PM_C0OUT/PM_TA0CLK/R13	P7.1							PM_C0OUT/PM_TA0CLK		R13	
P7.2/PM_C1OUT/PM_TA1CLK/R23	P7.2							PM_C1OUT/PM_TA1CLK		R23	
P7.3/PM_TA0.0	P7.3							PM_TA0.0			
P7.4/PM_TA1.4/C0.5/L31	P7.4							PM_TA1.4	C0.5	L31	
P7.5/PM_TA1.3/C0.4/L30	P7.5							PM_TA1.3	C0.4	L30	
P7.6/PM_TA1.2/C0.3/L29	P7.6							PM_TA1.2	C0.3	L29	
P7.7/PM_TA1.1/C0.2/L28	P7.7		PM_TA1.	1					C0.2	L28	
P8.0/TA1.0/UCB3STE/C0.1	P8.0		TA1.0			UCB3STE			C0.1		
P8.1/TA2.0/UCB3CLK/C0.0	P8.1		TA2.0			UCB3CLK			C0.0		
P8.2/A23/TA3.2/L47	P8.2	A23	TA3.2							L47	
P8.3/A22/TA3CLK/L46	P8.3	A22	TA3CLK							L46	
P8.4/A21/L45	P8.4	A21								L45	
P8.5/A20/L44	P8.5	A20								L44	
P8.6/A19/L19	P8.6	A19								L19	
P8.7/A18/L18	P8.7	A18								L18	
P9.0/A17/L17	P9.0	A17								L17	
P9.1/A16/L16	P9.1	A16								L16	
P9.2/TA3.3/L33	P9.2		TA3.3							L33	
P9.3/TA3.4/L32	P9.3		TA3.4							L32	
P9.4/UCA3STE/L43	P9.4					UCA3STE				L43	
P9.5/UCA3CLK/L42	P9.5					UCA3CLK				L42	
P9.6/UCA3RXD/UCA3SOMI/L41	P9.6			UCA3RX	b	UCA3SOM				L41	
P9.7/UCA3TXD/UCA3SIMO/L40	P9.7			UCA3TX		UCA3SIMO				L40	
P10.0/UCB3STE/L39	P10.0			00/101/1	1	UCB3STE				L39	
P10.1/UCB3CLK/L38	P10.1					UCB3CLK				L38	
P10.2/UCB3SDA/UCB3SIMO/L37	P10.2					UCB3SIMO				L37	
P10.3/UCB3SCL/UCB3SOMI/L36	P10.3					UCB3SOM				L36	
P10.4/TA3.0/C0.7/L35	P10.4		TA3.0		ОСВОЗСІ	DCD33CIV	1		C0.7	L35	
P10.5/TA3.1/C0.6/L34	P10.5		TA3.1						C0.6	L34	
PJ.0/LFXIN	PJ.0		IA3.1				LFXIN		C0.0	L34	
	PJ.1						LFXOU.	<u></u>		-	
PJ.1/LFXOUT	PJ.1 PJ.2		+	1	 		HFXOU			1	
PJ.2/HFXOUT			1		 			•			
PJ.3/HFXIN	PJ.3		1		_		HFXIN			1	
PJ.4/TDI	PJ.4		1							1	TDI
PJ.5/SWO	PJ.5		1							1	SWO
SWDIOTMS	1		1	1	 					1	SWDIOTMS
SWCLKTCK											SWCLKTCK