


TI-RSLK-MAX Pins Maps

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Energia

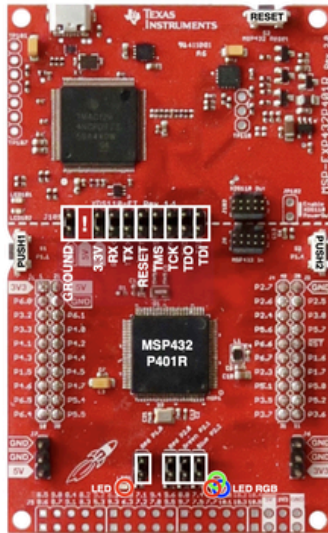
MSP432P401R and TI-RSLK MAX

Base configuration

Flash 256 KB
SRAM 64 KB

Serial hardware
ADC 14 bits
Use pins numbers only!
Default PC = (1)
PC (1) master only

△ Remove 5V shunt



Hardware
Pin number
Other pin number

PC
Serial UART
SPI

analogRead()
digitalRead() and digitalWrite()
digitalRead(), digitalWrite() and analogWrite()

RXD
TXD

△ Remove 5V shunt

Bumpers Motors Others

Bumpers: +3.3V, RX (1), TX (1), A15, P8.0, 2, 22, GROUND, A14, BMP0, BMP1, Scope, BMP2, A10, P4.3, 6, 26, P4.4, A9, BMP3, BMP4, SCK, P1.5, 7, 27, P4.5, A8, DIRL, DIRR, SCL (1), SDA (1), P4.6, 8, 28, P4.7, A6, P8.5, 9, 29, P5.4, A1, P8.4, 10, 30, P5.5, A0

GROUND
GROUND
+5V

J5

41 → 56
57 → 72

LED Motors Reflection

LEDBL LEDBR

CTRLLEVEN

QTR2 QTR0

QTR5 QTR7

ELA SCL

+5V +3.3V GROUND

Motors

PWML PWMR

CS

RESET

SDA (I) MOSI

SCL (I) MISO

ERB ELB nSLPR

GROUND
GROUND
+3.3V

Motors: PWMx, DIRx, nSLPx

Bumpers: BUMPx

Encoders: ELA, ELB, ERA, ERB

LEDs: LEDBL, LEDBR, LEDFL, LEDFR

Reflection: QTRx, CTRL

IR: DISTx, AUXx

Servos: FBx

Screen

IR

J5

41 → 56
57 → 72

LED Motors Reflection

LEDBL LEDBR

CTRLLEVEN

QTR2 QTR0

QTR5 QTR7

ELA SCL

+5V +3.3V GROUND

Motors

PWML PWMR

CS

RESET

SDA (I) MOSI

SCL (I) MISO

ERB ELB nSLPR

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GROUND
+3.3V

Motors: PWMx, DIRx, nSLPx

Bumpers: BUMPx

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Screen

IR

J5

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57 → 72

LED Motors Reflection

LEDBL LEDBR

CTRLLEVEN

QTR2 QTR0

QTR5 QTR7

ELA SCL

+5V +3.3V GROUND

Motors

PWML PWMR

CS

RESET

SDA (I) MOSI

SCL (I) MISO

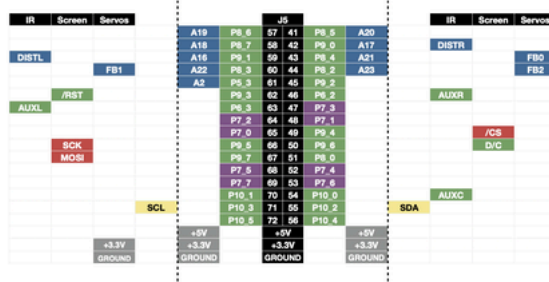
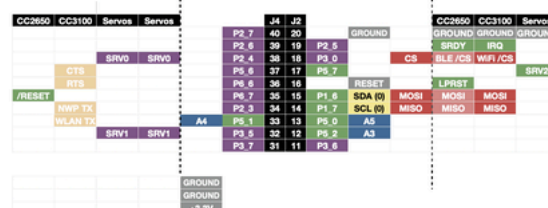
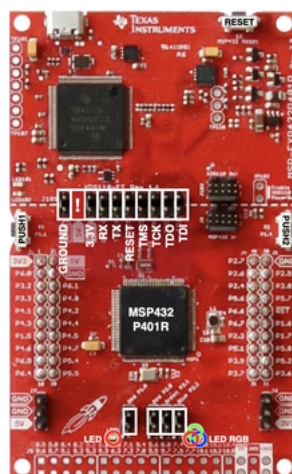
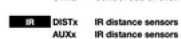
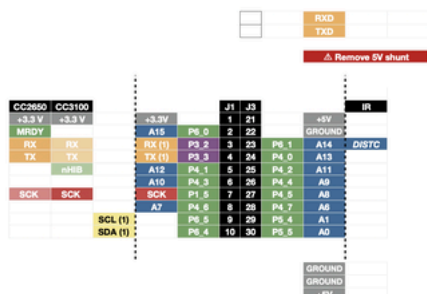
ERB ELB nSLPR

GROUND
GROUND
+3.3V

Rei Vito, 2012-2019
embeddedcomputing.weebly.com
Version 1.1 2019-09-23

Flash	256	KB
SRAM	64	KB
Serial	hardware	
ADC	14	bits
Use pins numbers only!		
Default I ² C = (1)		
I ² C (1) master only		

Hardware
Pin number
Other pin number
PC
Serial UART
SPI
analogRead()
digitalRead() and digitalWrite()
digitalRead(), digitalWrite() and analogWrite()



MSP432P401R and TI-RSLK MAX

Base and extended configuration

Flash	256	KB
SRAM	64	KB

Serial hardware

ADC	14	bits
Use pins numbers only		

Default I²C = (1)
I²C (1) master only

Remove 5V shunt

Hardware

Pin number
Other pin number

PCSerial UART
SPI

analogRead()

digitalRead() and digitalWrite()
digitalRead(), digitalWrite()

and `analogWrite()`

[illegible]

Motors	PWMx	PWM signal
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Dfltx	Direction
nSLPx	/Sleep

Bumpers **BMPx** **Bumper**

Encoders	ELA	Encoder Let
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ELC	Encoder Left
ELB	Encoder Left
ERA	Encoder Right

ERA	Encoder Rig
ERB	Encoder Rig

LEDs	LEDBL	LED back lit
	LEDBR	LED backlit

LEDER LED back light
LEDFL LED front left
LEDER LED front right

Reflection QTR- R-Spectrum

Reflection	CTRL	Reflection
	CTRL	Control odd

IR	DISTx	IR distance
		IR distance

AUXx	IR distance
0.000	0.000

Servo FBx Servo

Screen

184

73	P1_1	PUSH1
74	P1_4	PUSH2
75	P2_0	RED_LED
76	P2_1	GREEN_LED
77	P2_2	BLUE_LED
78	P1_0	YELLOW_LED

J5	41	→	56
	57	→	72

	B		Extended	
	Motors	Reflection	Servo	Screen
LED				III
LEDBL				
LEDBR				
			FB I	DISTL
		CTRL/EVEN	/RST	AUXL
		QTR5 QTR0		
			SCX MOSI	
		QTR5 QTR7		
	ELA		+6V +3.3V	

[illegible]

	Base		Extended			
	LED	Motors	Reflection	IR	Screen	Servo
LEDCFR				DIRN		
						FB0 FB2
			CTRL00D	ALUR		
			QTR3			
			QTR1			
					ICS D/C	
LEDPL						
			QTR4			
			QTR6			
				ALXC		
		ERA				

TI-RSLK MAX Main Board

Extended configuration

Flash 256 KB
SRAM 64 KB

Serial hardware
ADC 14 bits
Use pins numbers only!
Default PC = (1)
PC (1) master only

△ Remove 5V shunt

Robot R +3.3V R +5V
LaunchPad +3.3 V +5V

LED
LEDFR 41
GROUND

GROUND
R +3.3V
SDA (1) 10
SCL (1) 9
R +5V

IR
GROUND
DISTR 42
AUXR 46

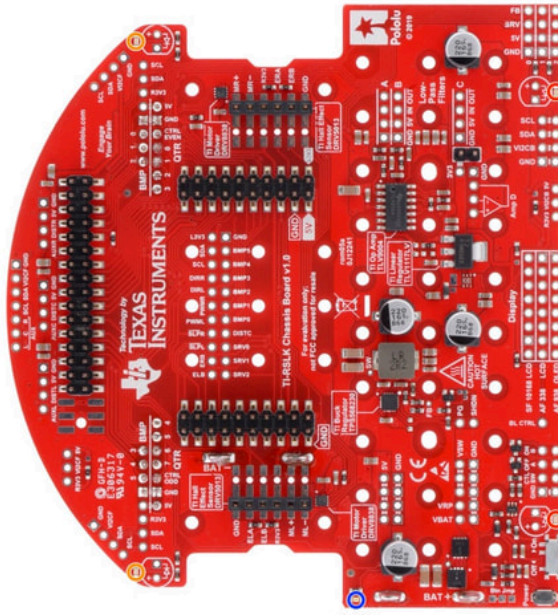
IR
GROUND
R +3.3V
SDA (1) 10
SCL (1) 9
AUXR 46
AUXC 54
AUXL 63

IR
GROUND
R +5V
DISTL 59
AUXL 63

R +5V
R +3.3V
R +3.3V

GROUND
R +3.3V
SDA (1) 10
SCL (1) 9
R +5V

LED
LEDFL 51
GROUND



POWER

Hardware
Pin number
Other pin number

PC
Serial UART
SPI

analogRead()
digitalRead() and digitalWrite()
digitalWrite(), digitalWrite() and analogWrite()

Op-Amp
OUT A B
IN A B
R +5V
GROUND

R +3.3V GROUND
OUT D
IN- D
IN+ D

Servos
FB0 43 FB1 60 FB2 44
SRV0 38 SRV1 32 SRV2 17
R +5V R +5V R +5V
GROUND GROUND GROUND

LED
LED8R 58
GROUND

SCL (1) 9
SDA (1) 10
R +3.3V
GROUND

R +5V
R +3.3V
R +3.3V

Screen
SF10168 AF338 AF338
R +5V GROUND R +5V
SCK 66 R +3.3V R +5V
MOSI 67 SCK 66
D/C 50 MOSI 67 /CS 49
/RST 62 D/C 50 /RST 62
/CS 49 /CS 49 D/C 50
GROUND /RST 62 SCK 66
R +3.3V BL=3.3 V MOSI 67

R +5V GROUND
R +5V GROUND
R +5V GROUND
R +5V GROUND

VSW GROUND
VSW GROUND
VSW GROUND
VBAT GROUND

ON B
OFF A
CTL SW
GROUND GROUND

LED
LED8L 57
GROUND

ON VSW reverse protected switched battery voltage
OFF VBP reverse protected battery voltage before power switch
VBAT direct battery voltage (no reverse protection)
R +5V regulated 5V output of TI-RSLK chassis board
R +3.3V regulated 3.3V output of TI-RSLK chassis board