2.4GHz Impedance Matched Balun + embedded FCC/ETSI Band Pass Filter For Texas P/N: 2450BM14G0011 Instruments CC2620, CC2630, CC2640, CC2650 chipsets operated on INTERNAL BIAS MODE

Detail Specification: 8/23/2016 Page 1 of 4

For the Full App Note and Layout Files, go to: www.johansontechnology.com/ti

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General Specifications			6
Part Number	2450BM14G0011		
Frequency (MHz)	2400 - 2500		
Unbalanced Impedance	50 Ω		61
Balanced Differential Impedance	Conjugate match to TI CC2620, CC2630, CC2640, CC2650, chipsets operated on INTERNAL BIAS MODE		
	INTERNAL BIAS MODE	Phase Difference (deg.)	
Insertion Loss when component	1.5 Typ (1.8dB max40C to+85C)	Amplitude Dif	ference
measured by itself (passive insertion loss)		Power Capacity	
	101000)	Qty/Reel (pcs)	
Return Loss (dB)	urn Loss (dB) 9.5 min.		np. Range
Attenuation Differential mode	Storage Temp	. Range	
25 typ. / 14dB min. @	Recommende Conditions of	Unused	
		Product on Ta	4K



1		
	Phase Difference (deg.)	180 ± 10
	Amplitude Difference	2.0 max.
	Power Capacity	2W max (CW)
	Qty/Reel (pcs)	4,000
	Operating Temp. Range	-40 ~ +85°C
	Storage Temp. Range	-40 ~ +85°C
	Recommended Storage Conditions of Unused Product on T&R	+5 ~ +35 °C, Humidity 45-75%
	Storage Period	18 months max.

Do you need hellpe selecting the best mini or micro 2.4GHz antenna for your application? Send us a message at: http://www.johansontechnology.com/ask-a-question and go to: http://www.johansontechnology.com/antennas

Part Number Explanation					
P/N Suffix	Packaging Style	Bulk	Suffix = S	E.g 2450BM14G0011S	
		T&R	Suffix = T	E.g 2450BM14G0011T	
	Termination Style	100% Tin	Suffix = None	E.g 2450BM14G0011(T or S)	

	Mechanical Dimensions					
Inches		Millimeter		ter		
L	0.063 ±	0.004	1.6	±	0.10	
W	0.031 ±	0.004	0.8	±	0.10]
T	0.024 ±	0.004	0.6	±	0.10	
а	0.008 ±	0.004	0.2	±	0.10	L C
b	0.008 +0.	1/-0.15	0.2	+0.1	/-0.15	
С	0.006 ±	0.004	0.15	±	0.10]
g	0.012 ±	0.004	0.3	±	0.10	
р	0.020 ±	0.002	0.5	±	0.05	
						
						3

20 typ. / 15dB min. @ 7200-7500 MHz

Terminal Configuration						
No	Function	No	Function			
1	Unbalanced Port (IN)	4	Balanced Port (OUT)			
2	NC	5	GND			
3	Balanced Port (OUT)	6	GND			
	(3) (4)	②⑤	6			

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Detail Specification: 8/23/2016 Page 2 of 4

Mounting Considerations 0.25 0.5 0.4 0.5 Units in mm

*Line width should be designed to match 50Ω characteristic impedance, depending on PCB material and thickness.

Land

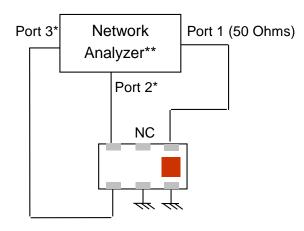
Through-hole ($\phi 0.3/\phi 0.2$) vias to GND

Would you like us to provide the layout files of the TI chipset + 2450BM14G0011? Review your layout for free? Please go to this link to contact our RF team:

www.johansontechnology.com/ask-a-question "Applications Engineering" on the drop down question type

Do you need the layout/gerber files of the above? Go to: www.johansontechnology.com/ti or send us a message to review your layout at: http://www.johansontechnology.com/ask-a-question

Measuring Diagram



Port 1:Unbalanced Port

Ports 2 and 3: Balanced Port

 $IL=S_{ds21}$

 $RL=S_{ss11}$

 $Amp_balance = dB(S(2,1)/S(3,1))$

Phase balance = Phase(S(2,1)/S(3,1))

- *Impedance for ports 2 and 3
- = Conjugate to Balanced Impedance/2
- **E5071C from Agilent

You can download the s-parameters at: http://www.johansontechnology.com/ti

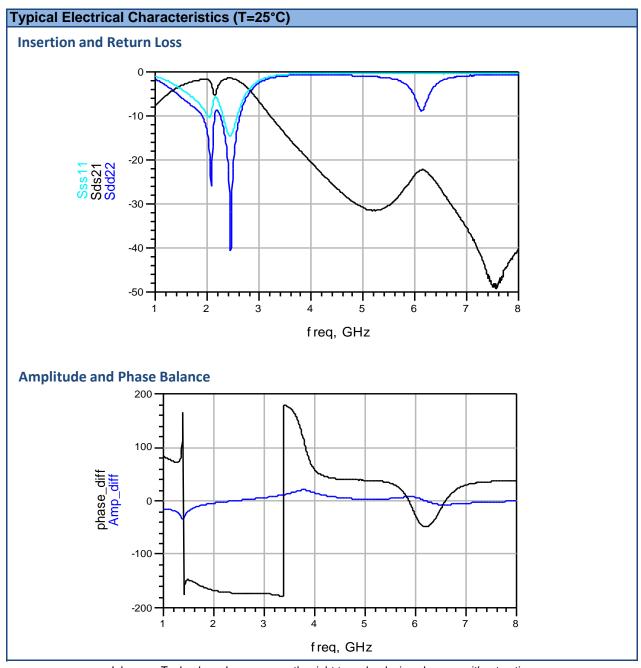
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Detail Specification: 8/23/2016 Page 3 of 4



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P/N: 2450BM14G0011

Detail Specification: 8/23/2016 Page 4 of 4

Application Notes, Layout Files, and more

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