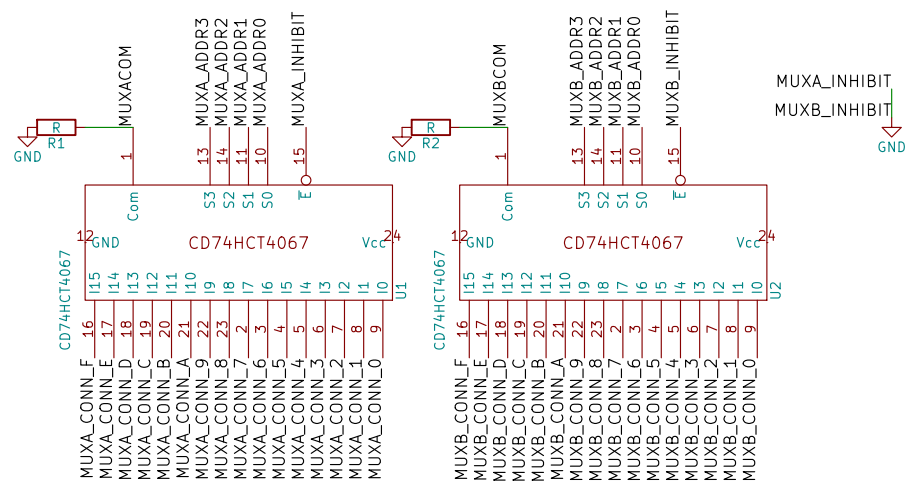
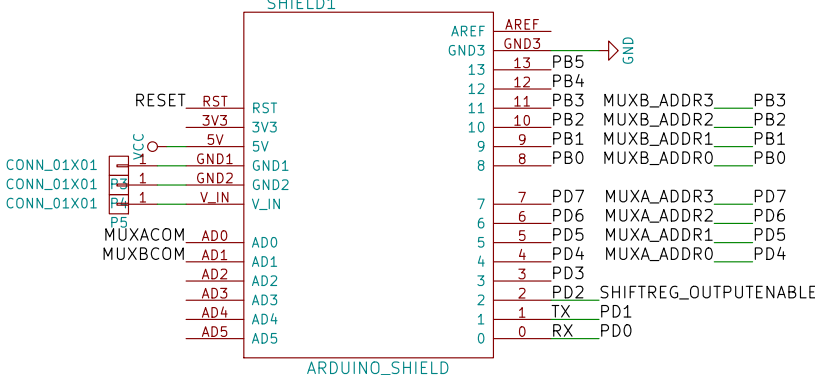


MUX SECTION



ARDUINO TO HARDWARE CONNECTIONS



example code for checking button number 1
DDRB |= 0x0F;
DDRD |= 0x0F;

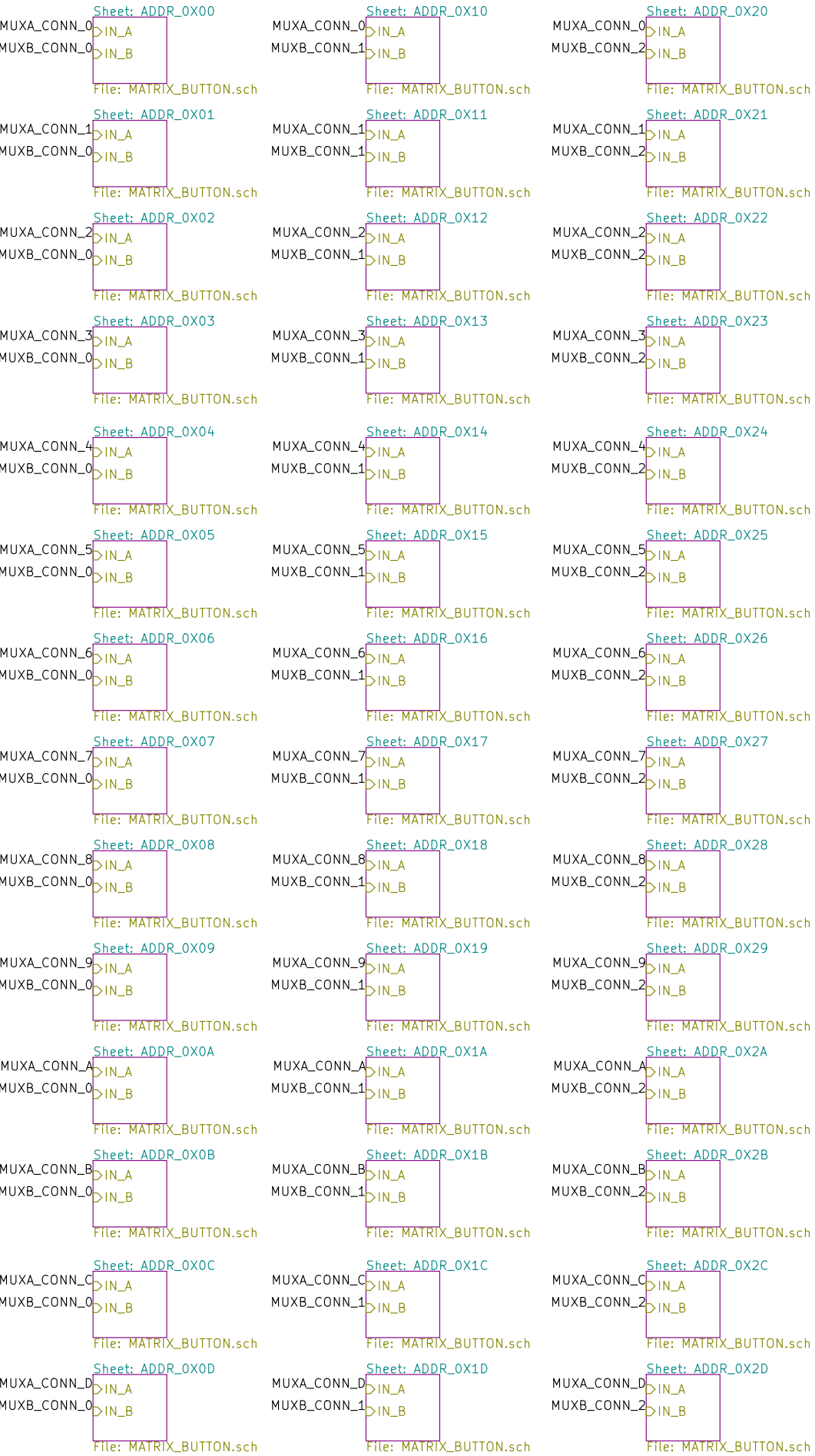
//SET MUX
char mask=0xF0&PIND;
PORTD=mask & 0X1<<4;
char mask=0X0F&PINB;
PORTB=mask & 0X0;

//SET MUXA HIGH
digitalWrite(A0,HIGH);

//READ RESULTING VALUE IN ANALOG
boolean status=digitalRead(A1);

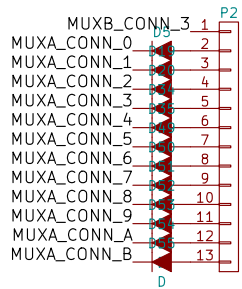
BUTTON MATRIX
ADDRESS:
0X00 TO 0X0D
1X00 TO 1X0D
2X00 TO 2X0D
diodes can be jumped here

ALLOCATION (pins used here)
MUXA_CONN_0 -> MUXA_CONN_D
MUXB_CONN_0 -> MUXB_CONN_2

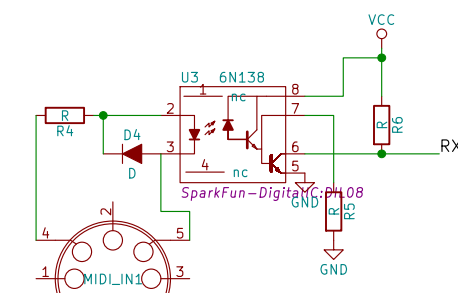


ROTARY SWITCH WITH 12 POSITIONS
ADDRESS:0X30 TO 0X3A
*diodes can be jumped here

ALLOCATION (pins used here)
MUXA_CONN_0 -> MUXA_CONN_B
MUXB_CONN_3 -> MUXB_CONN_3

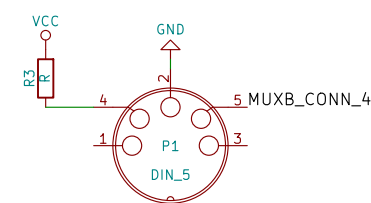


MIDI/ SERIAL IN
USING RX PIN



MIDI OUT
ADDRESS:
0X4_

ALLOCATION (pins used here)
MUXB_CONN_4



RESET BUTTON



LED SERIAL INTERFACE
ADDRESS:
0X5_

0X5_

0X5_

0X5_

0X5_

0X5_

0X5_

0X5_

0X5_

0X5_

0X5_

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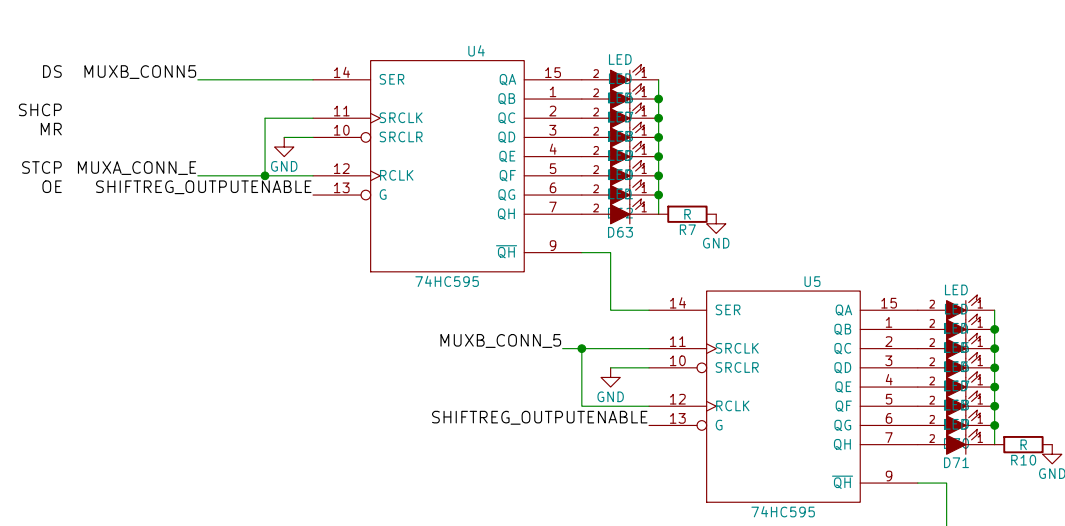
0X5_

0X5_

0X5_

0X5_

ALLOCATION (pins used here)
MUXB_CONN_5
MUXA_CONN_E



LED SCREEN 5
ADDRESS:
0X_F clock
0X7_ 0isDisabled
0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

0X_F clock

0X7_ 0isDisabled

0X6_ screen data in

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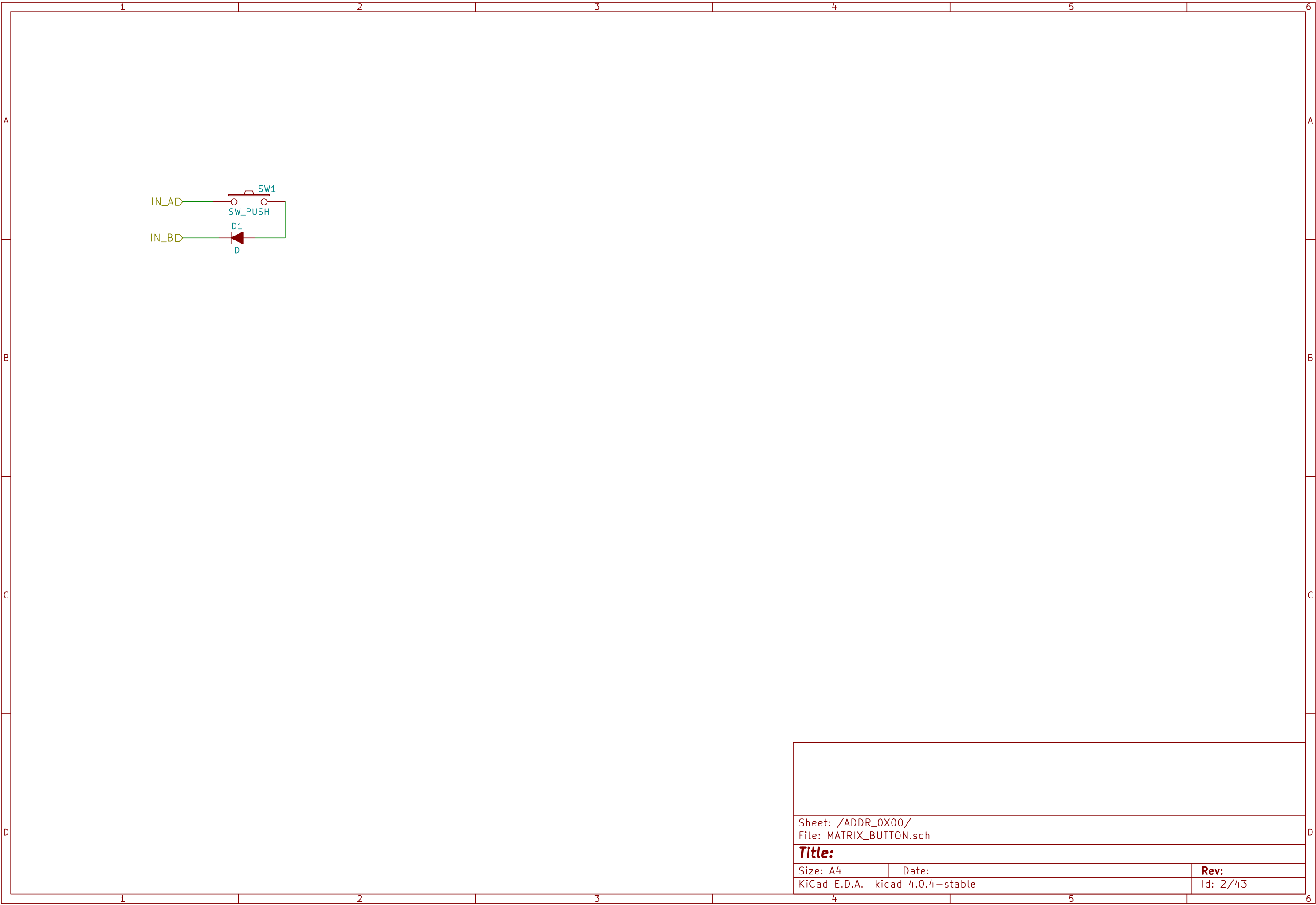
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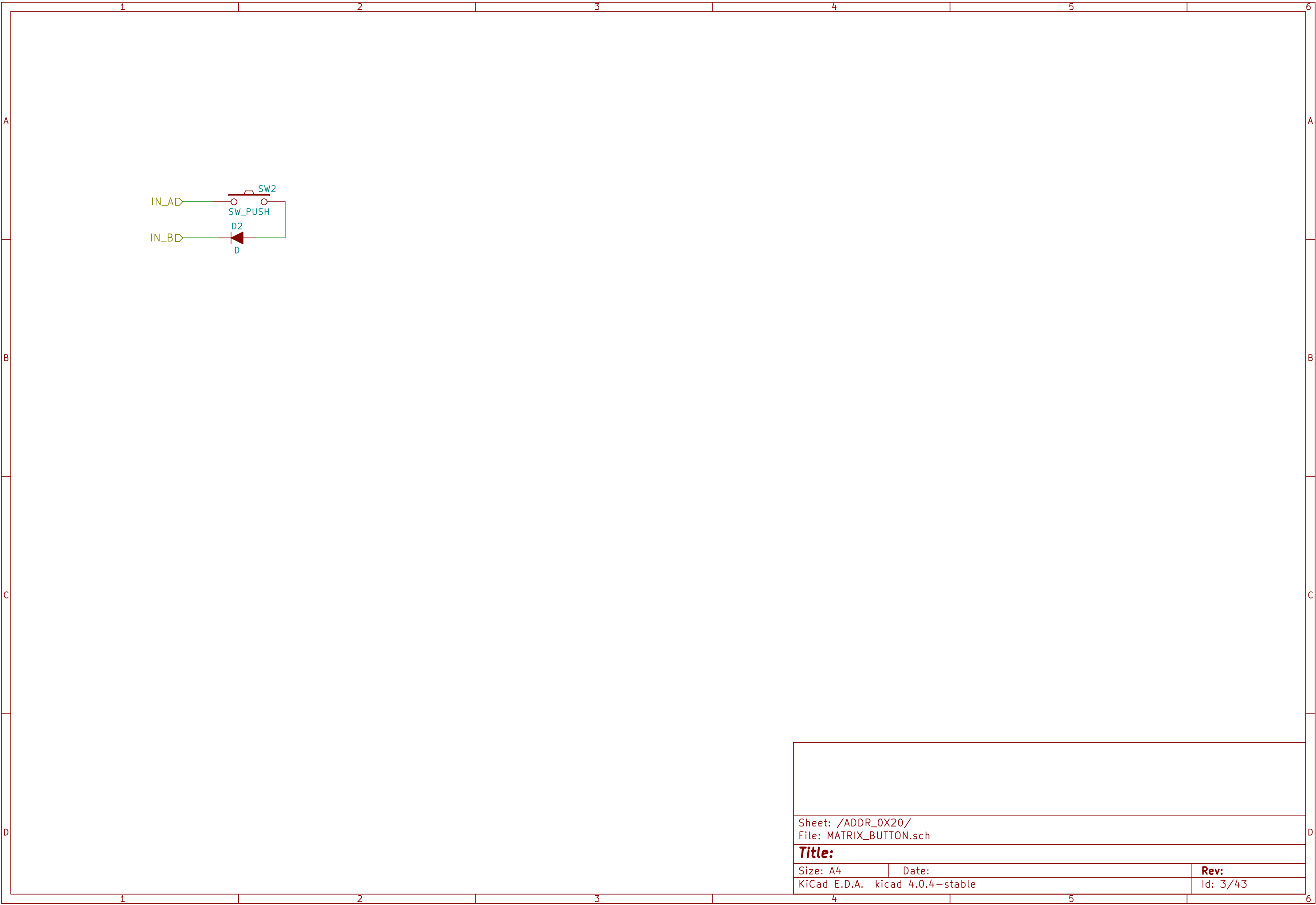
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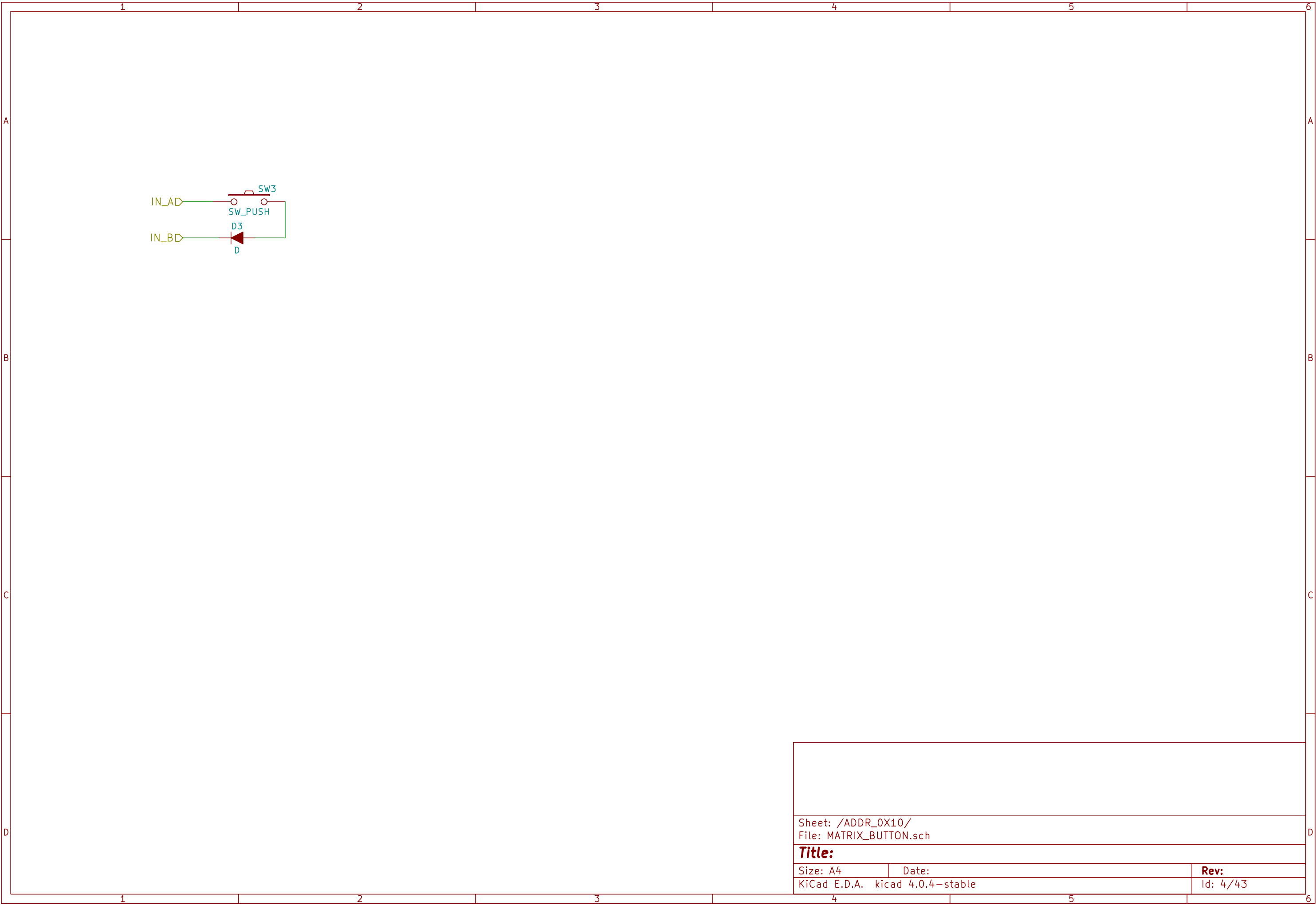
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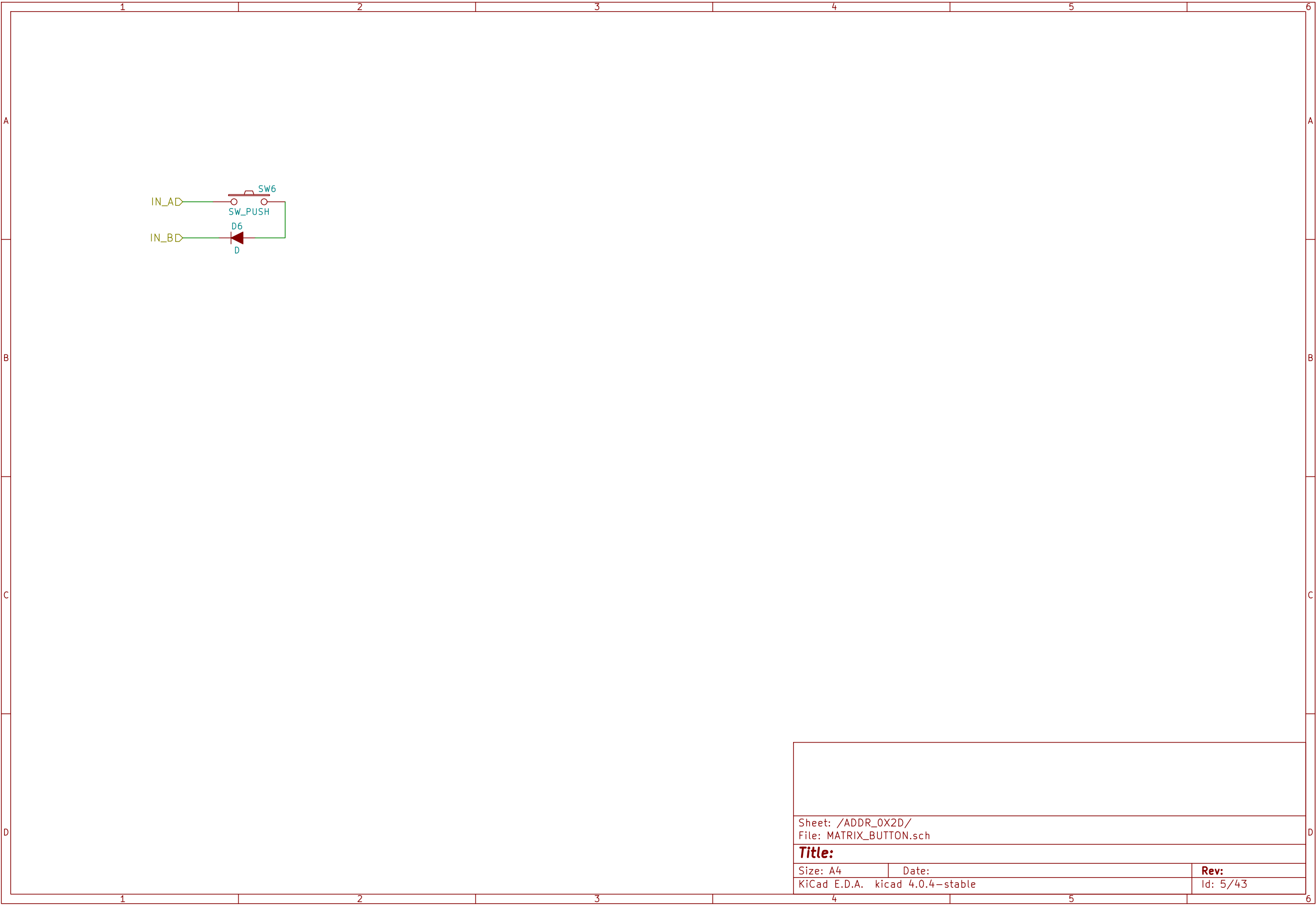
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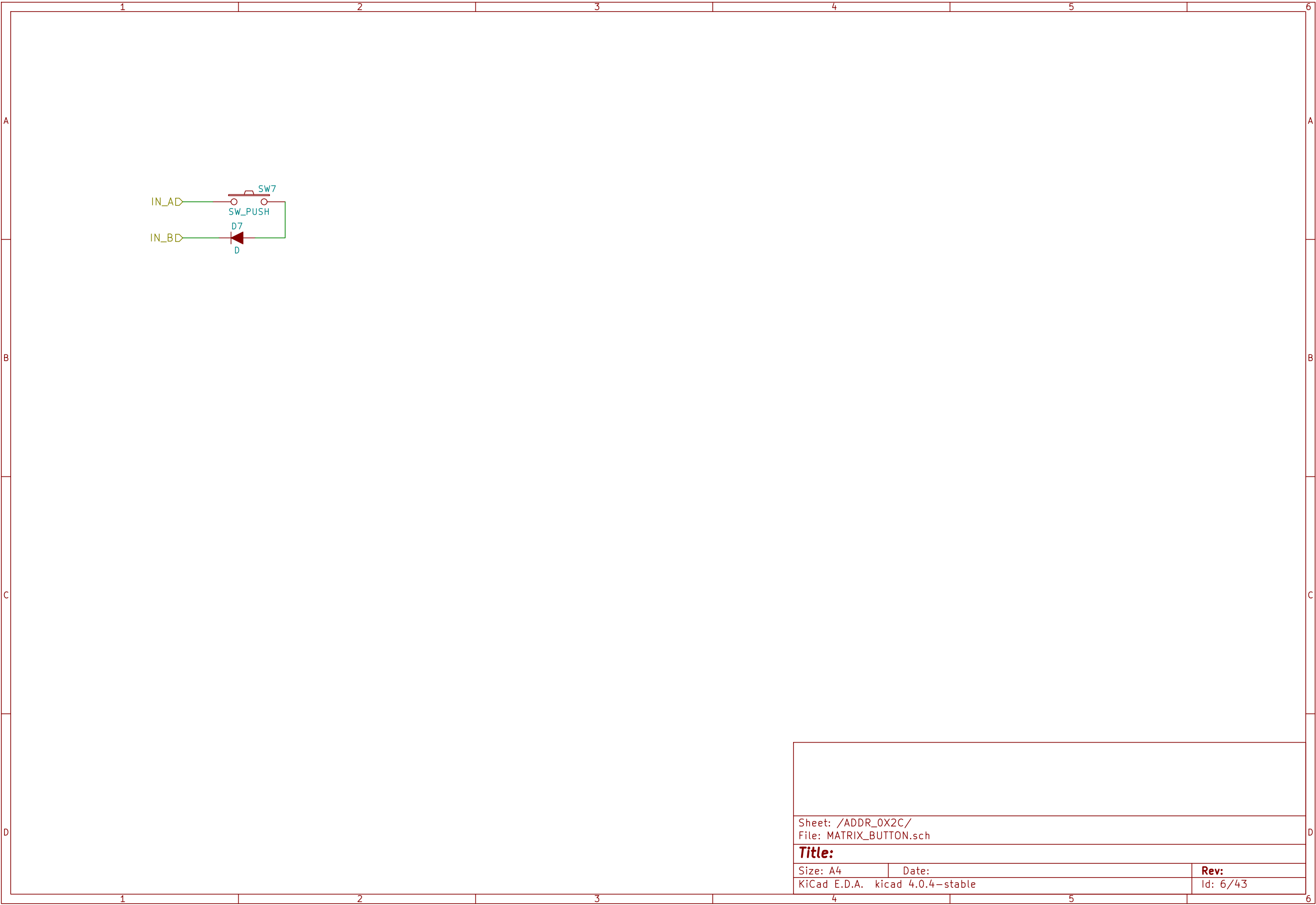
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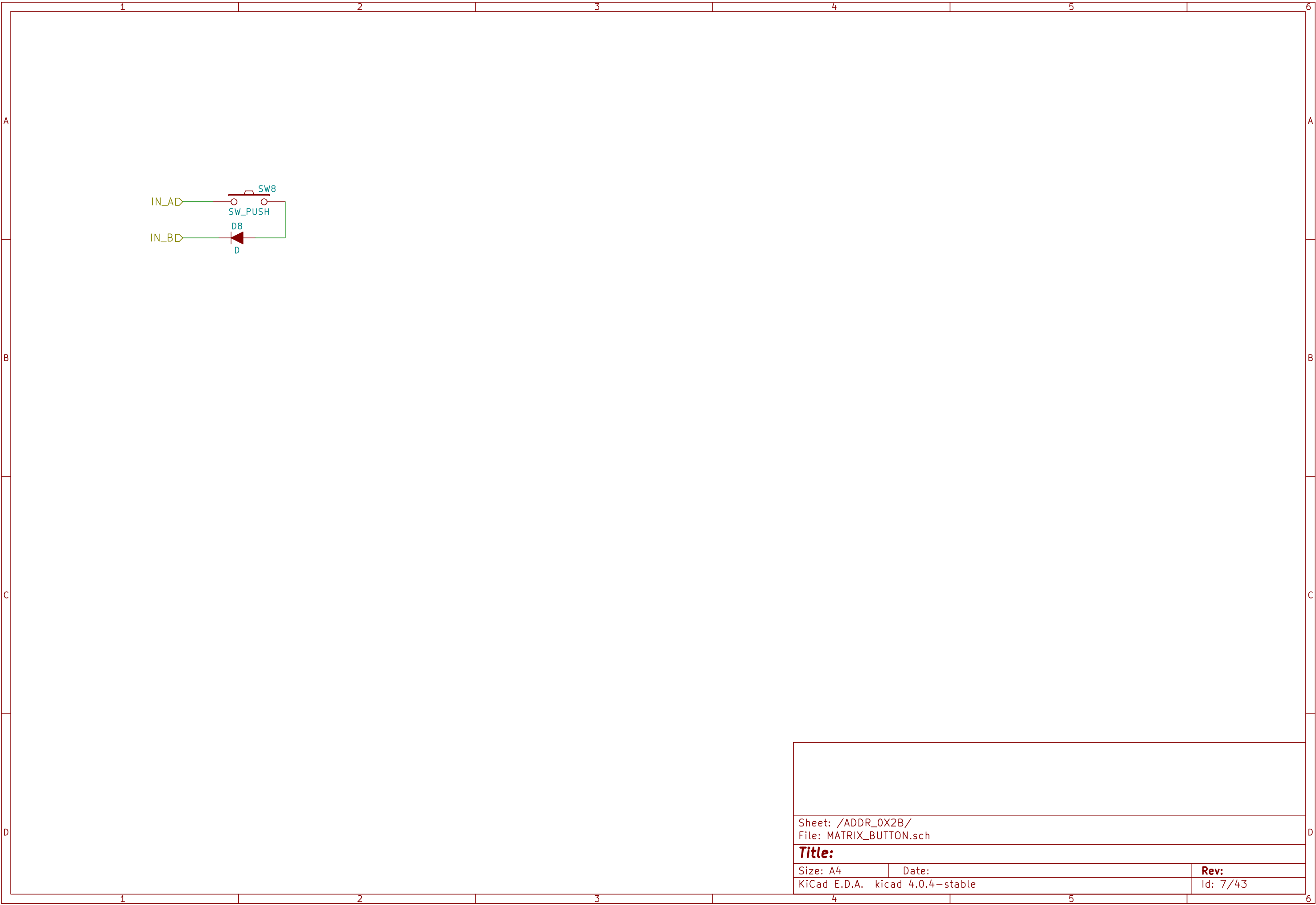


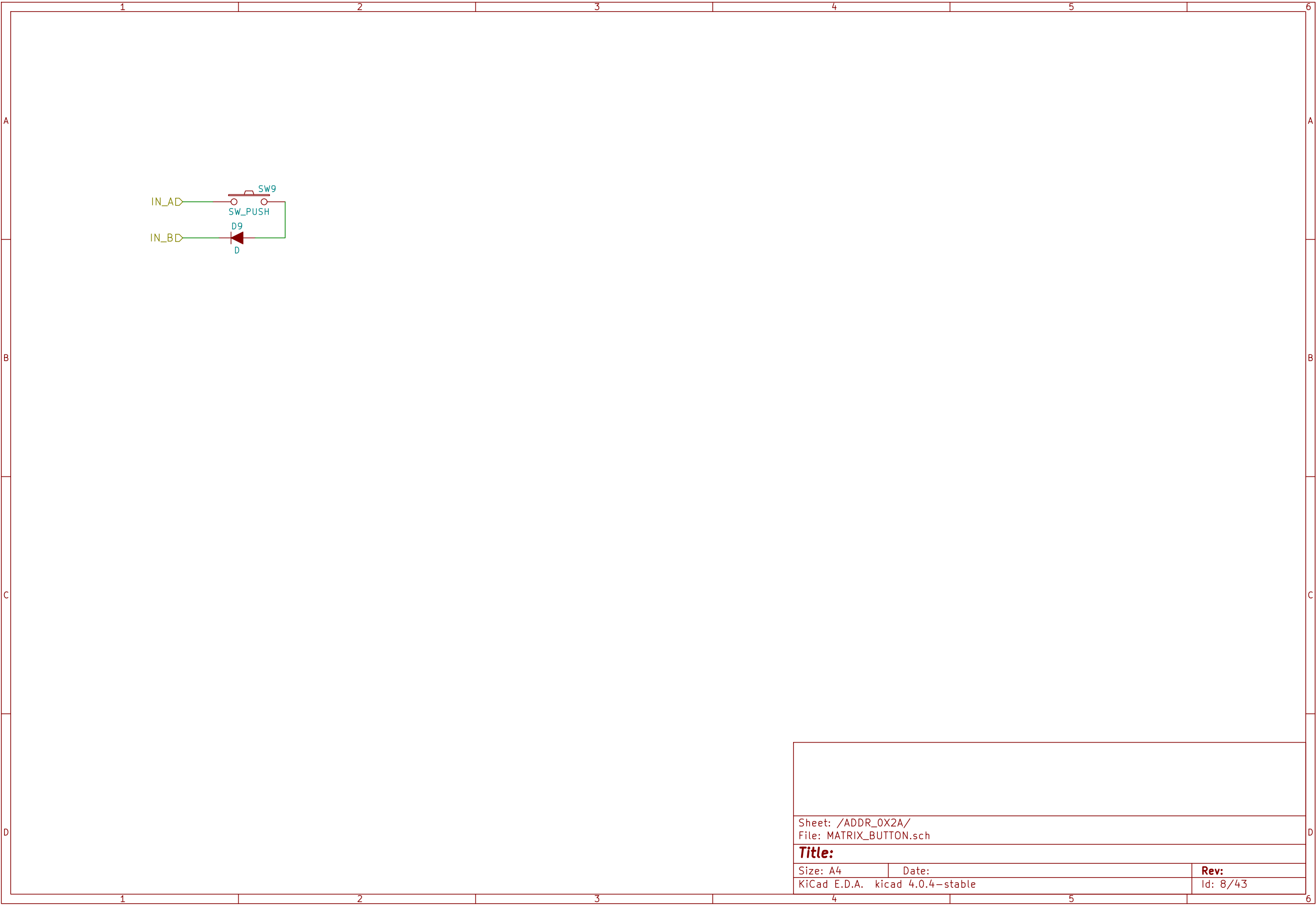


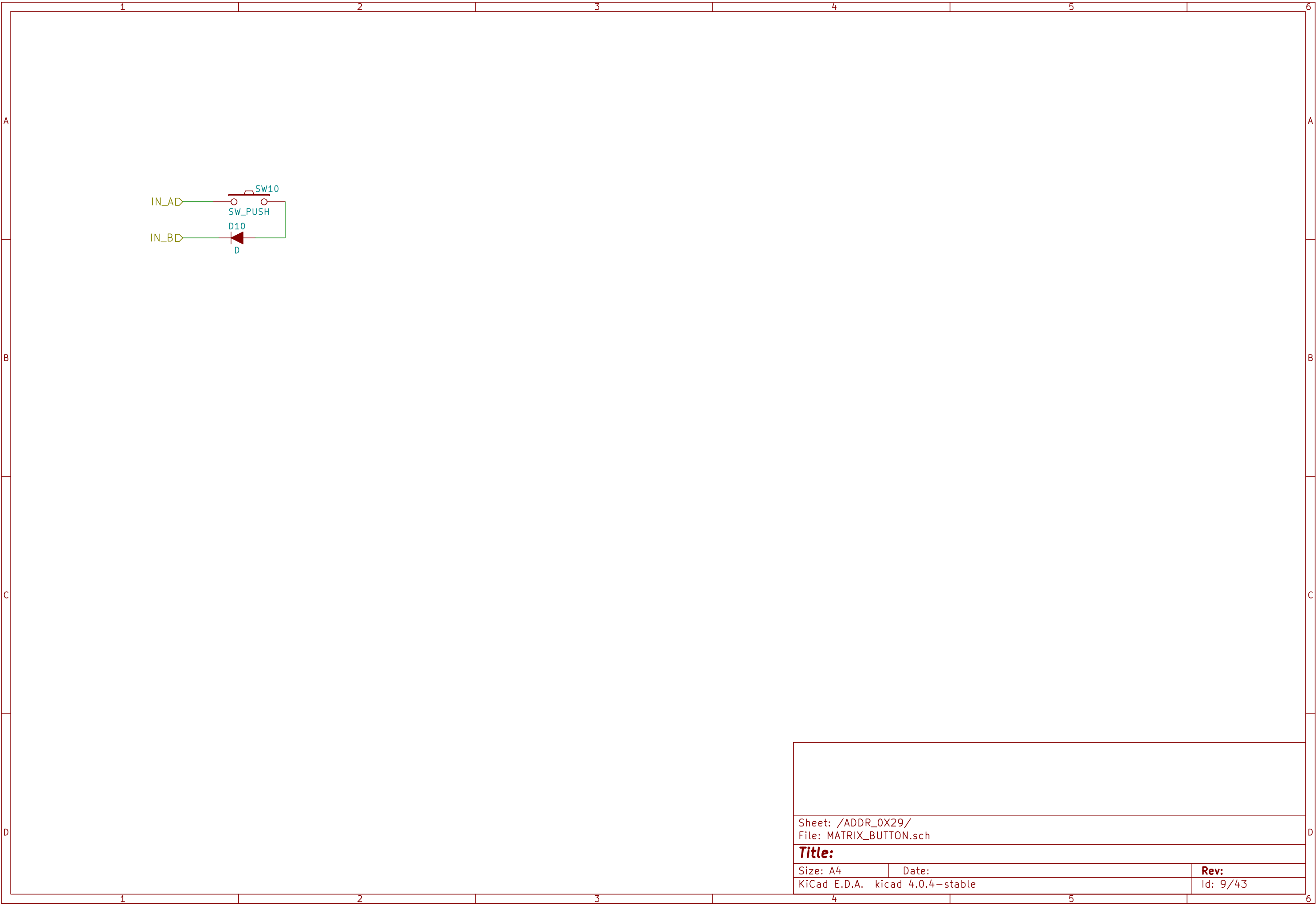


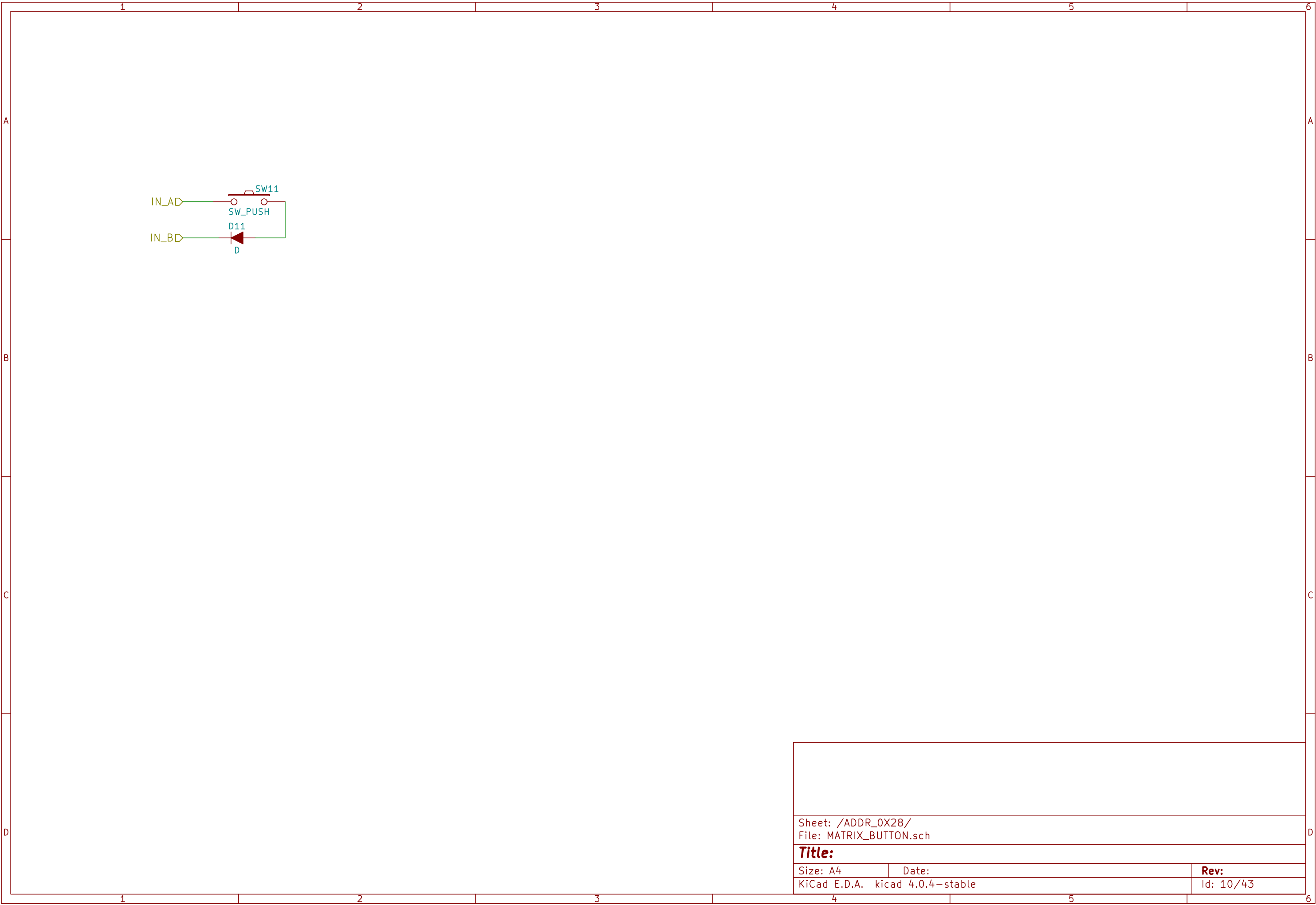


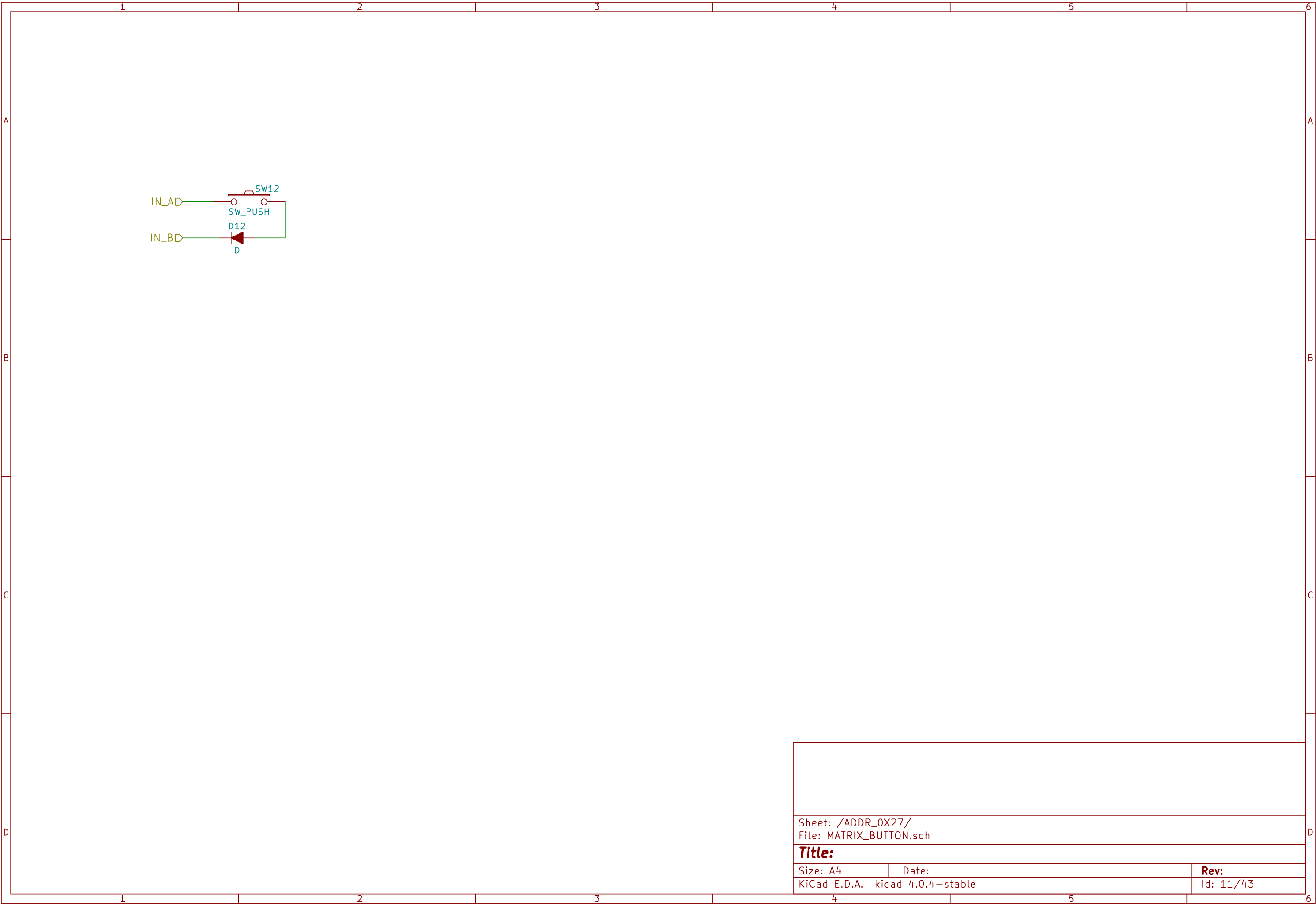


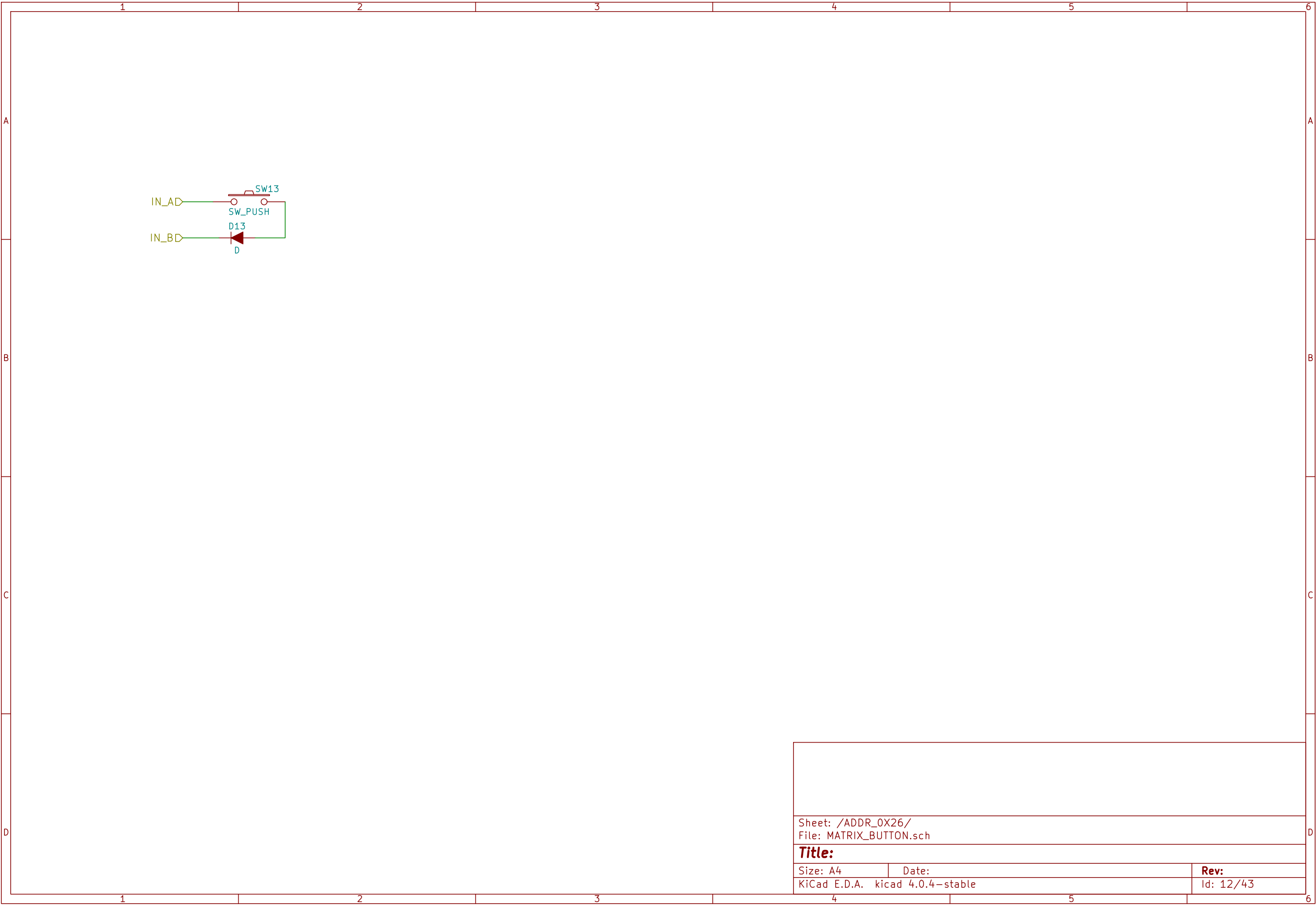


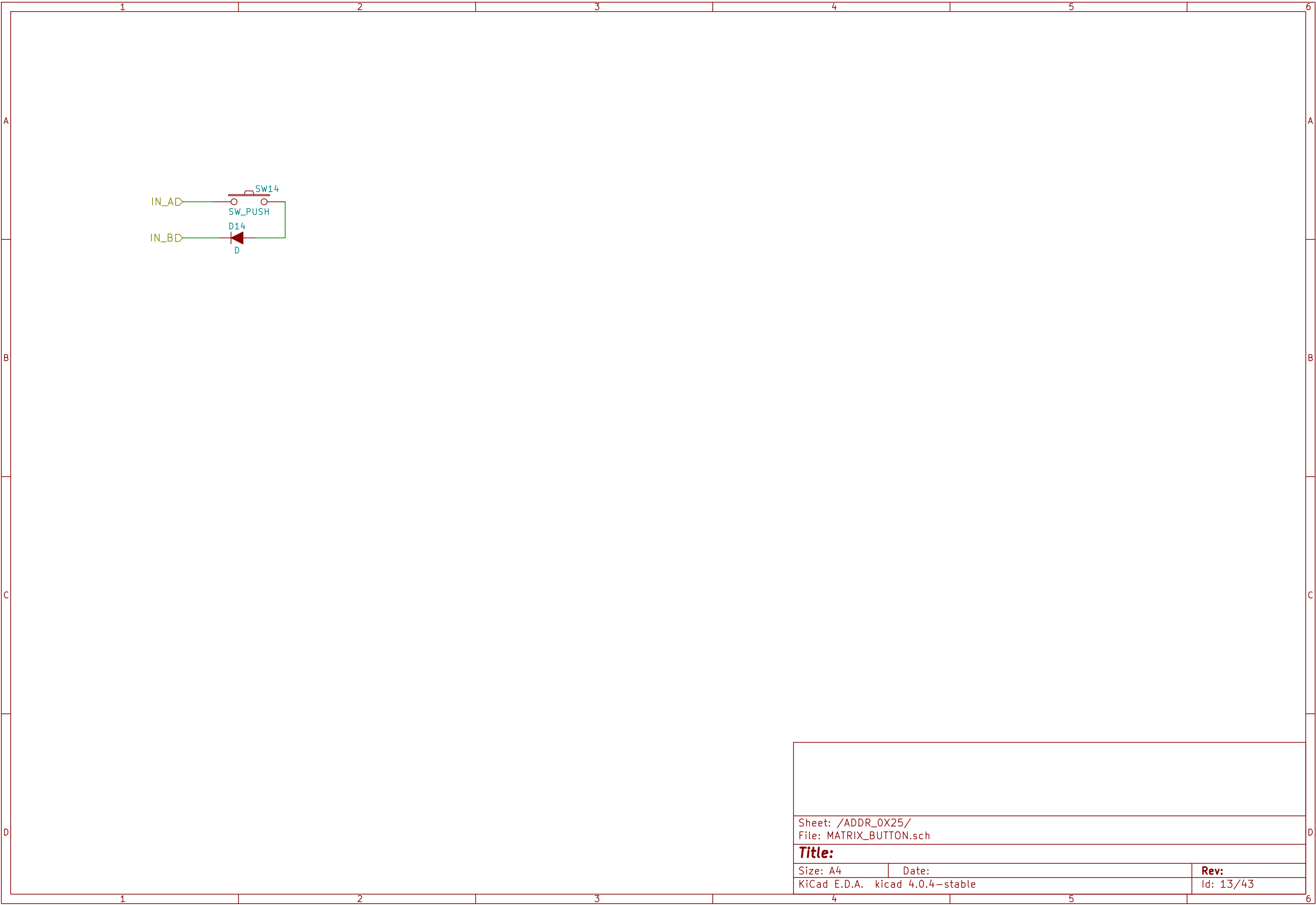


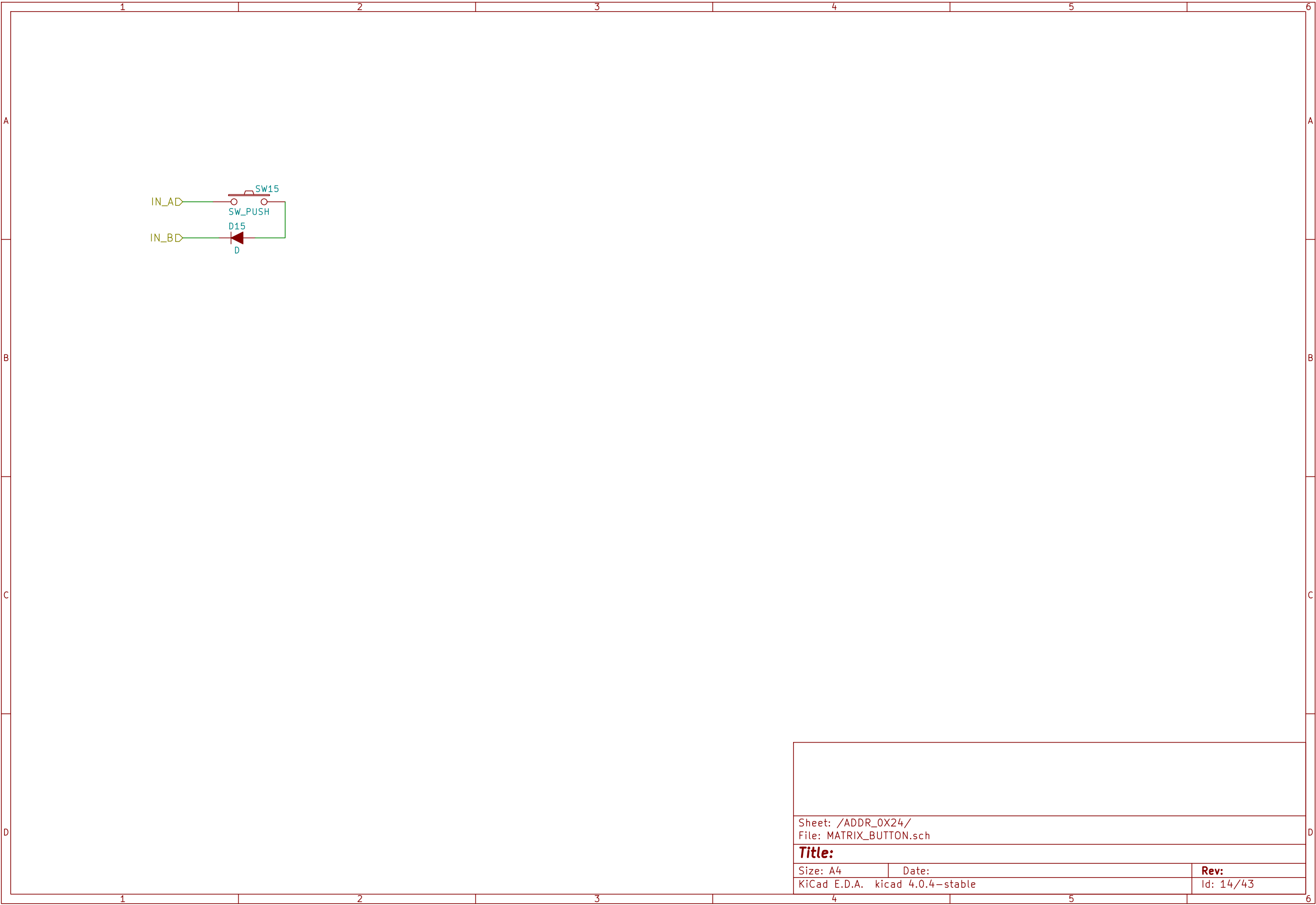




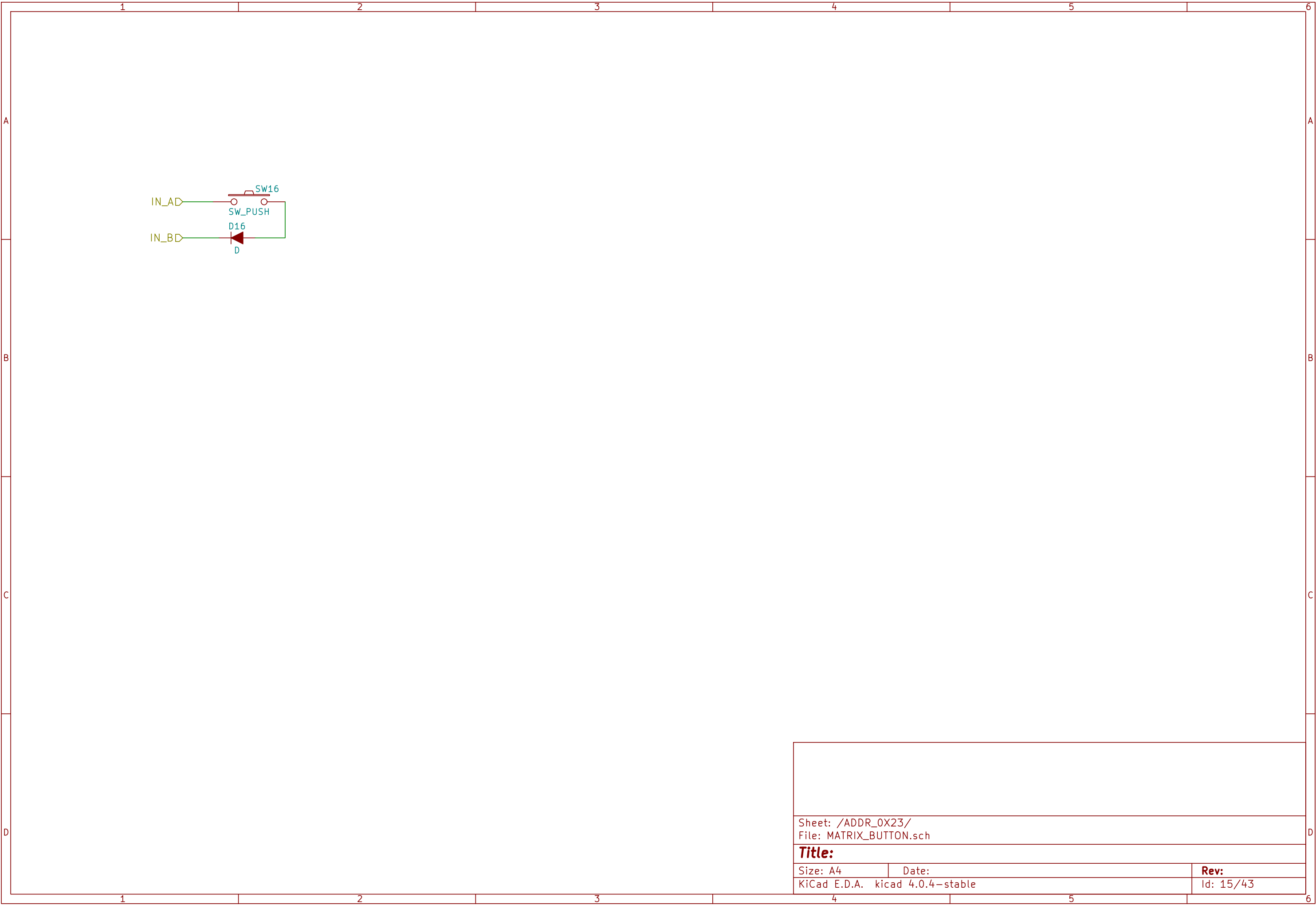


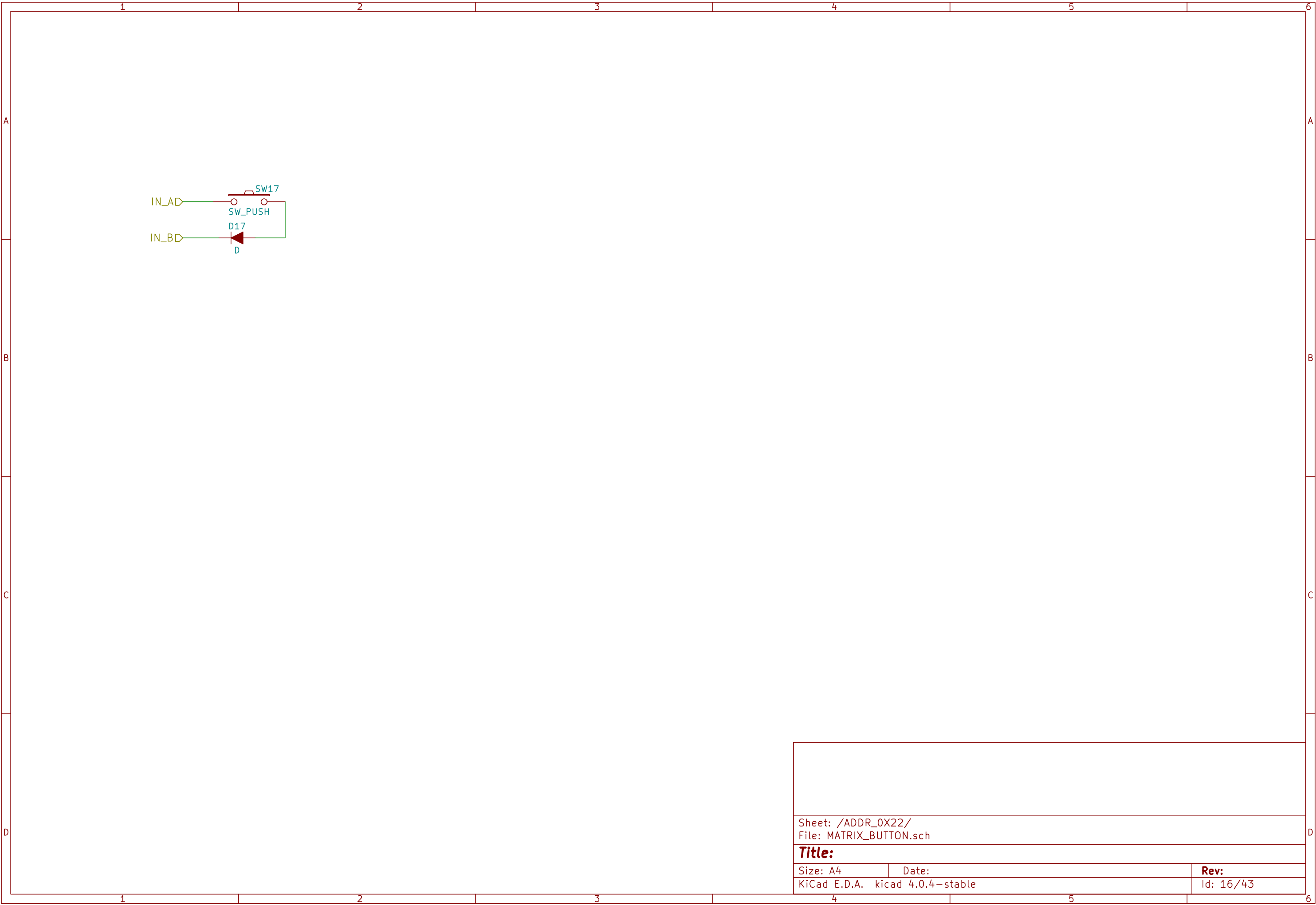


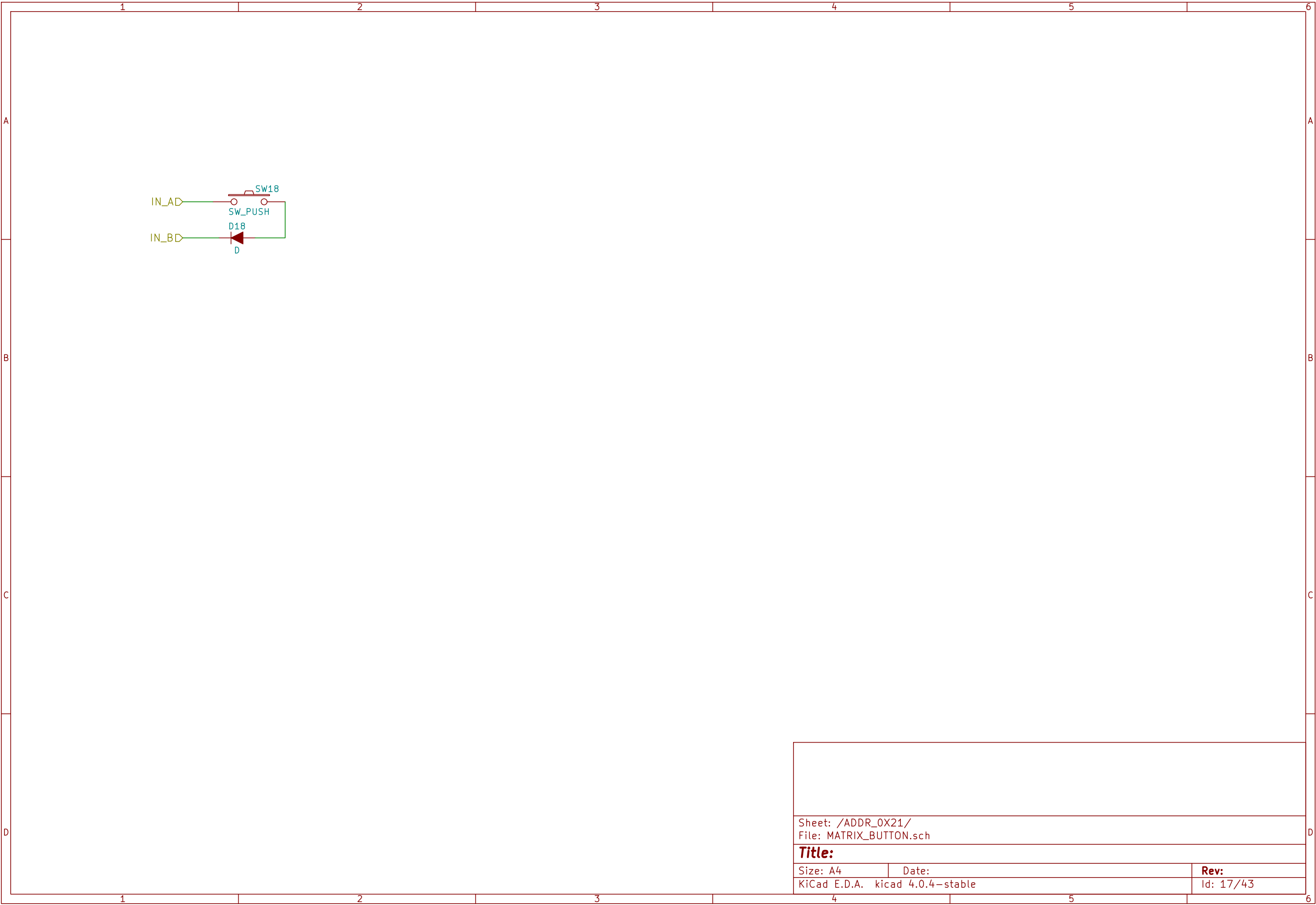


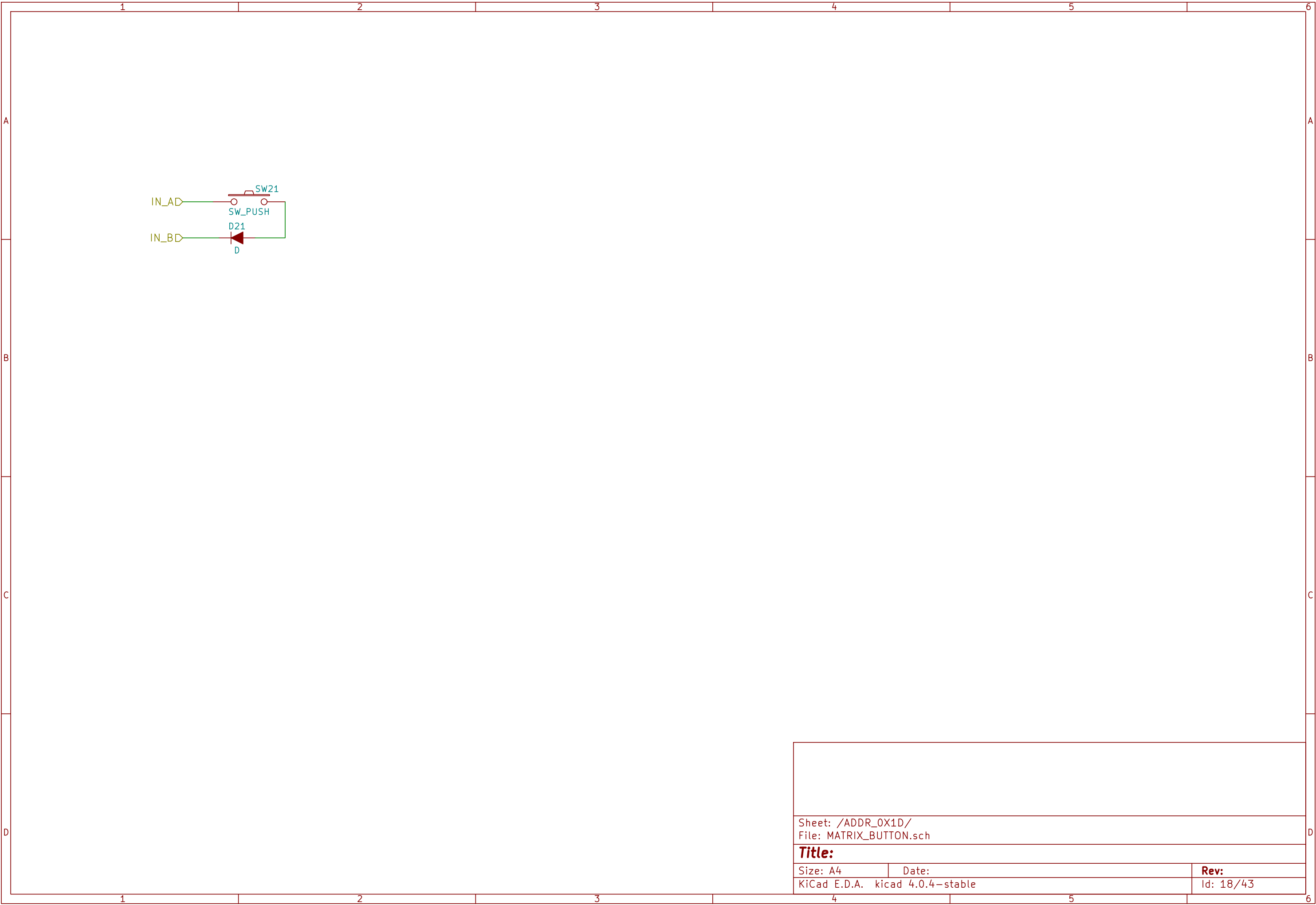


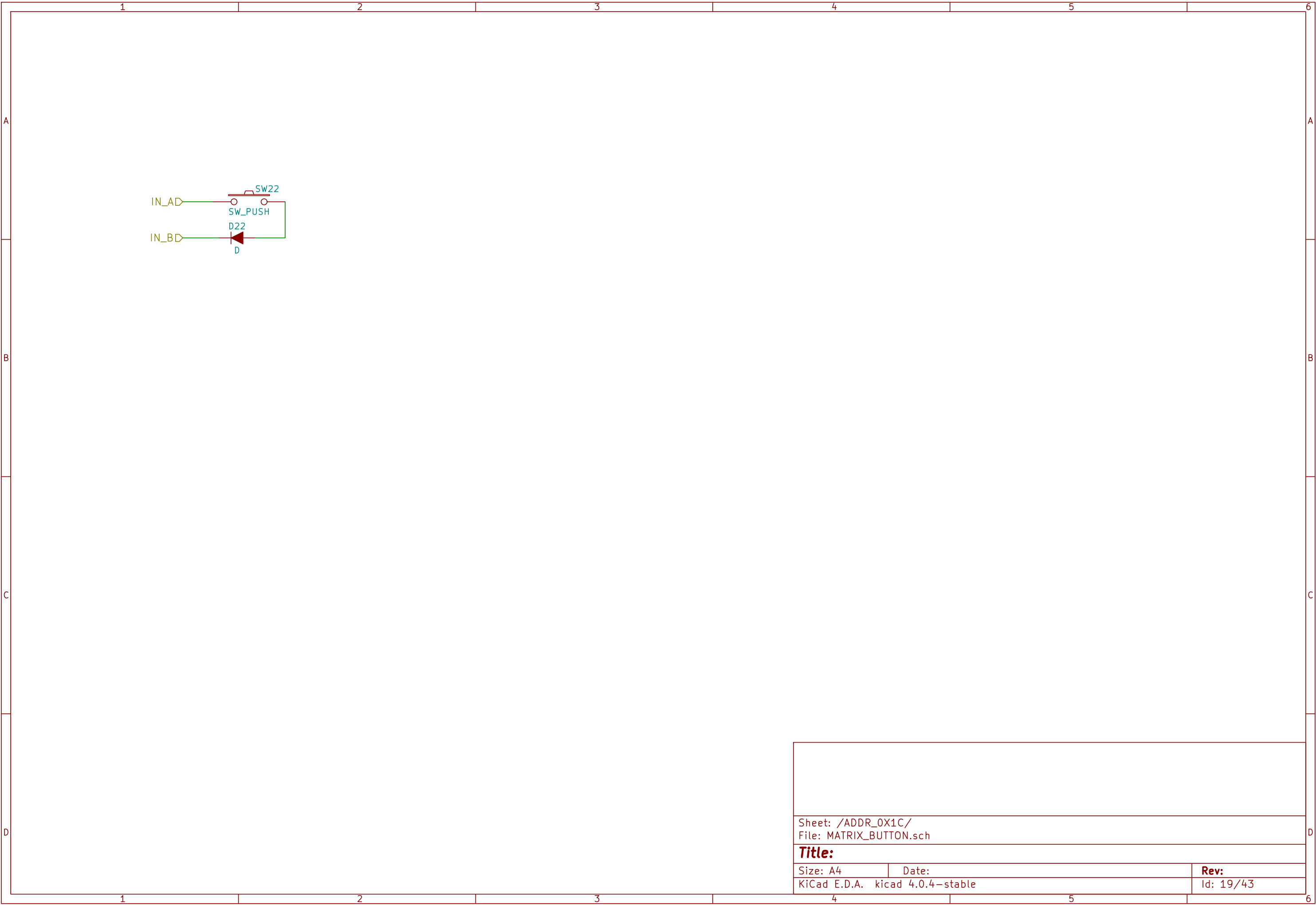
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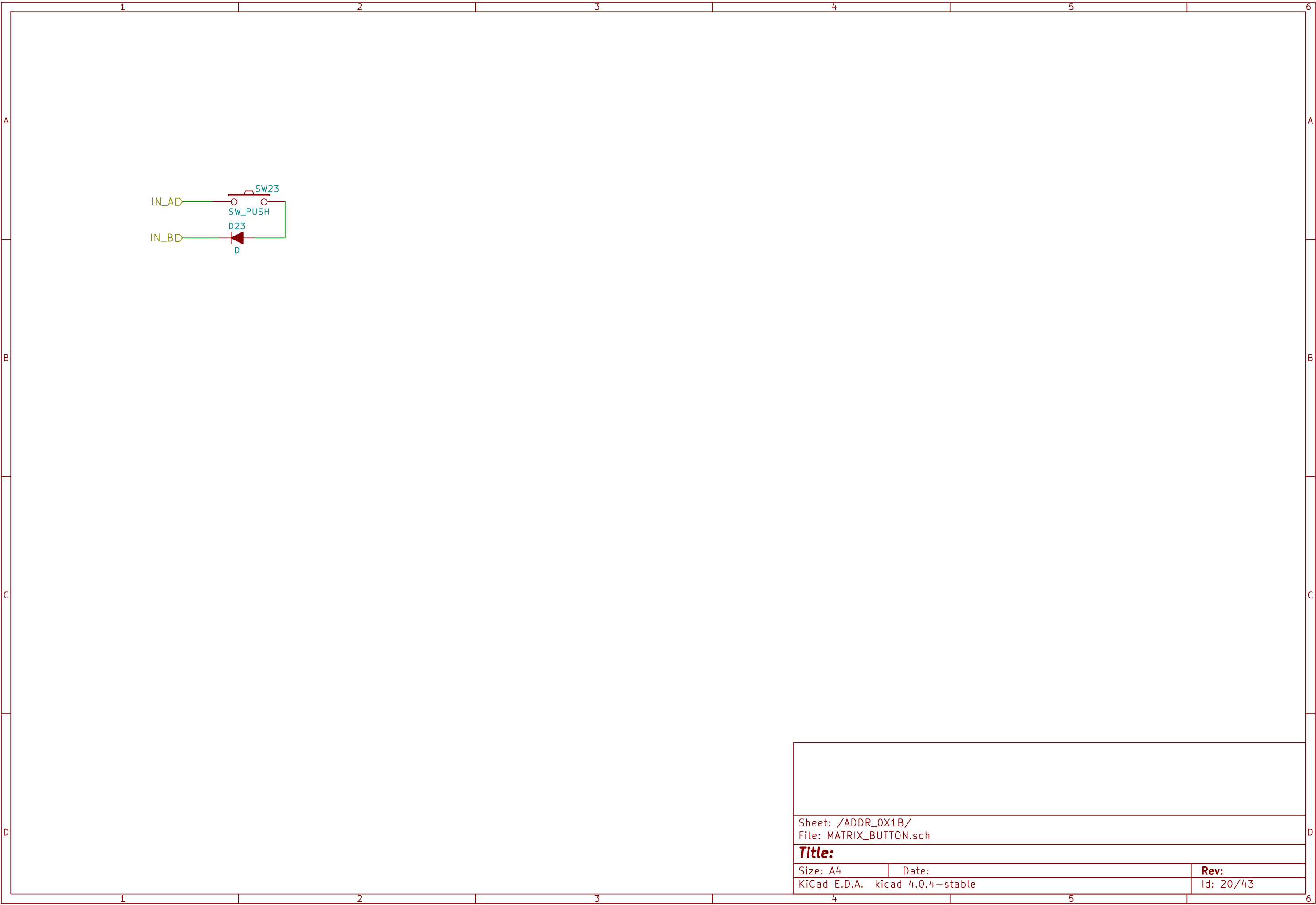


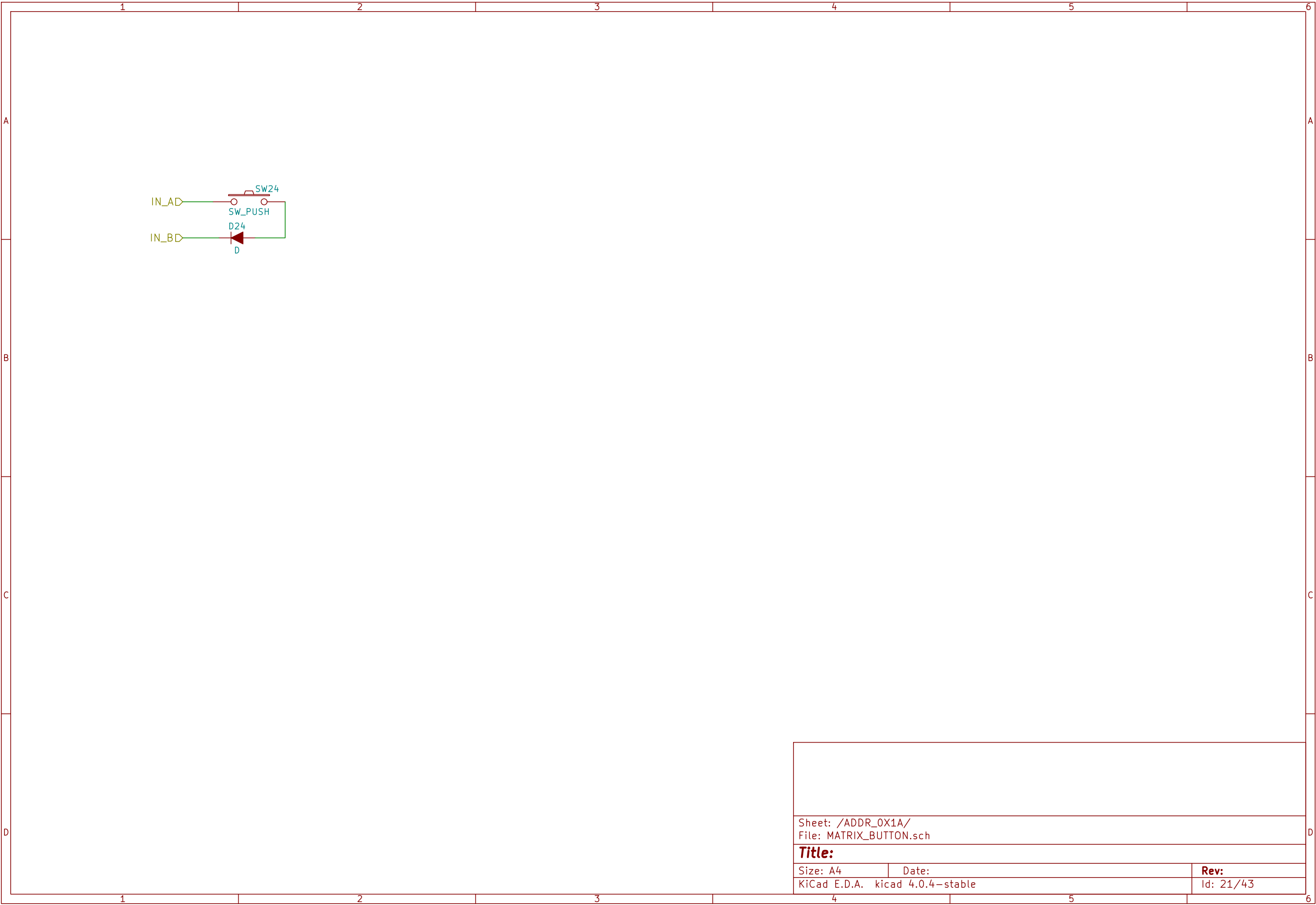


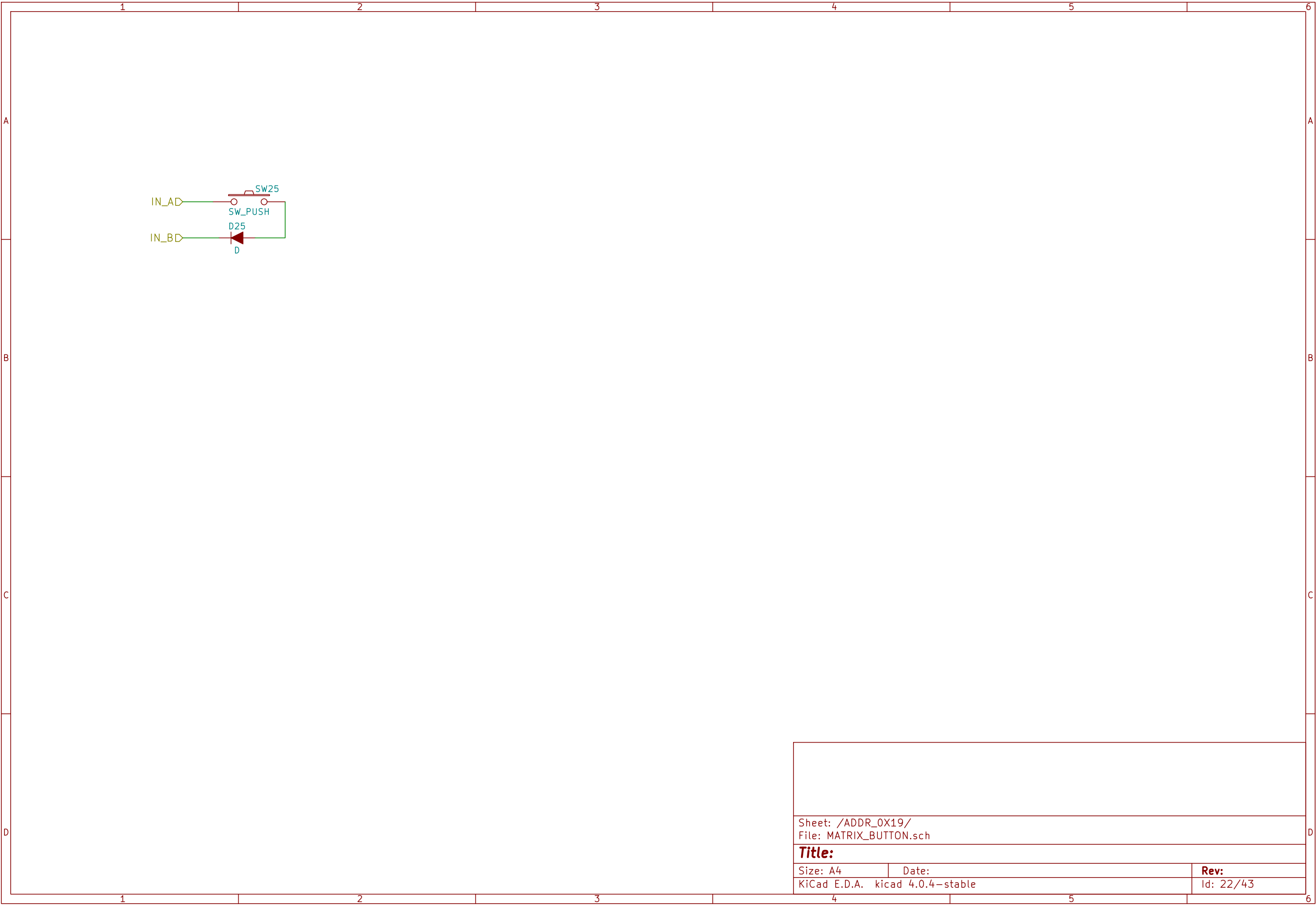


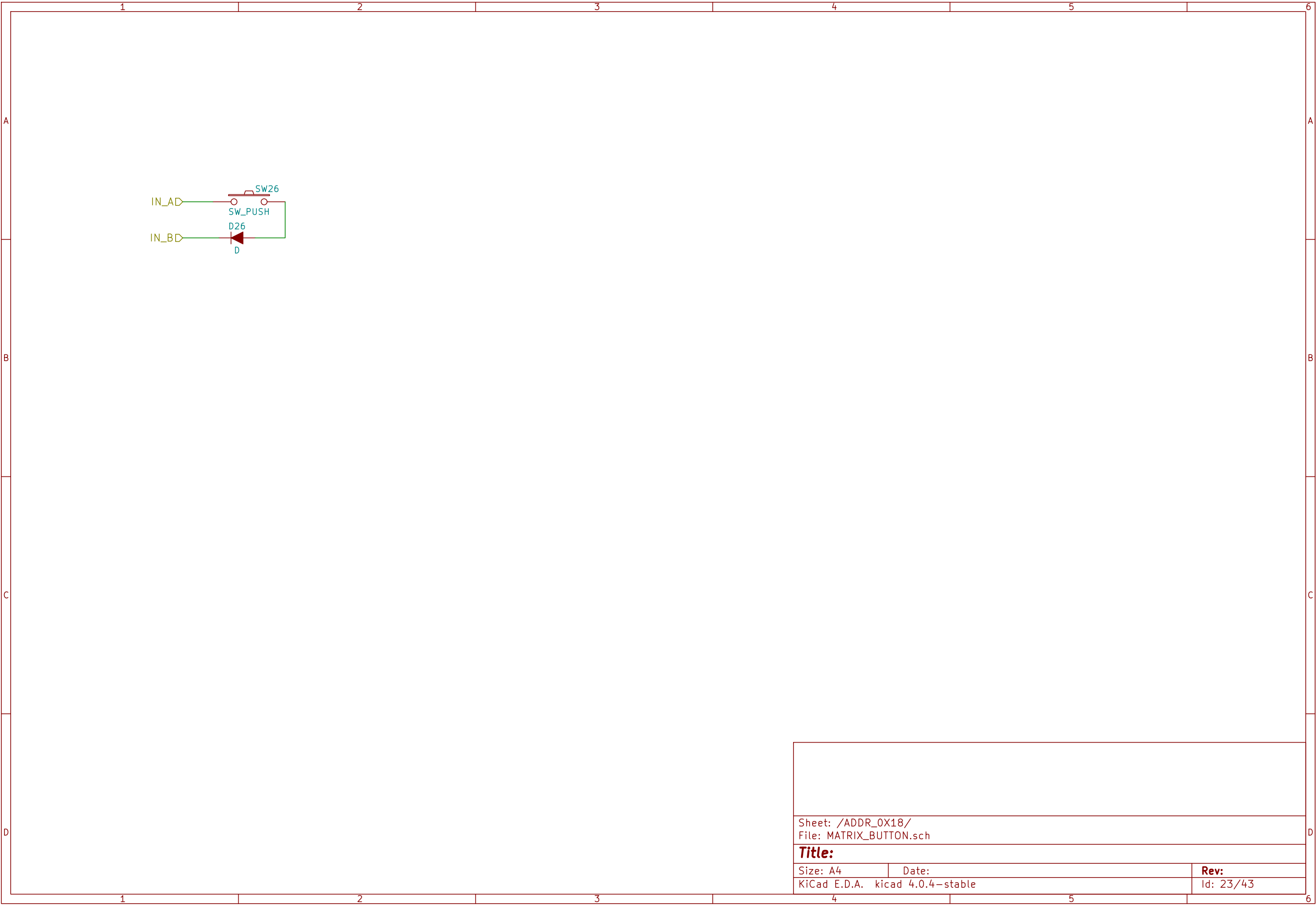


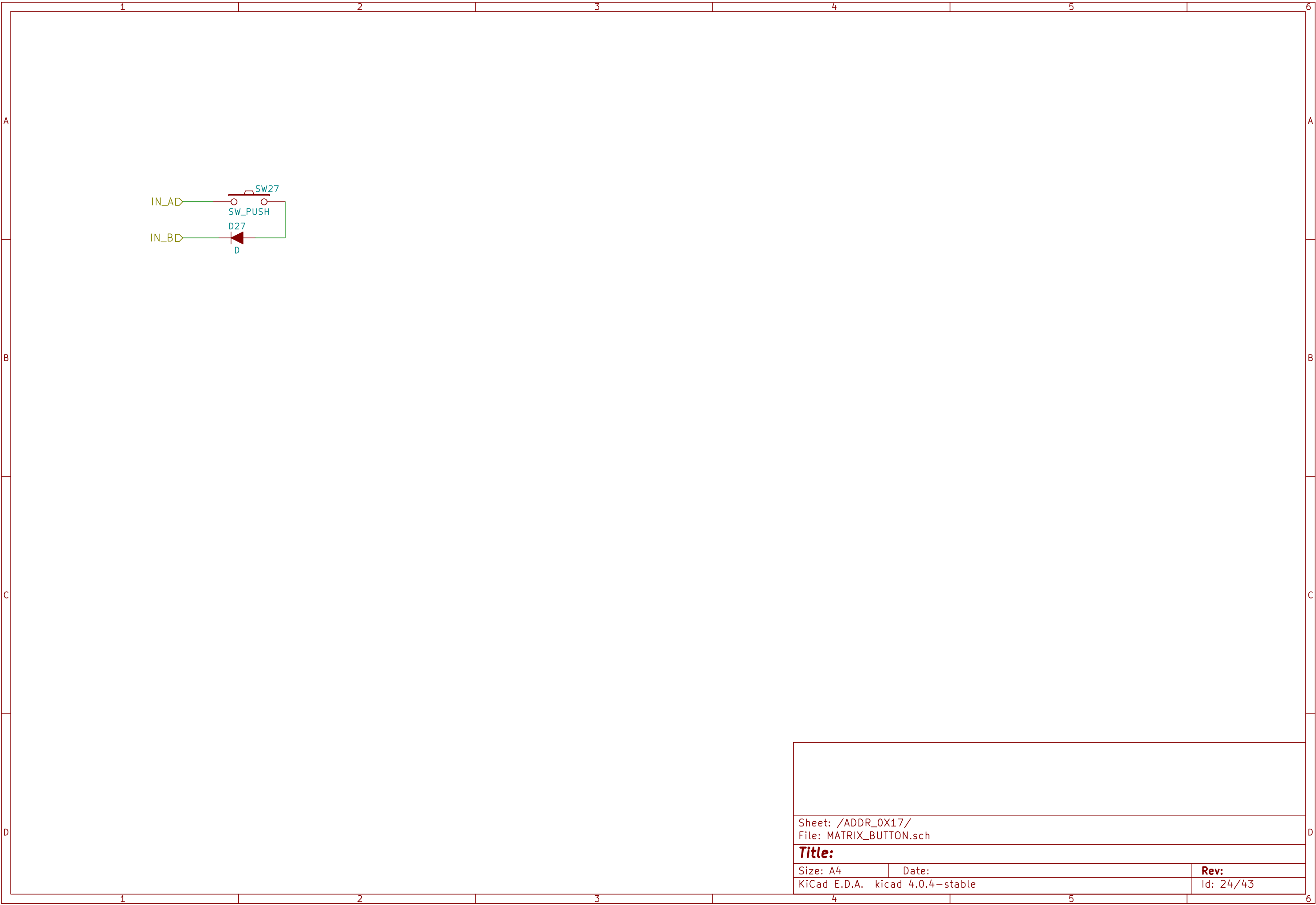


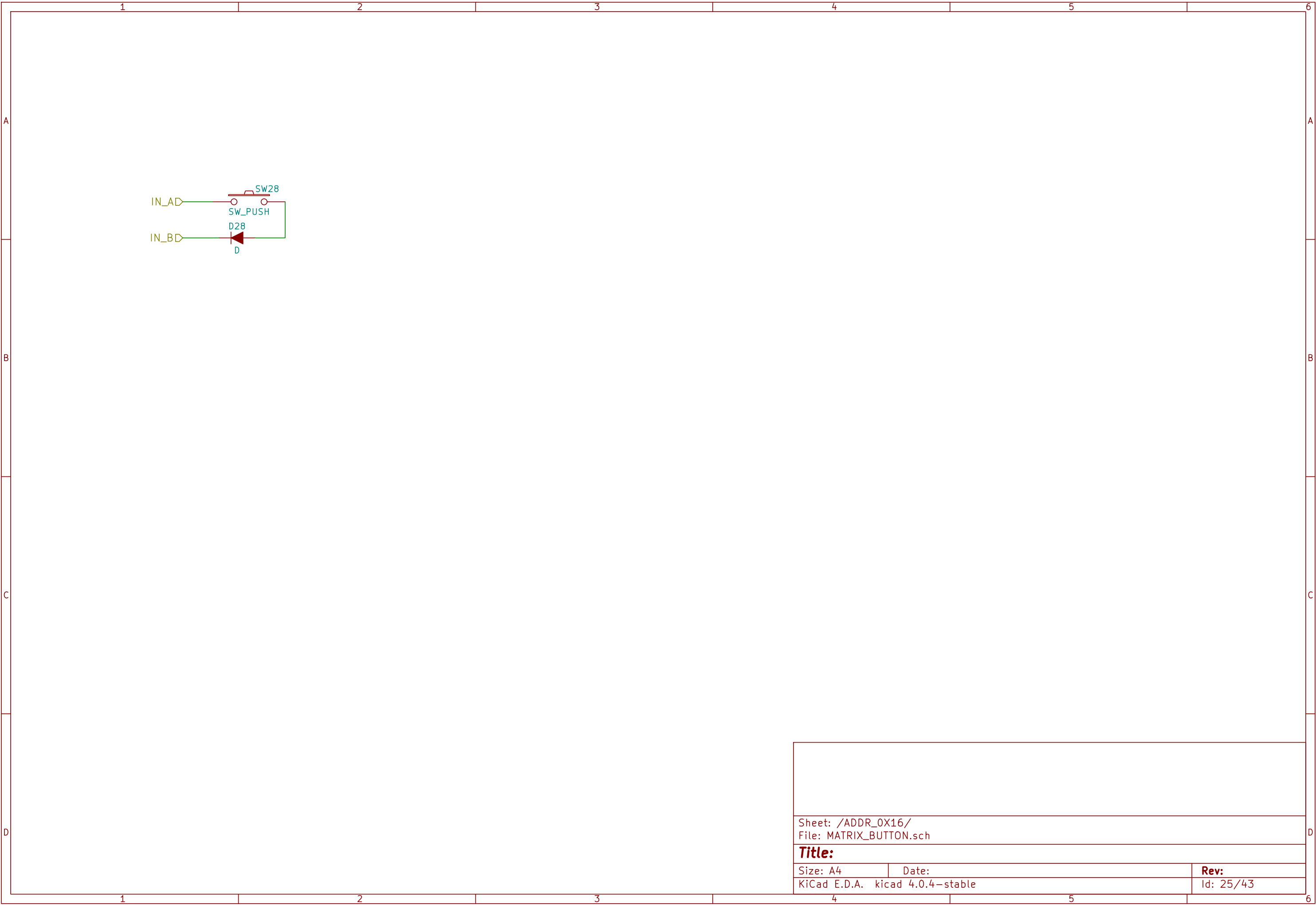












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