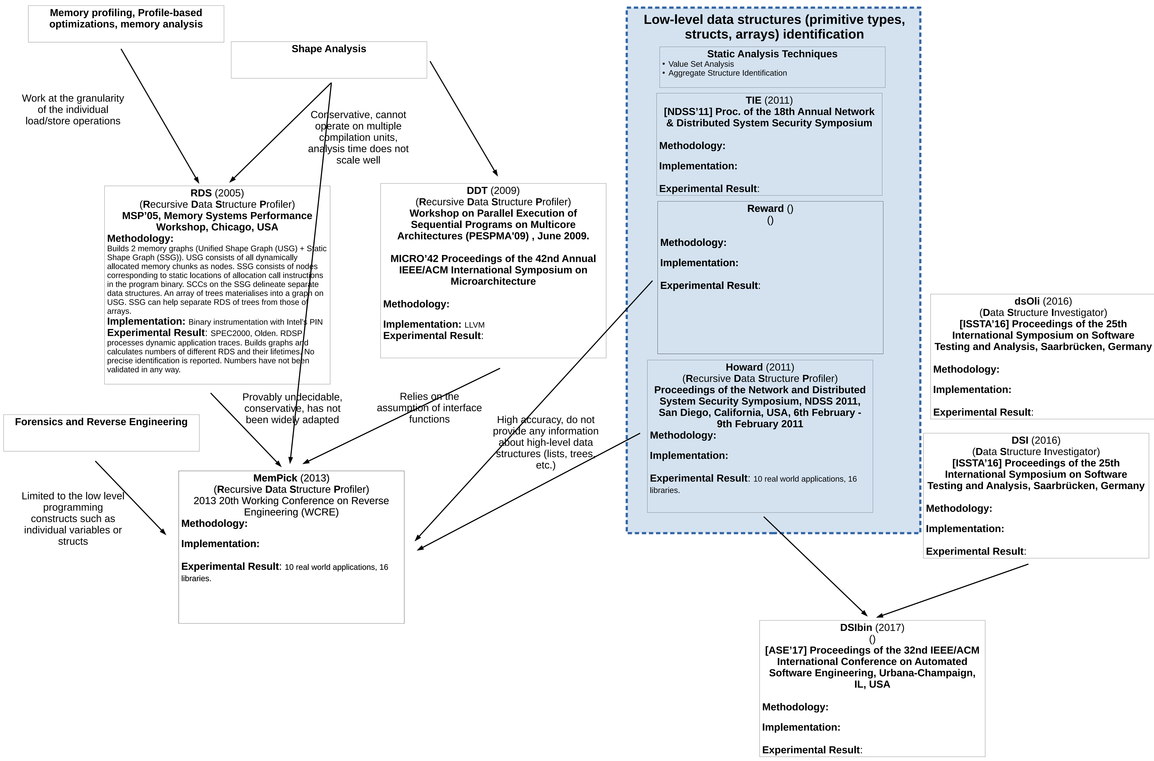
Memory profiling, Profile-based optimizations, memory analysis
Shape Analysis
Work at the granularity of the individual load/store operations
Conservative, cannot operate on multiple compilation units, analysis time does not scale well
RDS (2005)
(Recursive Data Structure Profiler)
MSP'05, Memory Systems Performance Workshop, Chicago, USA
Methodology:
Builds 2 memory graphs (Unified Shape Graph (USG) + Static Shape Graph (SSG)). USG consists of all dynamically allocated memory chunks as nodes. SSG consists of nodes corresponding to static locations of allocation call instructions in the program binary. SCCs on the SSG delineate separate data structures. An array of trees materialises into a graph on USG. SSG can help separate RDS of trees from those of arrays.
Implementation: Binary instrumentation with Intel's PIN
Experimental Result: SPEC2000, Olden. RDSP processes dynamic application traces. Builds graphs and calculates numbers of different RDS and their lifetimes. No precise identification is reported. Numbers have not been validated in any way.
DDT (2009)
(Recursive Data Structure Profiler)
Workshop on Parallel Execution of Sequential Programs on Multicore Architectures (PESPMA'09) , June 2009.
MICRO'42 Proceedings of the 42nd Annual IEEE/ACM International Symposium on Microarchitecture
Methodology:
Implementation: LLVM
Experimental Result:
Low-level data structures (primitive types, structs, arrays) identification
Static Analysis Techniques
Value Set Analysis
Aggregate Structure Identification
TIE (2011)
[NDSS'11] Proc. of the 18th Annual Network & Distributed System Security Symposium
Methodology:
Implementation:
Experimental Result:
Reward ()
()
Methodology:
Implementation:
Experimental Result:
dsOli (2016)
(Data Structure Investigator)
[ISSTA'16] Proceedings of the 25th International Symposium on Software Testing and Analysis, Saarbrücken, Germany
Methodology:
Implementation:
Experimental Result:
Howard (2011)
(Recursive Data Structure Profiler)
Proceedings of the Network and Distributed System Security Symposium, NDSS 2011, San Diego, California, USA, 6th February - 9th February 2011
Methodology:
Implementation:
Experimental Result: 10 real world applications, 16 libraries.
DSI (2016)
(Data Structure Investigator)
[ISSTA'16] Proceedings of the 25th International Symposium on Software Testing and Analysis, Saarbrücken, Germany
Methodology:
Implementation:
Experimental Result:
Provably undecidable, conservative, has not been widely adapted
Relies on the assumption of interface functions
High accuracy, do not provide any information about high-level data structures (lists, trees etc.)
Forensics and Reverse Engineering
Limited to the low level programming constructs such as individual variables or structs
MemPick (2013)
(Recursive Data Structure Profiler)
2013 20th Working Conference on Reverse Engineering (WCRE)
Methodology:
Implementation:
Experimental Result: 10 real world applications, 16 libraries.
DSIbin (2017)
()
[ASE'17] Proceedings of the 32nd IEEE/ACM International Conference on Automated Software Engineering, Urbana-Champaign, IL, USA
Methodology:
Implementation:
Experimental Result:

Abstract Dekker's (1994)
(**D**ata **S**tructure **D**etection **T**ool)
Workshop on Parallel Execution of Sequential Programs on Multicore Architectures (PESPMA'09) , June 2009.

MICRO'42 Proceedings of the 42nd Annual IEEE/ACM International Symposium on Microarchitecture

Input: binary

Methodology:

Implementation: LLVM

Experimental Result: GLib, Apache C++ Standard Library, STLport

Relies on its abilities at the parse tree level, does not require source code access

Forensics & Reverse engineering

Memory profiling, Profile-based optimizations, memory analysis

Shape Analysis

Most well known, but provably undecidable and thus necessarily conservative

Work at the granularity of the individual load/store operations

Conservative, cannot operate on multiple compilation units, analysis time does not scale well

Most well known, but provably undecidable and thus necessarily conservative

RDS (2005)
(**R**ecursive **D**ata **S**tructure **P**rofiler)
MSP'05, Memory Systems Performance Workshop, Chicago, USA

Methodology:
Builds 2 memory graphs (Unified Shape Graph (USG) + Static Shape Graph (SSG)). USG consists of all dynamically allocated memory chunks as nodes. SSG consists of nodes corresponding to static locations of allocation call instructions in the program binary. SCCs on the SSG delineate separate data structures. An array of trees materialises into a graph on USG. SSG can help separate RDS of trees from those of arrays.
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DDT (2009)
(**D**ata **S**tructure **D**etection **T**ool)
Workshop on Parallel Execution of Sequential Programs on Multicore Architectures (PESPMA'09) , June 2009.

MICRO'42 Proceedings of the 42nd Annual IEEE/ACM International Symposium on Microarchitecture

Input: binary

Methodology: builds MG, builds Call Graph, differentiates MG nodes by *allocation-site-based* typing and node merging.

Implementation: LLVM

Weaknesses:
- *allocation-site-based* nodes typing with a assumption of interface functions (not the same API function) might not work in general case
- complementary nodes merging (accessed with high accuracy) do not provide any information about high-level data structures (lists, trees, etc.)

Experimental Result: GLib, Apache C++ Standard Library, STLport

Provably undecidable, conservative, has not been widely adapted

Forensics and Reverse Engineering

Limited to the low level programming constructs such as individual variables or structs

MemPick (2013)
(**R**ecursive **D**ata **S**tructure **P**rofiler)
2013 20th Working Conference on Reverse Engineering (WCRE)

Methodology:

Implementation:

Experimental Result: 10 real world applications, 16 libraries.

Low-level data structures (primitive types, structs, arrays) identification

Static Analysis Techniques
- Value Set Analysis
- Aggregate Structure Identification

TIE (2011)
[NDSS'11] Proc. of the 18th Annual Network & Distributed System Security Symposium

Methodology:

Implementation:

Experimental Result:

Reward ()
()

Methodology:
Methodology:

Implementation:

Experimental Result:

Howard (2011)
(**R**ecursive **D**ata **S**tructure **P**rofiler)
Proceedings of the Network and Distributed System Security Symposium, NDSS 2011, San Diego, California, USA, 6th February - 9th February 2011

Target: stripped binaries

Methodology:

Implementation:

Experimental Result: 10 real world applications, 16 libraries.

DSI (2016)
(**D**ata **S**tructure Investigator)
[ISSTA'16] Proceedings of the 25th International Symposium on Software Testing and Analysis, Saarbrücken, Germany

Methodology:

Implementation:

Experimental Result:

DSIbin (2017)
()
[ASE'17] Proceedings of the 32nd IEEE/ACM International Conference on Automated Software Engineering, Urbana-Champaign, IL, USA

Methodology:

Implementation:

Experimental Result:

Memory Graph (MG)
Nodes: add new nodes to the graph on dynamic memory allocations, remove graph nodes on deallocations
Edges: store instructions (with destination and source operands being addresses pointing to locations withing previously allocated memory nodes/NULL values) create/remove MG edges
Limitations:
- Composite data structures (like array of binary trees) chunk up in the single graph and are hard to distinguish, data structure overlays suffer from the same problem
- Multiple data structure nodes allocated in the same memory chunk are undistinguishable
- Graphs evolve during a program execution, data structure manipulation operations break graph invariant properties

High-level data structures (arrays, linked-lists, trees, hash-tables, graphs, etc.) identification

Dynamic Techniques

RDS (2005)
(**R**ecursive **D**ata **S**tructure **P**rofiler)
MSP'05, Memory Systems Performance Workshop, Chicago, USA

Input: C source code
Problem:
Goal: better understanding of memory access behaviour of programs with RDS (not just individual load/store operations, but a higher-level view)
Contribution: proposal of RDS Profiling technique and a notion of RDS stability

Methodology:
- Builds MG, which is termed as a Unified Shape Graph (USG). USG consists of all dynamically allocated memory chunks as nodes.
- Builds another variant of MG: Static Shape Graph (SSG), which consists of nodes corresponding to static locations of allocation call instructions in the program binary. SCCs on the SSG delineate separate RDS types. An array of trees materialises into a graph on USG. SSG can help separate RDS of trees from those of arrays. To distinguish separate instances of tree RDS type from each other authors notice, that they are always connected by a different RDS type node (array here) existing between them. On-the-fly SCC membership checks are performed.

Implementation: Binary instrumentation with Intel's PIN
Weaknesses:
- Distinguishes different RDS types and instances, but does not analyse the types any further (is it a list, tree, etc.)
- Does not verify its results (counts the number of RDS types, instances, backward/forward edges, RDS lifetimes in the studied benchmarks)

Experimental Result: SPEC2000, Olden. RDSP processes dynamic application traces. Builds graphs and calculates numbers of different RDS and their lifetimes. No precise identification is reported. Numbers have not been validated in any way.

DDT (2009)
(**D**ata **S**tructure **D**etection **T**ool)
Workshop on Parallel Execution of Sequential Programs on Multicore Architectures (PESPMA'09) , June 2009.

MICRO'42 Proceedings of the 42nd Annual IEEE/ACM International Symposium on Microarchitecture

Input: LLVM IR, but can work with binaries
Methodology:
- Builds MG, differentiates MG nodes by *allocation-site-based* typing and node merging, assigns types to MG edges (*child*, *foreign*, *data*)
- Builds Call Graph and identifies data structure iface functions
- Gathers MG and iface function invariants with Daikon tool
- Uses hand-built decision tree to match identified invariants against reference data structure libraries (top-down: MG shape, iface function set and its invariants)

Implementation: LLVM
Weaknesses:
- *allocation-site-based* node typing with a complementary node merging (accessed with the same API function) might not work in general case
- Assumption of iface functions does not hold with optimizations and legacy non-structured code

Experimental Result: Correctly recognised vector, deque, linked-list, set (red-black tree) implementations in several different data structure libraries GLib, Apache C++ Standard Library, STLport

- Elegant, the most powerful to date, accurately detects data structures and the functions that manipulate them
- Limited by iface functions assumption
- Do not address overlapping data structures (like a linked-list, that connects the nodes of a tree)
- Node typing system based on the same memory allocation sites and iface functions

MemPick (2013)
()
2013 20th Working Conference on Reverse Engineering (WCRE)

Input: stripped binaries
Methodology:
- Builds MG
- Tagging MG with type information (*src* and *dst* objects of the same instruction get the same type + uses offsets)
- Split MG based on data structure types
- MG shape analysis: find minimal set of offsets $\{p_1, p_2, p_3, ..., p_n\}$, that still keeps DS connected and separate DS sub-structures
- Classify sub-structures by their shape (#nodes in and out)
- Refinement of special cases (recursive tree height calculation)

Implementation: Intel Pin
Weaknesses:
- Based solely on the shape of data structures not their contents (MemPick cannot tell whether a binary tree is a search tree or not)
- As all dynamic techniques, MemPick is limited by the code coverage of its profiler runs
- MG node tagging might trigger false positives
- Examines MG snapshots at "quiescent" time periods – need to catch them precisely (gaps between DS modifications in the number of instructions)
- MG shape analysis is based on heap buffer offsets – won't handle multiple nodes per heap buffer

Experimental Result:
- 16 popular DS libraries: boost:container, *clibutils*, GLib, GDSL, STL, STLport, etc.
- 10 real-world applications: clang, chromium, Lighttpd, etc. (MemPick finishes withing 1 hour),

- The approach is hampered in the presence of obfuscation, inlining, or optimization, which are often employed in legacy and kernel-space software
- DDT cannot identify lists if these run through differently typed nodes (nested structs, e.g. Linux CDLL)
- DDT does not recognize complex relationships between structures such as arbitrary parent-child nesting

- MemPick does not analyze the relationships between DSs, such as parent-child nesting, and thus cannot identify more complex DSs such as the Linux kernel CDLL that involves nodes of different types.

- The state-of-the-art in heuristic guided identification of data structures
- Limited by iface functions assumption
- Do not address overlapping data structures (like a linked-list, that connects the nodes of a tree)
- Node typing system based on the same memory allocation sites and face functions

University of Bamberg
Software Technologies Research Group
"Identifying and Analyzing Pointer-based Dynamic Data Structures" project
https://www.swt-bamberg.de/research/data-structure-investigator.html

2014, 2017 Grants
"Learning Data Structure Behaviour from Executions of Pointer Programs"

dsOli (2014)
(data structure **O**peration **I**ocation and identification)
[ICPC'14] Proceedings of the 22nd International Conference on Program Comprehension, Hyderabad, India

Input: C source code
Problem:

Goal: C source code comprehension by automatically discovering and identifying pointer-based dynamic data structure operations

Methodology:
- Collect sequences of program events (pointer writes, memory (de)allocations) $[E_1, E_2, \ldots, E_n]$, corresponding points-to graphs $[G_1, G_2, \ldots, G_n]$ and feature vectors $[F_1, F_2, \ldots, F_n]$
- Use Minimum Description Length (MDL) technique to identify repeating patterns of feature vectors
- Use points-to graph pairs (G_i, G_j) on the boundaries of identified sequence patterns to match against the library of XML templates (DS operation graph transformation)
- GUI visualisations of the results

Implementation: instrumentation (CIL),

Experimental Result: textbook and literature examples, ***Boa web server parts***, ***Linux kernel LL***,

DSI (2016)
(**D**ata **S**tructure **I**nvestigator)
[ISSTA'16] Proceedings of the 25th International Symposium on Software Testing and Analysis, Saarbrücken, Germany

Input: C source code
Problem:
- Varieties and complexities of DS implementations in C language enabled by the freedom offered by C and demanded by low-level software (OS, device drivers) efficiency concerns
- Degenerate shapes (DSI uses a different to MemPick (quiescent periods) and DDT (operation boundaries) approach): reinforcing evidence for DS observations through structural and temporal repetion

Goal: C source code comprehension, legacy software maintenance tool, memory visualization
Methodology:

Weaknesses:
- The core DSI algorithm requires type information on cells from source code

Implementation:

Experimental Result: textbook and literature examples, ***bash***, ***lzousb***

- Do not give any details on a high level usages of discovered structs

DSIbin (2017)
(**D**ata **S**tructure **I**nvestigator binary)
ASE 2017 Proceedings of the 32nd IEEE/ACM International Conference on Automated Software Engineering

Input: binary

Problem:

Goal:
Methodology:
Implementation: Intel's PIN + **Howard** (to excavate type information from binaries)
Experimental Result: textbook and literature examples, **Olden**, **theZoo aka Malware DB**: 10 out of 30 correctly identified and 26/30 with DSIref (Howard's imprecision refinement technique)

- Works with a source code and uses type information (struct sizes and layouts, etc.) available there
- The core DSI algorithm requires type information on cells
- does not make strong assumptions on the graph kind under analysis
- is supported by temporarily regenerate shapes
- is more general as it supports the identification of lists through different node types and arbitrarily nested combinations of lists

- Howard's inferred types are often too coarse
- Can deal with nested structs
- Deals with C/C++ binaries that include memcpy-like functions

Static Techniques

Shape Analysis
()

Input:
Methodology:
Implementation:
Weaknesses:
Experimental Result:

Parametric Shape Analysis Framework
via 3-Valued Logic

Input:
Methodology:
Implementation:
Weaknesses:
Experimental Result:

Traditional Compiler Analyses
()
may-alias-analysis

Input:
Methodology:
Implementation:
Weaknesses:
Experimental Result:

Low-level data structures (primitive types, structs, arrays) identification

Static Analysis Techniques
Value Set Analysis,
Aggregate Structure Identification

Disassemblers and decompilers
IDA Pro (2005), OllyDbg ()

Target: stripped binaries
Methodology: known library function calls to symbolic names

- Typically weak in reverse engineering of data structures. The research community has been aware of the importance of data structures in reverse engineering for several years now (2011), but even so, no adequate solution emerged.

Dynamic Analysis Techniques

Reward (2010)
()
Proceedings of the Network and Distributed System Security Symposium, NDSS 2010, San Diego, California, USA, 6th February - 9th February 2011

Target: stripped binaries
Methodology: Dynamic analysis. Whenever a program makes a call to a well-known function (like a system call), we know the types of all the arguments – so we label these memory locations accordingly. Next, we propagate this type information backwards and forwards.

- Unfortunately, the power of static analysis is quite limited and none of the techniques mentioned above can adequately handle even some of the most common data structures – like arrays.
- If a C struct is copied using a function like memcpy, VSAAS will misclassify it as having many fields of 4 bytes, simply because memcpy accesses the memory with a stride of 4 (on a 32 bit machine).
- Also, they cannot deal with functions like 'alloca'.

- The technique only recovers those data structures that appear, directly or indirectly, in the arguments of system calls. This is a very small portion of all data structures in a program. All internal variables and data structures in the program remain invisible.

Howard (2011)
(Data structure revovery from binaries)
Proceedings of the Network and Distributed System Security Symposium, NDSS 2011, San Diego, California, USA, 6th February - 9th February 2011

Target: stripped binaries
Goal: furnish existing disassemblers and debuggers with information about data structures and types to ease reverse engineering (Howard automatically generates debug symbols that can be used by all common tools)
Problem: compiler translates all explicitly structured data in the source to chunks of anonymous bytes in the binary

Methodology: recovery by access patterns
- Model of a function call stack (Hstack) tracks call and ret instructions
- Memory and register tags. Each memory location is tagged with its root address MBase(addr) it had been derived from as well as each register has been tagged with Rbase(addr)
- Tag propagation with pointer arithmetic
-

Implementation: Dynamic analysis on QEMU-based processor emulator

Experimental Result: Howard is able to recover most data structures in arbitrary (gcc-generated) binaries with a high degree of precision.

Memory Graph (MG)
Nodes: add new nodes to the graph on dynamic memory allocations, remove graph nodes on deallocations
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Limitations:
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