

Metric-based Software Parallelisability Analyzer

Aleksandr Maramzin



Master of Science by Research
Institute of Computing Systems Architecture
School of Informatics
University of Edinburgh
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Abstract

Parallelism pervades the modern computing world. Almost all modern computing systems provide parallel computing resources to some degree or another. The major problem in the field is that these available resources are not always efficiently utilized. To take the most out of these parallel resources, applications running on them must be parallel as well.

Despite progress in parallel programming language design and increased availability of parallel programming frameworks, writing efficient parallel software from scratch is still a challenging task mastered by only a few expert programmers. While these experts combine domain knowledge, algorithmic insight and parallel programming skills, most average programmers are often lacking skills in at least one of these areas. In this project we investigate methods for providing programmers with real-time feedback on the quality of their code with respect to parallelisation opportunities and scalability to address short-comings before they manifest as bad and hard-to-parallelise code.

We draw on the experience of the software engineering community and software metrics originally developed to identify bad sequential code, typically prone to errors and hard to maintain. The ambition of this project is to develop novel software parallelisability metrics, which can be used as quality indicators for parallel code and guide the software development process towards better parallel code.

Acknowledgements

Declaration

I declare that this thesis was composed by myself, that the work contained herein is my own except where explicitly stated otherwise in the text, and that this work has not been submitted for any other degree or professional qualification except as specified.

(Aleksandr Maramzin)

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Chapter 1

Introduction

Parallelism pervades the modern computing world. In the past parallel computations used to be employed only in high performance scientific systems, but now the situation has changed. Parallel elements present in the design of almost all modern computers from small embedded processors to large-scale supercomputers and computing networks. Unfortunately, these immense parallel computing resources are not always fully utilized during computations due to several problems in the field:

1. Abundance of legacy applications from previous sequential computing era. That abundance is one source of problems. Legacy applications are not designed to run on parallel machines and, by default, do not take advantage of all underlying resources. Automatic parallelisation techniques have been developed to transform these sequential applications into parallel ones. However, these techniques cannot efficiently deal with some codes in the spectrum of existent applications. Pointer-based applications with irregular data structures, applications with loop carried dependencies and entangled control flow have proven to be challenging to automatic parallelisation. Very often such programs hide significant amounts of parallelism behind suboptimal implementation constructs and represent meaningful potential for further improvements.

2. Difficulty of manual parallel programming. Hidden potential can be realised by writing parallel programs (applications designed to run on parallel systems) manually. However, the task of manual parallel programming is rather challenging by itself. To create efficient and well-designed parallel software programmer must be aware of application's domain field, must have good algorithmic background as well as solid general programming skills and working knowledge of exact parallel programming framework they are using. Most average programmers lack some of the necessary skills out of that set, which hinders the potential of manual parallelisation. Sometimes

sloppy program parallelisation can even slow sequential programs down due to parallel synchronisation/communication overhead incurred. In our project we propose to research the question of software parallelisability metrics. This research idea draws on the existent work in the area of software quality, where numerous software metrics have been proposed. Section 3 of this proposal gives a brief overview of the major software metrics to date. In many cases they can be used to supplement software engineering expertise and common sound judgement when it comes to engineering and managerial decisions during software development. These metrics are designed to address the issues of source code complexity, testability, maintainability, etc. and usually show a good correlation between these properties of software and their values. Despite possible correlations between some of these metrics and application performance, these metrics are not designed for that task. Performance of many compute-intensive applications on modern computers is directly proportional to their parallelisability. To our knowledge, there are no software metrics, which can be used for judging about source code parallelisability and that research area seems to be unexplored. Integration of such parallelisability metrics into major Interactive Development Environments (IDEs) could alleviate parallel programming task by providing programmers with real-time feedback about their code. Moreover, new software parallelisability metrics have a potential of paving the way into the new areas of parallel programming research.

Chapter 2

Background

This section of the thesis introduces a reader into the context of the work. First, it describes

2.1 Software Quality Metrics

2.1.1 McCabe's Cyclomatic Complexity

[1]

2.2 Metrics in the area of Parallel Computing

2.3 Dependence Theory

[2]

2.4 Information from the graph theory

2.5 Program Dependence Graph

[3]

2.6 Loop Decoupling

[9]

Chapter 3

Software Parallelisability Metrics

This chapter defines proposed software source code parallelisability metrics and gives the basic intuition behind them. Proposed metrics inherited dependence-based nature from the work [2]. This book is built on and describes the results gathered through countless years of research and tremendous amount of work done in the field of optimizing compilers and high-performance computer architectures.

The chapter is structured in the following way. Section 3.1 puts the metrics work into the context and gives the general perspective from which one has to look at parallelisability metrics. Section 3.2 introduces the actual metrics, along with the basic motivation for them. Metrics are introduced as a set of conceptual groups. Each group has roughly the same intuition and motivation for all its metrics.

3.1 General foundation and perspective of the work

3.1.1 Diversity in modern computer languages

3.1.2 The modern role of compilers

3.1.3 The famous 80/20 rule

3.1.4 Dependence-based approach to metrics computation

Program parallelisation of program statements is basically hindered by the execution-order constraints imposed on those statements, which, in turn, are defined by different sorts of program dependencies, which were described in the section 2.3 of the thesis.

3.2 Metric Groups

The whole set of proposed metrics is divided into several conceptual groups.

3.2.1 Loop Proportion Metrics

3.2.1.1 Loop Absolute Size

3.2.1.2 Loop Absolute Size

3.2.1.3 Loop Absolute Size

3.2.2 Loop Dependence Metrics

3.2.3 Loop Cohesion Metrics

The main motivation behind the metrics out of this group is the tighter the parts of a loop are coupled together (in terms of dependencies), the harder it is going to be to split and parallelize the loop.

Chapter 4

Tool Implementation

The tool developed for source code parallelisability metrics research is completely based on the LLVM library of modular and reusable compiler technologies [4] [5] and implemented as a set of LLVM passes (see LLVM online documentation for further technical details [6]). The tool can be found at [7] and is implemented as a set of LLVM passes. All parts of the tool rely heavily on the standard C++ template mechanism and C++ Standard Template Library (STL).

The tool operates on the level of LLVM intermediate representation [8] (LLVM IR) and is completely decoupled from input languages as well as from target machine instruction set. Theoretically the tool can be used for source code parallelisability analysis of any arbitrary programming languages as it does not depend on any exact programming language concepts, data structures and constructs (such as conditional loops, for loops, range-for loops, goto statements, lists, maps, etc). The tool operates on the level of different program dependencies (data, control, etc), which are abstracted away from programming languages domain into a separate dependence analysis theory. In order to use a tool, one must provide a way of compiling input language into LLVM intermediate representation.

Conceptually the tool does the following. It accepts C/C++ programs as an input.

In this project all proposed concepts are being examined with the use of Clang/Clang++ as a front end to transform input C/C++ source code into LLVM instruction set.

4.1 LLVM analyses used in the tool

There are several LLVM provided analyses being used by the tool.

4.1.1 LoopInfo analysis

This analysis function pass identifies all natural loops within the given function and assigns a loop depth to every function's basic block. This analysis calculates the nesting structure of loops in the function. For each natural loop identified, this analysis identifies natural loops, contained entirely within the loop and basic blocks that make up the loop.

4.1.2 DependenceAnalysis analysis

4.2 Dependence Graphs

Since LLVM, as of version 6.0, does not currently provide a standard Dependence Graph (DG) implementation, custom graph building facilities were implemented as a **Graph<NODE,EDGE>** C++ template. Template expects two parameters, which must be pointers to the NODE and EDGE classes. These classes represent information associated with every graph node and edge correspondingly.

4.2.1 Data Dependence Graph (DDG) pass

4.2.2 Memory Dependence Graph (MDG) pass

4.2.3 Control Dependence Graph (CDG) pass

4.2.4 Program Dependence Graph (PDG) pass

4.3 Loop Decoupling Pass

Before metrics can be computed by the tool, the program must be disassembled into finer pieces of granularity. The tool uses LLVM pass framework to run its passes on functions and builds PDGs for all functions of the given program. Then the tool uses `llvm::LoopInfo` analysis to get information about all loops inside functions and build separate PDGs for them. Once that is done, the tool finds Strongly Connected Components (SCCs) of the graphs and uses approach described in the Loop Iterator Recognition work [9] to further decouple identified loops into iterator and payload parts.

4.4 DOT graph printing facilities

4.5 Metric Groups

Chapter 5

Benchmarks

Tool developed as a set of LLVM passes

5.1 GraphPass

In the graph pass

5.1.1 Graph

5.1.1.0.1 Program Dependence Graph

Chapter 6

Analysis

Chapter 7

Results

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