

[10/13] store i32 %rem55, i32* %arrayidx59, align 4, !dbg !1434

[0/9] store i32 %inc61, i32* %j50, align 4, !dbg !1436

[14/21] %30 = load i32, i32* %i46, align 4, !dbg !1432

[22/23] %29 = load i32**, i32*** %b, align 8, !dbg !1431