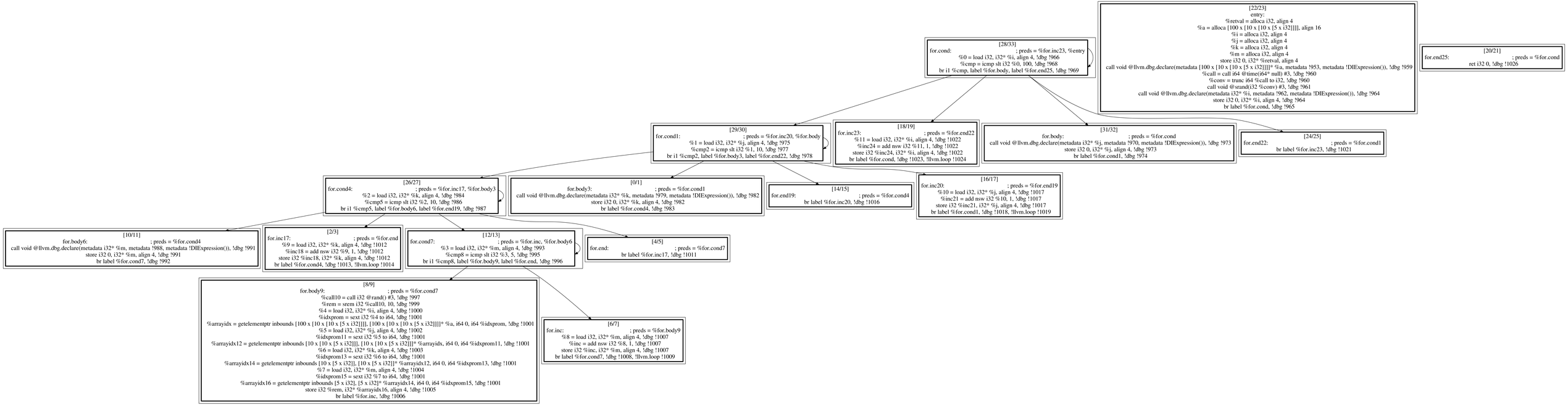
[22/23]
entry:
%retval = alloca i32, align 4
%a = alloca [100 x [10 x [10 x [5 x i32]]]], align 16
%i = alloca i32, align 4
%j = alloca i32, align 4
%k = alloca i32, align 4
%m = alloca i32, align 4
store i32 0, i32* %retval, align 4
call void @llvm.dbg.declare(metadata [100 x [10 x [10 x [5 x i32]]]]* %a, metadata !953, metadata !DIExpression()), !dbg !959
%call = call i64 @time(i64* null) #3, !dbg !960
%conv = trunc i64 %call to i32, !dbg !960
call void @srand(i32 %conv) #3, !dbg !961
call void @llvm.dbg.declare(metadata i32* %i, metadata !962, metadata !DIExpression()), !dbg !964
store i32 0, i32* %i, align 4, !dbg !964
br label %for.cond, !dbg !965
[20/21]
for.end25: ; preds = %for.cond
ret i32 0, !dbg !1026
[28/33]
for.cond: ; preds = %for.inc23, %entry
%0 = load i32, i32* %i, align 4, !dbg !966
%cmp = icmp slt i32 %0, 100, !dbg !968
br i1 %cmp, label %for.body, label %for.end25, !dbg !969
[29/30]
for.cond1: ; preds = %for.inc20, %for.body
%1 = load i32, i32* %j, align 4, !dbg !975
%cmp2 = icmp slt i32 %1, 10, !dbg !977
br i1 %cmp2, label %for.body3, label %for.end22, !dbg !978
[18/19]
for.inc23: ; preds = %for.end22
%11 = load i32, i32* %i, align 4, !dbg !1022
%inc24 = add nsw i32 %11, 1, !dbg !1022
store i32 %inc24, i32* %i, align 4, !dbg !1022
br label %for.cond, !dbg !1023, !llvm.loop !1024
[31/32]
for.body: ; preds = %for.cond
call void @llvm.dbg.declare(metadata i32* %j, metadata !970, metadata !DIExpression()), !dbg !973
store i32 0, i32* %j, align 4, !dbg !973
br label %for.cond1, !dbg !974
[24/25]
for.end22: ; preds = %for.cond1
br label %for.inc23, !dbg !1021
[26/27]
for.cond4: ; preds = %for.inc17, %for.body3
%2 = load i32, i32* %k, align 4, !dbg !984
%cmp5 = icmp slt i32 %2, 10, !dbg !986
br i1 %cmp5, label %for.body6, label %for.end19, !dbg !987
[0/1]
for.body3: ; preds = %for.cond1
call void @llvm.dbg.declare(metadata i32* %k, metadata !979, metadata !DIExpression()), !dbg !982
store i32 0, i32* %k, align 4, !dbg !982
br label %for.cond4, !dbg !983
[14/15]
for.end19: ; preds = %for.cond4
br label %for.inc20, !dbg !1016
[16/17]
for.inc20: ; preds = %for.end19
%10 = load i32, i32* %j, align 4, !dbg !1017
%inc21 = add nsw i32 %10, 1, !dbg !1017
store i32 %inc21, i32* %j, align 4, !dbg !1017
br label %for.cond1, !dbg !1018, !llvm.loop !1019
[10/11]
for.body6: ; preds = %for.cond4
call void @llvm.dbg.declare(metadata i32* %m, metadata !988, metadata !DIExpression()), !dbg !991
store i32 0, i32* %m, align 4, !dbg !991
br label %for.cond7, !dbg !992
[2/3]
for.inc17: ; preds = %for.end
%9 = load i32, i32* %k, align 4, !dbg !1012
%inc18 = add nsw i32 %9, 1, !dbg !1012
store i32 %inc18, i32* %k, align 4, !dbg !1012
br label %for.cond4, !dbg !1013, !llvm.loop !1014
[12/13]
for.cond7: ; preds = %for.inc, %for.body6
%3 = load i32, i32* %m, align 4, !dbg !993
%cmp8 = icmp slt i32 %3, 5, !dbg !995
br i1 %cmp8, label %for.body9, label %for.end, !dbg !996
[4/5]
for.end: ; preds = %for.cond7
br label %for.inc17, !dbg !1011
[8/9]
for.body9: ; preds = %for.cond7
%call10 = call i32 @rand() #3, !dbg !997
%rem = srem i32 %call10, 10, !dbg !999
%4 = load i32, i32* %i, align 4, !dbg !1000
%idxprom = sext i32 %4 to i64, !dbg !1001
%arrayidx = getelementptr inbounds [100 x [10 x [10 x [5 x i32]]]], [100 x [10 x [10 x [5 x i32]]]]* %a, i64 0, i64 %idxprom, !dbg !1001
%5 = load i32, i32* %j, align 4, !dbg !1002
%idxprom11 = sext i32 %5 to i64, !dbg !1001
%arrayidx12 = getelementptr inbounds [10 x [10 x [5 x i32]]], [10 x [10 x [5 x i32]]]* %arrayidx, i64 0, i64 %idxprom11, !dbg !1001
%6 = load i32, i32* %k, align 4, !dbg !1003
%idxprom13 = sext i32 %6 to i64, !dbg !1001
%arrayidx14 = getelementptr inbounds [10 x [5 x i32]], [10 x [5 x i32]]* %arrayidx12, i64 0, i64 %idxprom13, !dbg !1001
%7 = load i32, i32* %m, align 4, !dbg !1004
%idxprom15 = sext i32 %7 to i64, !dbg !1001
%arrayidx16 = getelementptr inbounds [5 x i32], [5 x i32]* %arrayidx14, i64 0, i64 %idxprom15, !dbg !1001
store i32 %rem, i32* %arrayidx16, align 4, !dbg !1005
br label %for.inc, !dbg !1006
[6/7]
for.inc: ; preds = %for.body9
%8 = load i32, i32* %m, align 4, !dbg !1007
%inc = add nsw i32 %8, 1, !dbg !1007
store i32 %inc, i32* %m, align 4, !dbg !1007
br label %for.cond7, !dbg !1008, !llvm.loop !1009