```
[0/1]
entry:
%a.addr = alloca i32*, align 8
%b.addr = alloca i32*, align 8
%tmp = alloca i32, align 4
store i32* %a, i32** %a.addr, align 8
call void @llvm.dbg.declare(metadata i32** %a.addr, metadata !1294, metadata !DIExpression()), !dbg !1295
store i32* %b, i32** %b.addr, align 8
call void @llvm.dbg.declare(metadata i32** %b.addr, metadata !1296, metadata !DIExpression()), !dbg !1297
call void @llvm.dbg.declare(metadata i32* %tmp, metadata !1298, metadata !DIExpression()), !dbg !1299
%0 = load i32*, i32** %a.addr, align 8, !dbg !1300
%1 = load i32, i32* %0, align 4, !dbg !1300
store i32 %1, i32* %tmp, align 4, !dbg !1301
%2 = load i32*, i32** %b.addr, align 8, !dbg !1302
%3 = load i32, i32* %2, align 4, !dbg !1302
%4 = load i32*, i32** %a.addr, align 8, !dbg !1303
store i32 %3, i32* %4, align 4, !dbg !1304
%5 = load i32, i32* %tmp, align 4, !dbg !1305
%6 = load i32*, i32** %b.addr, align 8, !dbg !1306
store i32 %5, i32* %6, align 4, !dbg !1307
ret void, !dbg !1308
```