

[30/31] %20 = load i32, i32* %hsize.addr, align 4, !dbg !1345

[34/35] %26 = load i32**, i32*** %b, align 8, !dbg !1352

[12/19] store i32 %25, i32* %arrayidx16, align 4, !dbg !1355

[36/37] %21 = load i32**, i32*** %a.addr, align 8, !dbg !1348

[20/29] %31 = load i32, i32* %i, align 4, !dbg !1362

[32/33] %18 = load i32, i32* %vsize.addr, align 4, !dbg !1336

[0/11] store i32 %inc18, i32* %j, align 4, !dbg !1357