

[8/9] %8 = load i32, i32\* %hsize.addr, align 4, !dbg !1320

[0/7] store i32 %inc, i32\* %i, align 4, !dbg !1327

[10/11] %14 = load i32\*\*, i32\*\*\* %b, align 8, !dbg !1323

[12/13] %7 = load i32, i32\* %vsize.addr, align 4, !dbg !1317

[14/15] store i32\* %13, i32\*\* %arrayidx, align 8, !dbg !1325