

[10/11] %3 = load i32\*\*, i32\*\*\* %a, align 8, !dbg !1316

[8/9] store i32\* %2, i32\*\* %arrayidx, align 8, !dbg !1318

[0/7] store i32 %inc, i32\* %i, align 4, !dbg !1320