

[0/5] store i32 %inc, i32* %i, align 4, !dbg !1307

[10/13] store i64 %5, i64* %num_pp, align 8, !dbg !1303

[14/15] %2 = load i32, i32* %num.addr, align 4, !dbg !1294

[6/9] %6 = load i64, i64* %fibb_num, align 8, !dbg !1304

[16/21] %5 = load i64, i64* %num_p, align 8, !dbg !1302