

[0/7] store i32 %inc55, i32\* %i, align 4, !dbg !1420

[1/2] %54 = load i32, i32\* %i, align 4, !dbg !1410

[5/6] %57 = load i32, i32\* %i, align 4, !dbg !1417

[3/4] %60 = load i32, i32\* %i, align 4, !dbg !1420

[10/11] %56 = load i32\*\*, i32\*\*\* %b, align 8, !dbg !1415

[8/9] %55 = load i32, i32\* %vsize.addr, align 4, !dbg !1412

[12/13] %58 = load i32\*, i32\*\* %arrayidx53, align 8, !dbg !1415

