

[44/55] %49 = load i32, i32\* %i66, align 4, !dbg !1480

[56/57] %53 = load i32\*\*, i32\*\*\* %c, align 8, !dbg !1488

[62/63] %43 = load i32\*\*, i32\*\*\* %b, align 8, !dbg !1475

[30/43] %51 = load i32, i32\* %j70, align 4, !dbg !1481

[0/11] %52 = load i32, i32\* %k, align 4, !dbg !1484

[12/29] store i32 %mul, i32\* %arrayidx88, align 4, !dbg !1482

[60/61] %38 = load i32\*\*, i32\*\*\* %a, align 8, !dbg !1471

[58/59] %48 = load i32\*\*, i32\*\*\* %c, align 8, !dbg !1479