

[8/13] %6 = load i32*, i32** %b.addr, align 8, !dbg !1306

[0/7] store i32 %5, i32* %6, align 4, !dbg !1307

[14/17] %5 = load i32, i32* %tmp, align 4, !dbg !1305

[18/23] %4 = load i32*, i32** %a.addr, align 8, !dbg !1303