

[10/13] store i32 %rem, i32\* %arrayidx39, align 4, !dbg !1400

[0/9] %25 = load i32, i32\* %j, align 4, !dbg !1402

[14/21] %22 = load i32, i32\* %i28, align 4, !dbg !1398

[22/23] %21 = load i32\*\*, i32\*\*\* %a, align 8, !dbg !1397