

[50/51] %41 = load i32**, i32*** %b, align 8, !dbg !1388

[44/47] %52 = load i32, i32* %total, align 4, !dbg !1401

[0/13] store i32 %inc38, i32* %j, align 4, !dbg !1393

[40/41] %35 = load i32, i32* %hsize.addr, align 4, !dbg !1380

[52/53] %36 = load i32**, i32*** %b, align 8, !dbg !1383

[48/49] %33 = load i32, i32* %vsize.addr, align 4, !dbg !1371

[28/39] %42 = load i32, i32* %i, align 4, !dbg !1389

[42/43] %47 = load i32**, i32*** %b, align 8, !dbg !1397

[14/27] %45 = load i32, i32* %arrayidx36, align 4, !dbg !1391