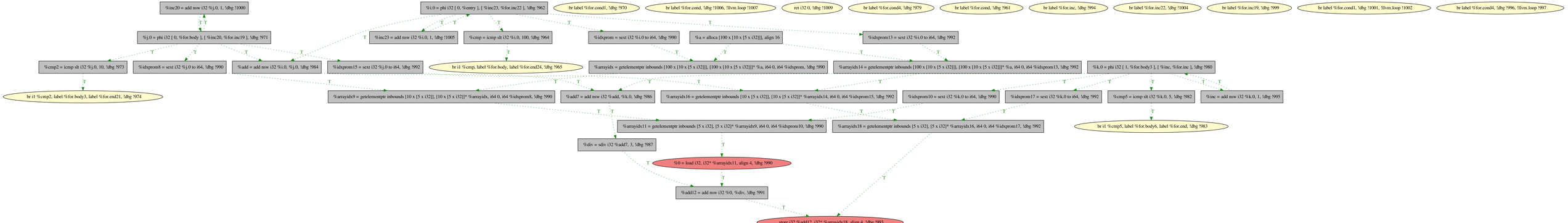
%inc20 = add nsw i32 %j.0, 1, !dbg !1000
%i.0 = phi i32 [0, %entry], [%inc23, %for.inc22], !dbg !962
br label %for.cond1, !dbg !970
br label %for.cond, !dbg !1006, !llvm.loop !1007
ret i32 0, !dbg !1009
br label %for.cond4, !dbg !979
br label %for.cond, !dbg !961
br label %for.inc, !dbg !994
br label %for.inc22, !dbg !1004
br label %for.inc19, !dbg !999
br label %for.cond1, !dbg !1001, !llvm.loop !1002
br label %for.cond4, !dbg !996, !llvm.loop !997
%j.0 = phi i32 [0, %for.body], [%inc20, %for.inc19], !dbg !971
%inc23 = add nsw i32 %i.0, 1, !dbg !1005
%cmp = icmp slt i32 %i.0, 100, !dbg !964
%idxprom = sext i32 %i.0 to i64, !dbg !990
%a = alloca [100 x [10 x [5 x i32]]], align 16
%idxprom13 = sext i32 %i.0 to i64, !dbg !992
%cmp2 = icmp slt i32 %j.0, 10, !dbg !973
%idxprom8 = sext i32 %j.0 to i64, !dbg !990
%add = add nsw i32 %i.0, %j.0, !dbg !984
%idxprom15 = sext i32 %j.0 to i64, !dbg !992
br i1 %cmp, label %for.body, label %for.end24, !dbg !965
%arrayidx = getelementptr inbounds [100 x [10 x [5 x i32]]], [100 x [10 x [5 x i32]]]* %a, i64 0, i64 %idxprom, !dbg !990
%arrayidx14 = getelementptr inbounds [100 x [10 x [5 x i32]]], [100 x [10 x [5 x i32]]]* %a, i64 0, i64 %idxprom13, !dbg !992
%k.0 = phi i32 [1, %for.body3], [%inc, %for.inc], !dbg !980
br i1 %cmp2, label %for.body3, label %for.end21, !dbg !974
%arrayidx9 = getelementptr inbounds [10 x [5 x i32]], [10 x [5 x i32]]* %arrayidx, i64 0, i64 %idxprom8, !dbg !990
%add7 = add nsw i32 %add, %k.0, !dbg !986
%arrayidx16 = getelementptr inbounds [10 x [5 x i32]], [10 x [5 x i32]]* %arrayidx14, i64 0, i64 %idxprom15, !dbg !992
%idxprom10 = sext i32 %k.0 to i64, !dbg !990
%idxprom17 = sext i32 %k.0 to i64, !dbg !992
%cmp5 = icmp slt i32 %k.0, 5, !dbg !982
%inc = add nsw i32 %k.0, 1, !dbg !995
%arrayidx11 = getelementptr inbounds [5 x i32], [5 x i32]* %arrayidx9, i64 0, i64 %idxprom10, !dbg !990
%arrayidx18 = getelementptr inbounds [5 x i32], [5 x i32]* %arrayidx16, i64 0, i64 %idxprom17, !dbg !992
br i1 %cmp5, label %for.body6, label %for.end, !dbg !983
%div = sdiv i32 %add7, 3, !dbg !987
%0 = load i32, i32* %arrayidx11, align 4, !dbg !990
%add12 = add nsw i32 %0, %div, !dbg !991
store i32 %add12, i32* %arrayidx18, align 4, !dbg !993