

[16/21] %4 = load i64, i64* %num_pp, align 8, !dbg !1299

[0/7] %8 = load i64, i64* %fibb_num, align 8, !dbg !1311

[22/25] store i32 %num, i32* %num.addr, align 4

[26/35] %5 = load i64, i64* %num_p, align 8, !dbg !1302

[8/15] store i32 %inc, i32* %i, align 4, !dbg !1307