

[122/129] %8 = load i32, i32* %hsize.addr, align 4, !dbg !1320

[142/145] store i32** %a, i32*** %a.addr, align 8

[80/105] %51 = load i32, i32* %arrayidx44, align 4, !dbg !1397

[8/53] store i32 %inc55, i32* %i, align 4, !dbg !1420

[130/141] store i32 %vsize, i32* %vsize.addr, align 4

[54/79] %50 = load i32, i32* %j, align 4, !dbg !1399

[0/7] %63 = load i32, i32* %total, align 4, !dbg !1426

[106/121] %56 = load i32**, i32*** %b, align 8, !dbg !1415