

8

7

6

5

4

3

BRCM P/N

822-128343-0000

SH

1

REV

01

1

NOTES: UNLESS OTHERWISE SPECIFIED

1. SPECIFICATIONS/TOLERANCES:

A. FABRICATE PER IPC-6012, CLASS 2, USING PROVIDED DATA FILES 200-128343-0000_01.TGZ OR 823-128343-0000_01.ZIP.

B. ALL SPECIFICATIONS USED SHALL BE PER THEIR LATEST REVISIONS.

C. THE DIMENSIONS OF CIRCUIT FEATURES IN THE PROVIDED DATA MAY BE ADJUSTED ONLY TO COMPENSATE FOR PROCESS TOLERANCES; ADDING, REMOVING OR RELOCATING CIRCUIT FEATURES, INCLUDING NON-FUNCTIONAL PADS, IS NOT ALLOWED, AND THE DESIGN OF ALL PLANE STRUCTURES MUST BE MAINTAINED TO ENSURE PROPER ELECTRICAL PERFORMANCE.

D. REMOVE ALL BURRS AND BREAK SHARP EDGES, .381 [.015] MAX RADIUS.

E. PARENTHETICAL INFORMATION IS FOR REFERENCE ONLY.

F. REPAIR OF PCB DEFECTS IS NOT PERMITTED.

2. DIELECTRIC MATERIAL:

A. DIELECTRIC MATERIAL SHALL BE PER IPC-4101/99, /124, /126 OR /129 (RoHS COMPLIANT EPOXY-GLASS).

B. MINIMUM DIELECTRIC THICKNESS SHALL BE .051 [.002] FOR REFERENCED STACK-UP DIMENSIONS OF.076 [.003] OR GREATER; IPC-6012 REQUIREMENTS SHALL OTHERWISE APPLY. SINGLE-PLY CONSTRUCTION IS ALLOWED.

C. SEE LAYER STACK-UP FOR REQUIRED COPPER WEIGHTS AND THE FINISHED PCB THICKNESS. IF SPECIFIED, 1/3 OZ. STARTING FOIL MAY BE ACHIEVED BY 1/2 OZ. FOIL REDUCTION.

D. FINISHED PCB THICKNESS SHALL BE MEASURED OVER LANDS AND/OR CONDUCTORS NOT COVERED BY SOLDER MASK.

3. DRILLING:

A. VIA DIAMETERS (TOL. = +.051/- DRILL DIAMETER [+0.020/- DRILL DIAMETER]) SHALL BE VERIFIED BEFORE PLATING; ALL OTHER HOLE DIAMETERS SHALL BE VERIFIED AT FINAL INSPECTION.

B. LAYER-TO-LAYER MISREGISTRATION SHALL BE .127 [.005] MAXIMUM.

4. SOLDER MASK:

A. APPLY LPI SOLDER MASK USING PROVIDED DATA.

B. SOLDER MASK SHALL BE PER IPC-SM-840, CLASS T, COLOR GREEN.

C. THE DIMENSIONS OF SOLDER MASK-DEFINED PADS ON PLANES AND/OR WIDE CONDUCTORS SHALL NOT BE MODIFIED.

5. MARKING:

A. MARK PCB PER PROVIDED DATA USING SILKSCREEN OR AUTOMATED INJET PROCESSING WITH PERMANENT, NON-CONDUCTIVE INK, COLOR WHITE.

B. SUPPLIER ID AND TRACEABILITY INFORMATION SHALL BE APPLIED USING PERMANENT, NON-CONDUCTIVE INK, COLOR WHITE.

C. INK SHALL NOT BE APPLIED TO ANY SOLDERABLE SURFACE.

6. ELECTRICAL TEST:

A. DESIGN VERIFICATION SHALL BE DONE PRIOR TO PCB FABRICATION USING SUPPLIED VALOR ODB++ DATABASE, OR GERBER DATA AND AN IPC-D-356 NETLIST.

B. ALL PCBs SHALL BE 100% ELECTRICALLY TESTED FOR OPENS AND SHORTS USING PROVIDED DATA.

C. APPLY TEST STAMP IN NON-LEGEND AREA ON REAR SIDE OF PCB; OK TO APPLY TO PANEL RAILS IF SPACE DOES NOT PERMIT.

7. FINAL FINISH:

A. FINAL FINISH ON ALL EXPOSED CONDUCTORS SHALL BE IMMERSION SILVER PER IPC-4553, .15 - .38 MICROMETERS [6 - 15 MICROINCHES] THICK.

8. IMPEDANCE:

A. IMPEDANCE TOLERANCE SHALL BE +/- 10%.

B. SEE LAYER STACK-UP FOR IMPEDANCE REQUIREMENTS.

9. IF PANELIZATION SPECIFICATIONS ARE PROVIDED, THE PCBs SHALL BE DELIVERED IN PANEL FORM. HOWEVER, THESE SPECIFICATIONS MAY BE CHANGED AS REQUIRED BY THE CONTRACT MANUFACTURER TO SUPPORT VOLUME ASSEMBLY REQUIREMENTS.

1.0

+/- 10%

(.185)

(.457)

(.185)

STACK - UP

LAYER DESCRIPTION	START COPPER WT	SE IMP OHMS	SE TRACE WIDTH	REF LAYER	CPW SPACE	DIFF IMP OHMS	DIFF TRACE WIDTH/SPACE	REF LAYER	CPW SPACE
L01 - TOP	0.5 OZ	50	0.310	2	-----	90	0.165/0.109	2	-----
L02 - PLANE	1.0 OZ	--	-----	---	-----	---	----	---	-----
L03 - PLANE	1.0 OZ	--	-----	---	-----	---	----	---	-----
L04 - BOTTOM	0.5 OZ	--	-----	---	-----	90	0.165/0.109	3	-----

SEE BOM

SEE BOM

NEXT ASSY

USED ON

APPLICATION

THE INFORMATION CONTAINED HEREIN IS CONFIDENTIAL AND PROPRIETARY TO BROADCOM CORPORATION AND SHALL NOT BE REPRODUCED OR DISCLOSED IN WHOLE OR IN PART OR USED FOR ANY DESIGN, MANUFACTURE, OR OTHERWISE, EXCEPT WHEN SUCH USER POSSESSES DIRECT, WRITTEN AUTHORIZATION FROM BROADCOM CORPORATION.

UNLESS OTHERWISE SPECIFIED

DIMENSIONS ARE IN METRIC

WITH INCHES IN BRACKETS

.XXX .XX ANGLES

±.064 ±.13 ±.5°

[±.005] [±.01]

DO NOT SCALE DRAWING

APPROVALS

DATE

DRAWN

P.PILOT/K.SEAMAN

10/21/14

ENGINEER

M.LIU

10/21/14

CHECKER

-

-

OA

-

PROJ. ENG.

-

-

TITLE

PCB FABRICATION,

BCM920737_LE_TAG4

SIZE

D

CAGE CODE

-

BRCM P/N

822-128343-0000

SCALE

1/1

SHEET

1 OF 3

REV

01

BROADCOM

Connecting everything

IRVINE, CA USA

FAB NOTES REV 08/01/14

7

6

5

4

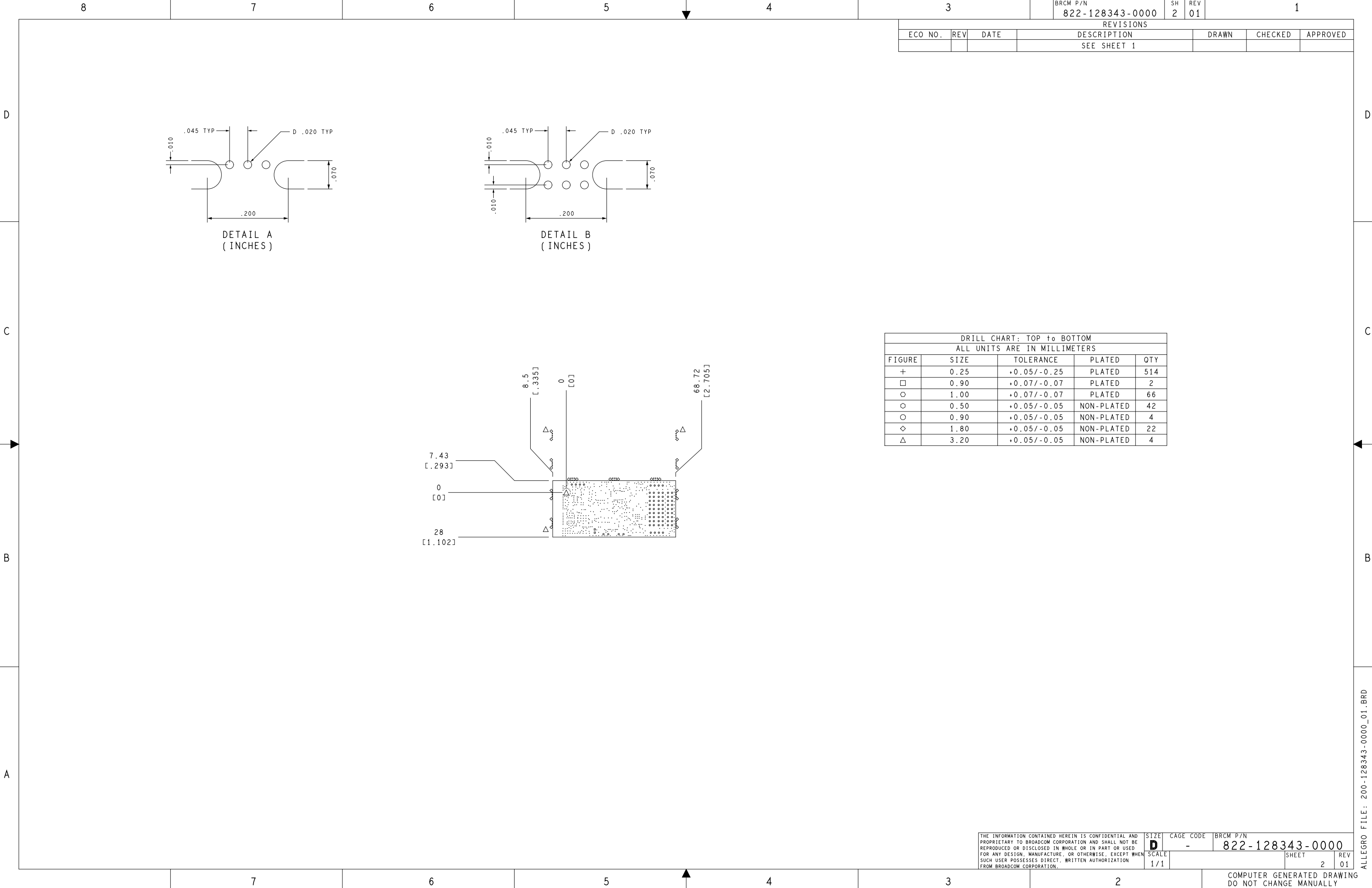
3

2

COMPUTER GENERATED DRAWING

DO NOT CHANGE MANUALLY

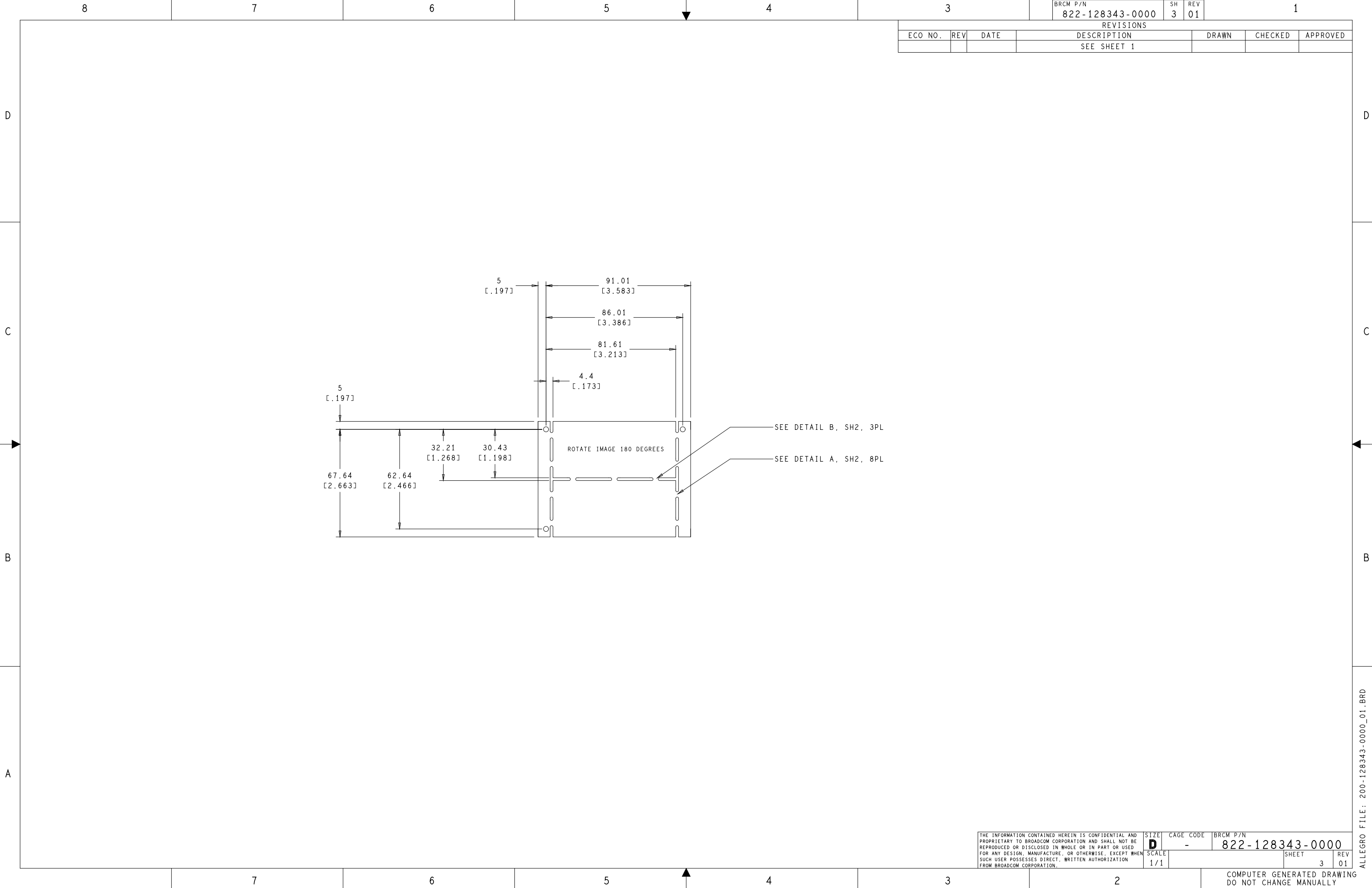
ALLEGR0 FILE: 200-128343-0000_01.BRD



3			BRCM P/N 822-128343-0000		SH 2	REV 01	1		
REVISIONS									
ECO NO.	REV	DATE	DESCRIPTION			DRAWN	CHECKED	APPROVED	
			SEE SHEET 1						

THE INFORMATION CONTAINED HEREIN IS CONFIDENTIAL AND PROPRIETARY TO BROADCOM CORPORATION AND SHALL NOT BE REPRODUCED OR DISCLOSED IN WHOLE OR IN PART OR USED FOR ANY DESIGN, MANUFACTURE, OR OTHERWISE, EXCEPT WHEN SUCH USER POSSESSES DIRECT, WRITTEN AUTHORIZATION FROM BROADCOM CORPORATION.		SIZE	CAGE CODE	BRCM P/N	
		D	-	822-128343-0000	
SCALE		SHEET			REV
1/1		2			01

COMPUTER GENERATED DRAWING
DO NOT CHANGE MANUALLY



REVISIONS				
ECO NO.	REV	DATE	DESCRIPTION	DRAWN
			SEE SHEET 1	

BRCM P/N	SH	REV
822-128343-0000	3	01

1

THE INFORMATION CONTAINED HEREIN IS CONFIDENTIAL AND PROPRIETARY TO BROADCOM CORPORATION AND SHALL NOT BE REPRODUCED OR DISCLOSED IN WHOLE OR IN PART OR USED FOR ANY DESIGN, MANUFACTURE, OR OTHERWISE, EXCEPT WHEN SUCH USER POSSESSES DIRECT, WRITTEN AUTHORIZATION FROM BROADCOM CORPORATION.	SIZE	CAGE CODE	BRCM P/N
	D	-	822-128343-0000
SCALE	SHEET		REV
1/1	3		01

COMPUTER GENERATED DRAWING
DO NOT CHANGE MANUALLY

ALLEGO FILE: 200-128343-0000_01.BRD