REVISION HISTORY:

Rev -0000P1

1) Initial release, BCM20737, 5x5, 32pin QFN

Rev -0000P2

1) U3, Changed to MXIC PN MX25U4033
2) RFB1 Changed to 360K
3) DNI R60 and R61
4) Changed C6 and C7 to 47uF & changed C30 and C31 to 22uF
5) Sensors removed: LIS3DSH, LPS331APTR, LSM9DS1, UVIS25 and BH1780GLI-E2
6) Sensors added: LPS25HBTR, LIS3MDLTR and LSM6DS3
7) Change Q3 from PMZB290UN to PBSS3540M

Rev -0000P3

1) Changed PUART_TX to P24, PUART_RX to P25
2) Added strapping options on the SDO pins of P10, P15 and P16
3) Added R66 - Option to pull P0 high by default

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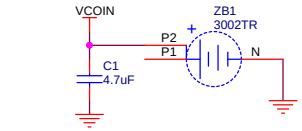
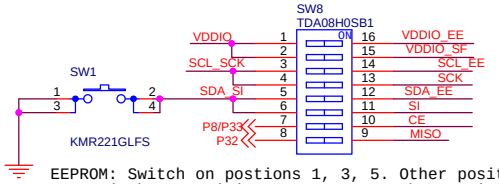
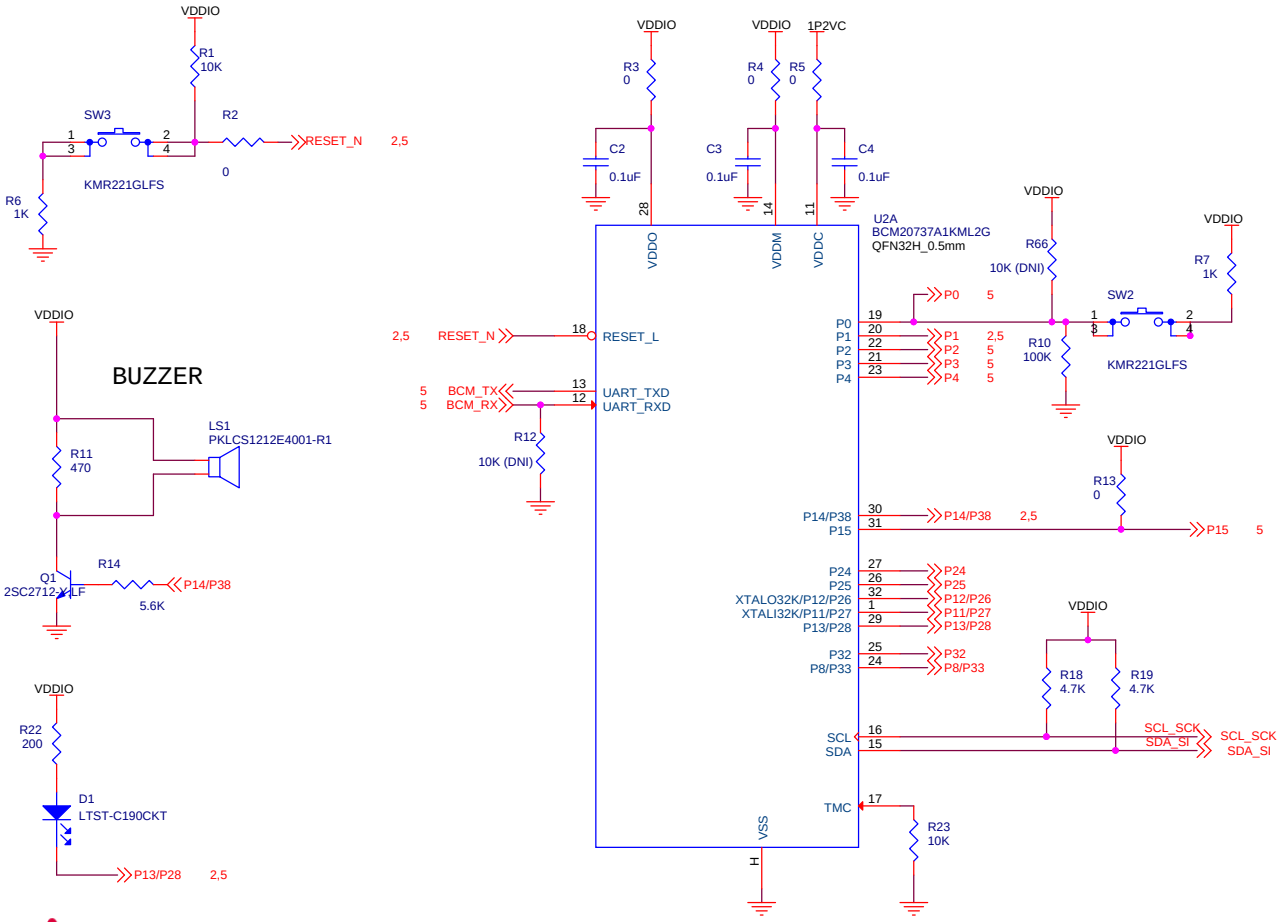


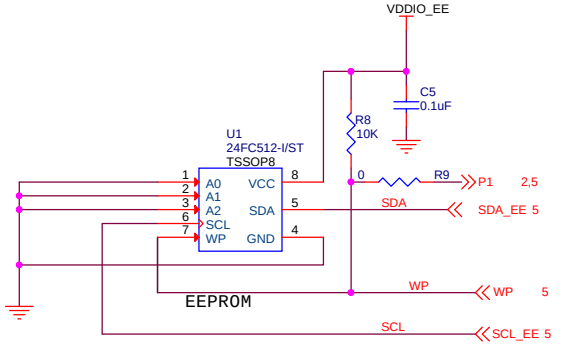
Add solder paste for GND pin of coin cell holder.

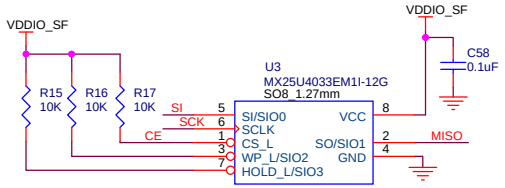


EEPROM: Switch on postions 1, 3, 5. Other positions off
SF: Switch on positions 2,4,6,7,8. Other positions off

*VDDIO_SF: must use regulated supply (2V) to VDDIO







SERIAL FLASH

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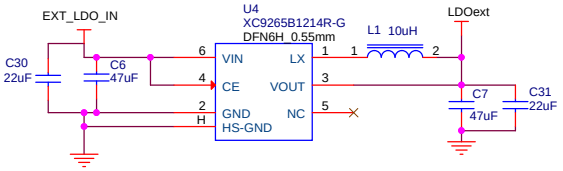
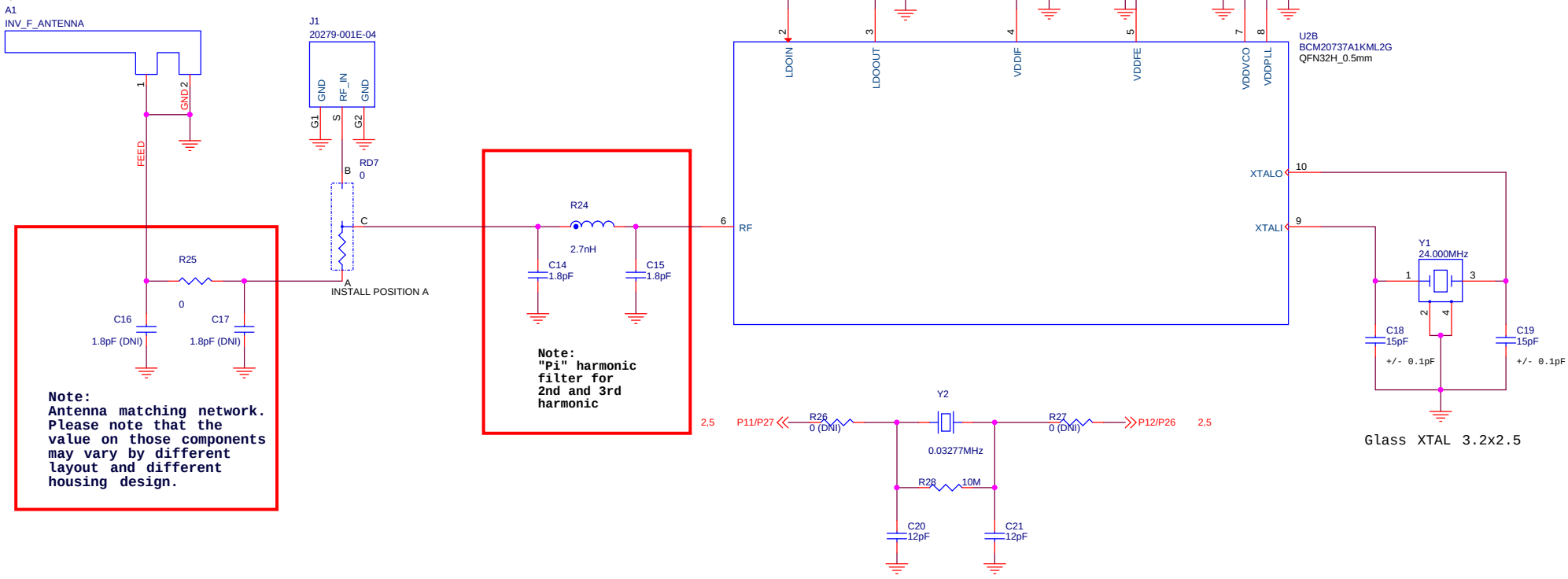
VDDC and VDDRF can come from internal LDO or external LDO
internal LDO: RD6=BC, RD5=BC
external LDO: RD6=AC, RD5=AC



Layout note: use antenna from 200-121921-0000 P1



Note:
Antenna matching network. Please note that the value on those components may vary by different layout and different housing design.

Note:
"Pi" harmonic filter for 2nd and 3rd harmonic

Glass XTAL 3.2x2.5

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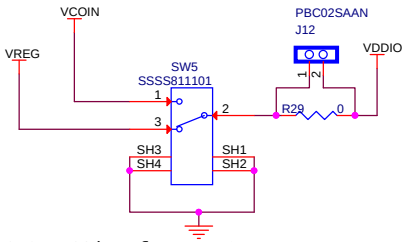
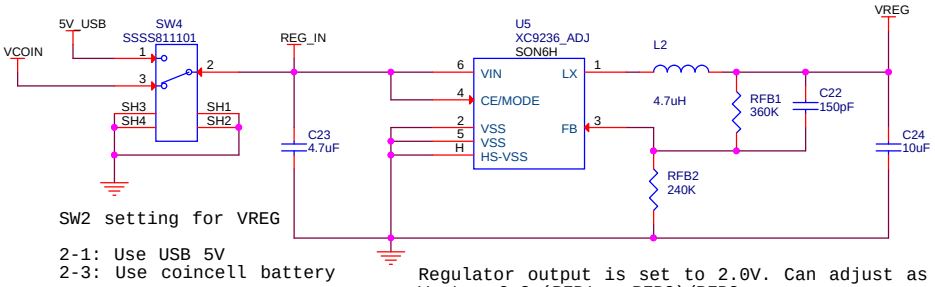
POWER



SW3 setting for VDDIO

2-1: Use coin cell battery (1.8V-3.0V)
2-3: Use regulated voltage



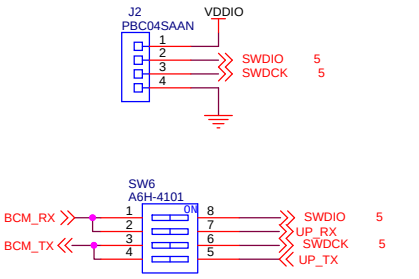
SW2 setting for VREG

2-1: Use USB 5V
2-3: Use coincell battery

Regulator output is set to 2.0V. Can adjust as needed:
Vout = 0.8x(RFB1 + RFB2)/RFB2

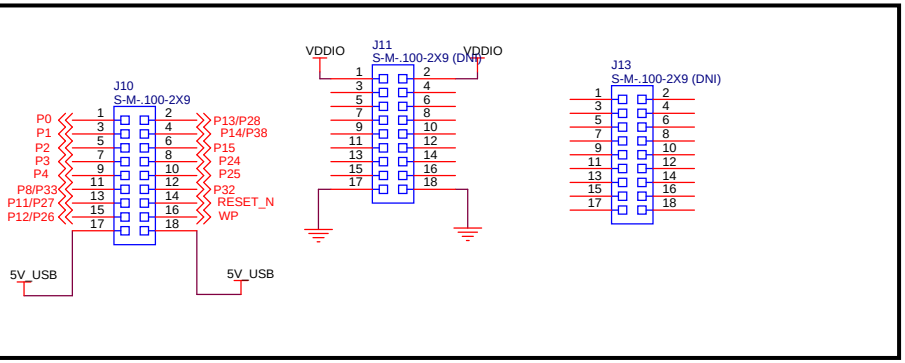
Vout = 3.3V	RFB1=470K	RFB2=150K	C22=100pF
Vout = 3.0V	RFB1=330K	RFB2=120K	C22=150pF
Vout = 2.0V	RFB1=360K	RFB2=240K	C22=150pF
Vout = 1.8V	RFB1=300K	RFB2=240K	C22=150pF

JLINK DEBUG

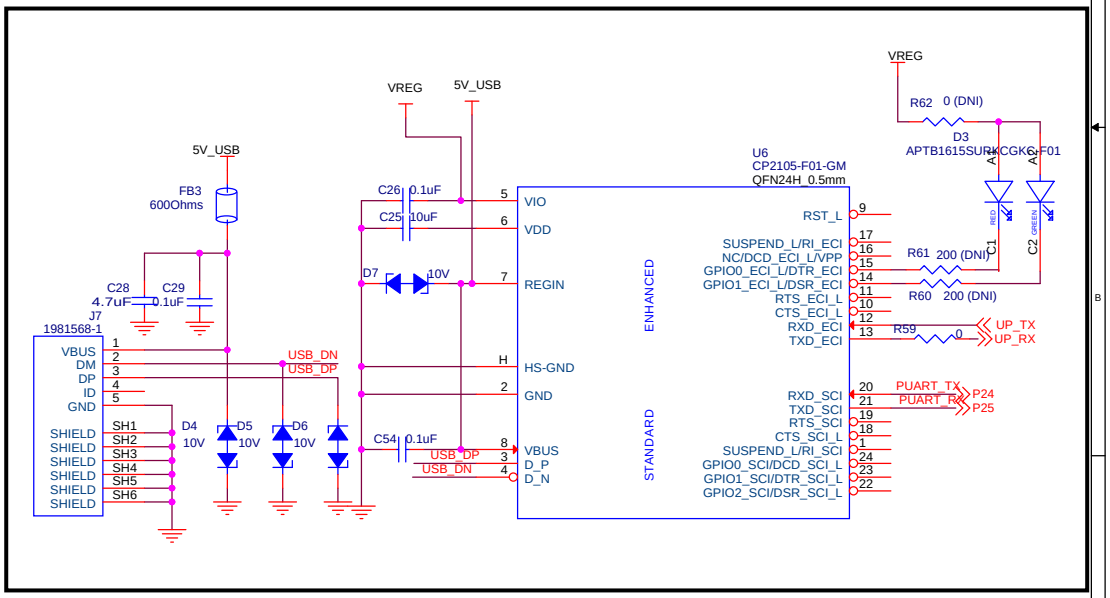


HCI Mode: Switch on POS 2 and 4. Other POS off.
JLINK Mode: Switch on POS 1 and 3. Other POS off.
APP Mode: All POS off.

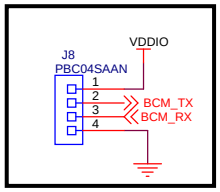
GPIO HEADERS



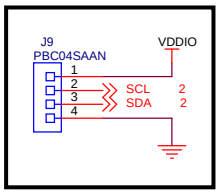
USB TO UART



UART HEADER



I2C HEADER



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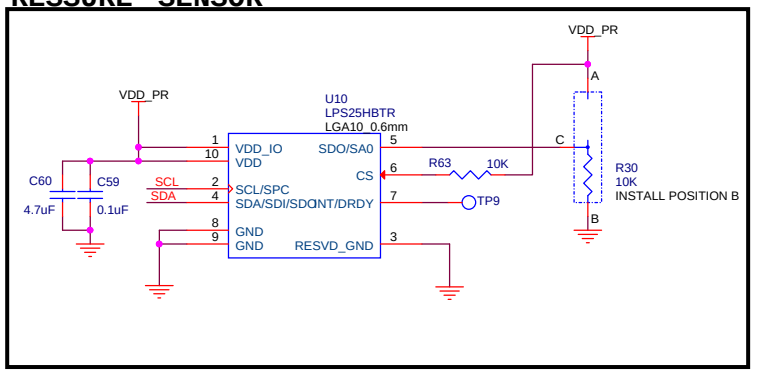
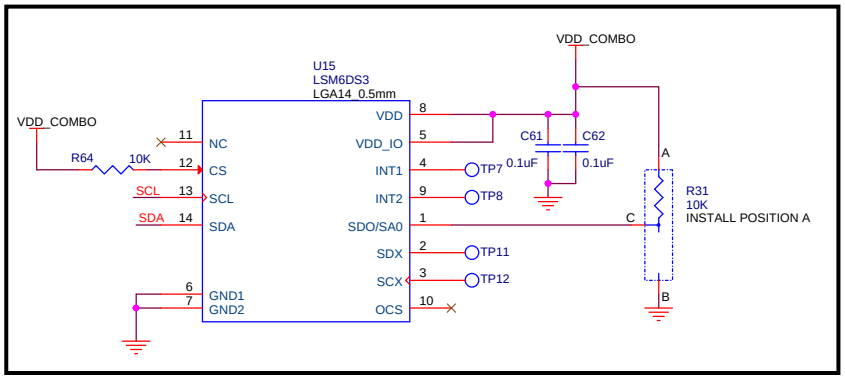
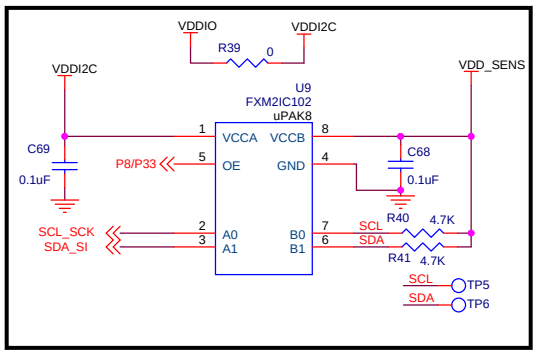
PRESSURE SENSOR



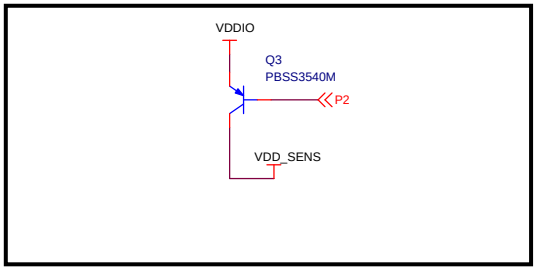
ACCELEROMETER + GYROSCOPE COMBO



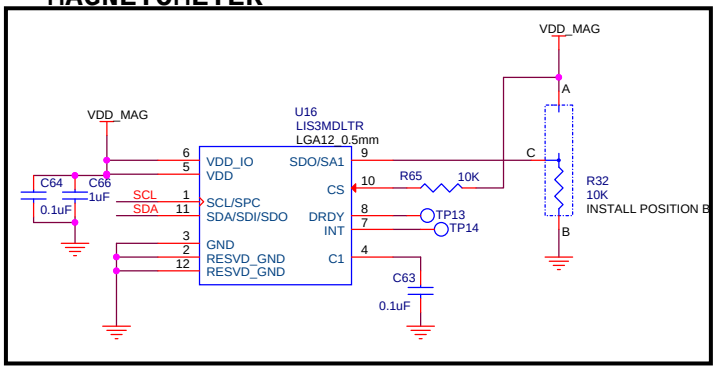
I2C EXPANDER



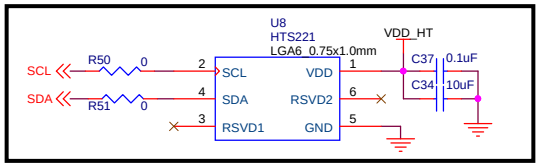
FET TO SWITCH OFF SENSORS

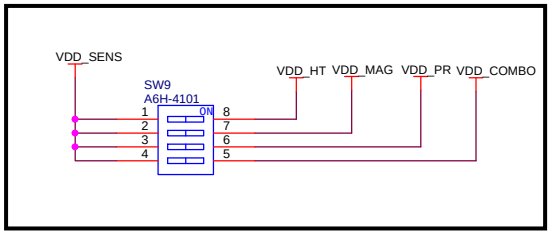


MAGNETOMETER



HUMIDITY & TEMPERATURE





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