

Ava Zahedi
ME 333
Quiz 4

1. Cacheable memory is memory that we can store once and come back to repeatedly when we don't expect it to change. Memory that is not cacheable includes memory that we always want to be refreshed and the newest version. SFRs, for example, should not be placed in cache because we always want the most updated version of them.
2. The SFR TRISA, there are 9 implemented bitfields. After reset, the bitfields initially set are 0, 1, 2, 3, 4, 5, 8, 9, 10, 11.
3.
TRISASET 0b0000000011010
TRISACLR 0b111111100101
4. The hex file compiles only the functions that are actually used, whereas the elf file contains a lot of extra information. As a result, the hex file is much smaller than the elf file and is more compressed for usage on the chip.