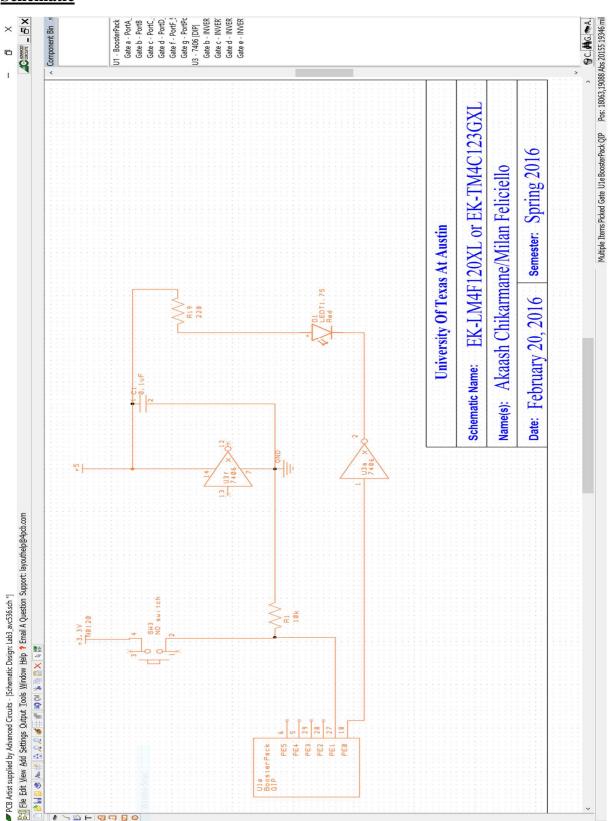
Lab 3 Deliverables

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Section: 16085

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Schematic



Delay Screenshot

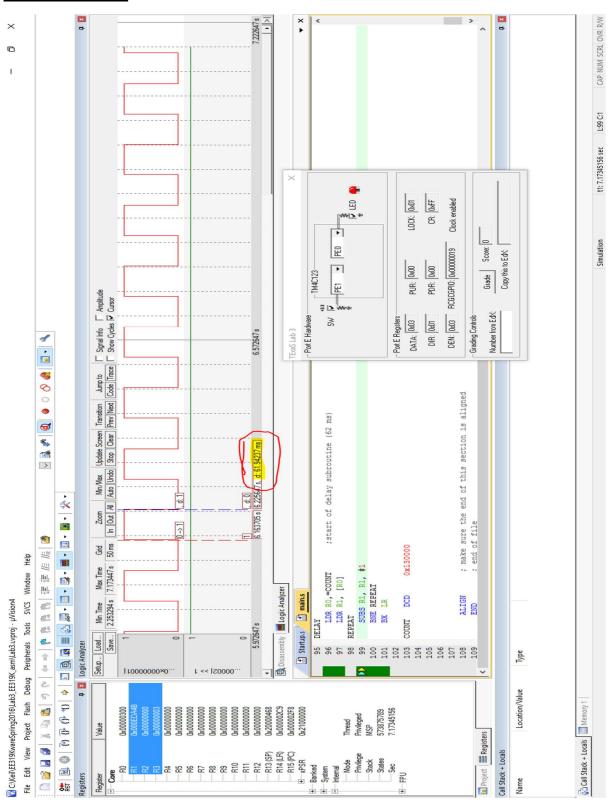


Table 3.1 (Switch Measurements)

Parameter	Value	Units	Conditions
Resistance of the $10 \text{k}\Omega$ resistor, R1	9.89 kΩ	ohms	with power off and disconnected from circuit (measured with ohmmeter)
Supply Voltage, V _{+3.3}	3.285 V	volts	Powered (measured with voltmeter)
Input Voltage, V _{PE1}	3.4 mV	volts	Powered, but with switch not pressed (measured with voltmeter)
Resistor current	Calculated: ~ 0.0 mA Measured: 0 mA	mA	Powered, but switch not pressed I=V _{PE1} /R1 (calculated and measured with an ammeter)
Input Voltage, V _{PE1}	3.28 V	volts	Powered and with switch pressed (measured with voltmeter)
Resistor current	Calculated: .33 mA Measured: .33 mA	mA	Powered and switch pressed

Table 3.2 (LED Measurements)

Row	Parameter	Value	Units	Conditions
	Resistance of the	219.8 V		with power off and
1	220 Ω resistor, R19		ohms	disconnected from circuit
				(measured with ohmmeter)
	+5 V power supply	5.04 V		(measured with voltmeter relative to
2	V ₊₅		volts	ground, notice that the +5V power is not exactly +5 volts)
	TM4C123 Output, V _{PEO}	93.7 mV		with PE0 = 0
3	input to 7406		volts	(measured with voltmeter relative to ground)
	7406 Output, V _k -	3.71		with PE0 = 0
4	LED k-		volts	(measured with voltmeter relative to ground)
	LED a+, V _{a+}	5.03		with PE0 = 0
5	Bottom side of R19		volts	(measured with voltmeter relative to ground)
		1.32		
6	LED voltage		volts	calculated as V_{a+} - V_{k-}
		.045		calculated as $(V_{+5} - V_{a+})/R19$
7	LED current		mA	and
		0.00		measured with an ammeter
	TM4C123 Output, V _{PEO}	2.20		with PE0 = 1
0		3.28	volte	
8	input to 7406		volts	(measured with voltmeter relative to ground)
	7406 Output, V _{k-}	128.5 mV		with PE0 = 1
9	LED k-		volts	(measured with voltmeter relative to ground)
<u> </u>	1	1	1	

	LED a+, V _{a+}	2.055		with PE0 = 1
10	Bottom side of R19		volts	(measured with voltmeter relative to ground)
		1.9265		
11	LED voltage		volts	calculated as V_{a+} - V_{k-}
		13.58		calculated as $(V_{+5} - V_{a+})/R19$
12	LED current	13.26	mA	and
				measured with an ammeter

Assembly Source Code

;************ main.s **********

; Program written by: Akaash Chikarmane

; Date Created: 1/22/2016

; Last Modified: 2/22/2016

; Section: Tuesday 2-3

; Instructor: Ramesh Yerraballi

; Lab number: 3

; Brief description of the program

; If the switch is presses, the LED toggles at 8 Hz

; Hardware connections

; PE1 is switch input (1 means pressed, 0 means not pressed)

; PEO is LED output (1 activates external LED on protoboard)

;Overall functionality of this system is the similar to Lab 2, with six changes:

- ;1- the pin to which we connect the switch is moved to PE1,
- ;2- you will have to remove the PUR initialization because pull up is no longer needed.
- ;3- the pin to which we connect the LED is moved to PEO,
- ;4- the switch is changed from negative to positive logic, and
- ;5- you should increase the delay so it flashes about 8 Hz.

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;6- the LED should be on when the switch is not pressed
; Operation
; 1) Make PEO an output and make PE1 an input.
; 2) The system starts with the LED on (make PEO =1).
; 3) Wait about 62 ms
; 4) If the switch is pressed (PE1 is 1), then toggle the LED once, else turn the LED on (0).
; 5) Steps 3 and 4 are repeated over and over
GPIO_PORTE_DATA_R EQU 0x400243FC
GPIO_PORTE_DIR_R EQU 0x40024400
GPIO_PORTE_AFSEL_R EQU 0x40024420
GPIO_PORTE_DEN_R EQU 0x4002451C
GPIO_PORTE_AMSEL_R EQU 0x40024528
GPIO_PORTE_PCTL_R EQU 0x4002452C
SYSCTL_RCGCGPIO_R EQU 0x400FE608
   IMPORT TExaS_Init
   AREA |.text|, CODE, READONLY, ALIGN=2
   THUMB
   EXPORT Start
Start
; TExaS_Init sets bus clock at 80 MHz
   BL TExaS_Init; voltmeter, scope on PD3
   LDR RO,=SYSCTL_RCGCGPIO_R
   LDR R1, [R0]
   ORR R1, #0x10
                                   ;enable clock for Port E (0001 0000)
   STR R1, [R0]
   NOP
                                                 ;stabilize clock
   NOP
```

LDR R0,=GPIO_PORTE_DEN_R

LDR R1, [R0]

ORR R1, #0x03 ;enable digital I/O for PF3, PF4

STR R1, [R0]

LDR RO,=GPIO_PORTE_DIR_R

LDR R1, [R0]

ORR R1, #0x01 ;PE0=output (1)

BIC R1, #0x02 ;PE1=input (0)

STR R1, [R0]

LDR RO,=GPIO_PORTE_AFSEL_R

LDR R1, [R0]

BIC R1, #0x03 ;no alternate function for PF3,4

STR R1, [R0]

LDR RO,=GPIO_PORTE_DATA_R

LDR R1, [R0]

ORR R1, #0x01 ;LED initially on

STR R1, [R0]

CPSIE I ; TExaS voltmeter, scope runs on interrupts

loop

BL DELAY

LDR RO,=GPIO_PORTE_DATA_R

LDR R1, [R0] ;read the switch (R1 = adjusting PE0)

MOV R2, R1 ;R2 = checking PE1

MOV R3, R1 ;R3 = original data

AND R2, #0x02 ;clear all but PE1

SUBS R2, #0x02

BNE SETON ;if PE1 = 0, turn on LED

AND R1, #0x01 ;clear all but PE0

EOR R1, #0x01 ;toggle PE0

BIC R3, #0x01 ;clear original PE0

ORR R3, R1, R3 ;recombine

STR R3, [R0]

B loop

SETON

ORR R3, #0x01

STR R3, [R0]

B loop

DELAY

LDRRO,=COUNT ;start of delay subroutine (62 ms)

LDRR1, [RO]

REPEAT

SUBS R1, R1, #1

BNE REPEAT

BX LR

COUNT DCD 0x130000

ALIGN $\;\;$; make sure the end of this section is aligned

END ; end of file