8 Bit Superscalar Processor

Aven Bross

Example Instructions

Instruction: 10001000

Yes	No	2	R0
1	0	0010	00
is const	shifted	constant	write register

Example Instructions

Instruction: 00000001

No	Mov	R1	R0
0	000	01	00
is const	operation	read register	write register

Operation

000	Move
001	Add
010	Sub
100	Mul
101	Div