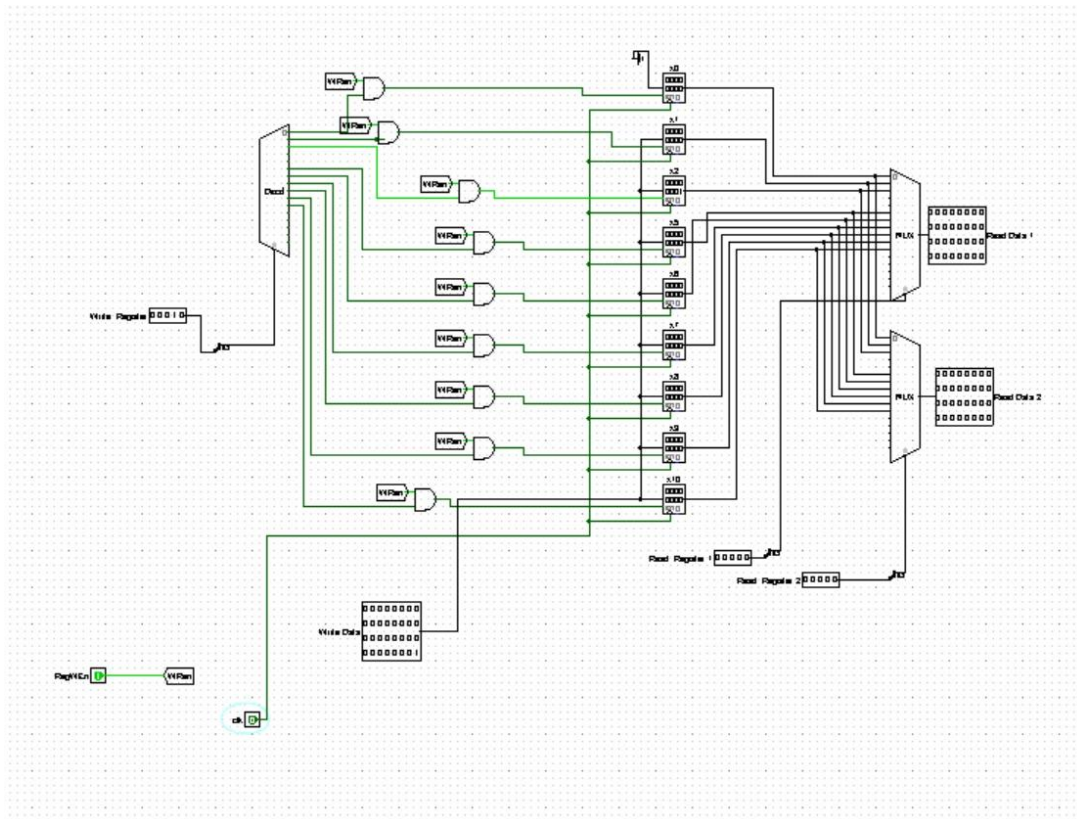
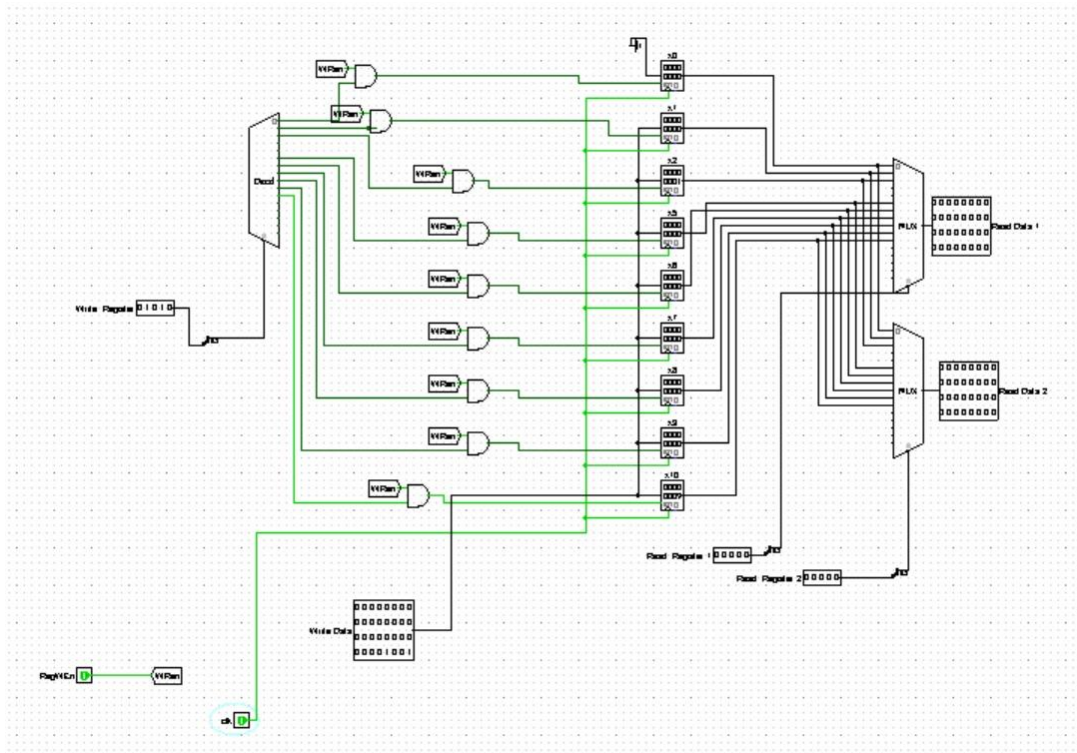


REPORT

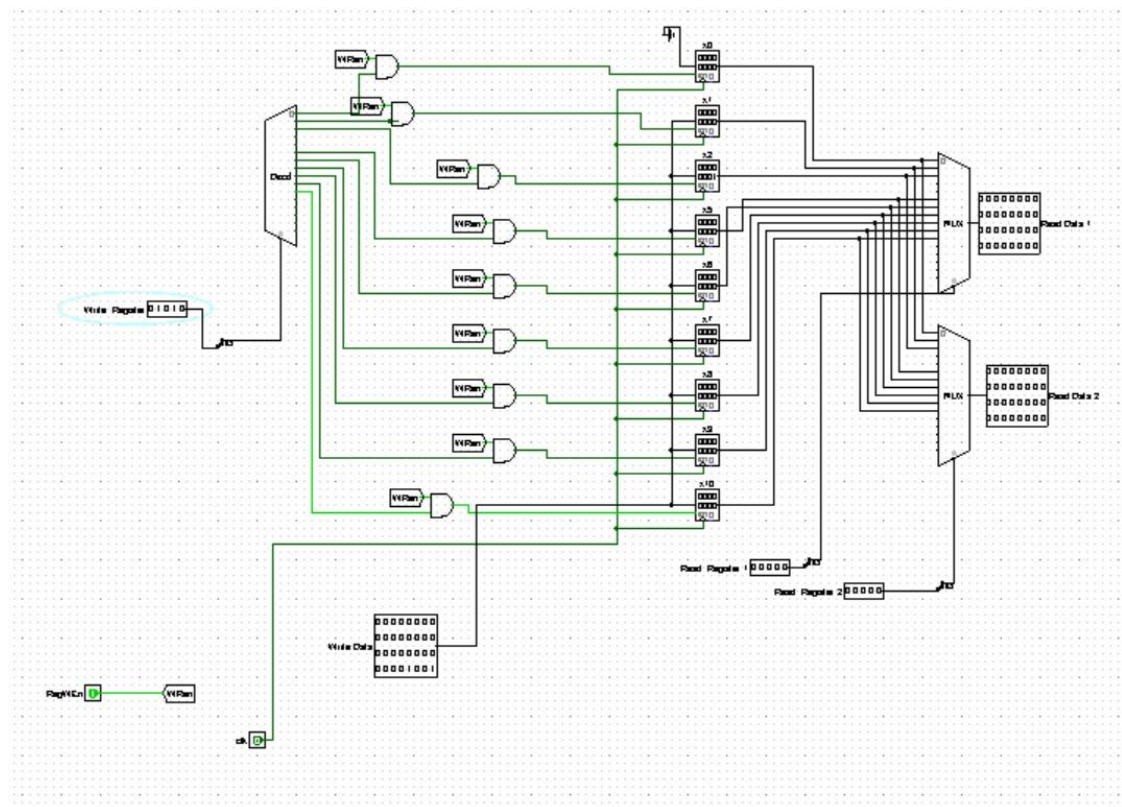
Processor part 3: Register file



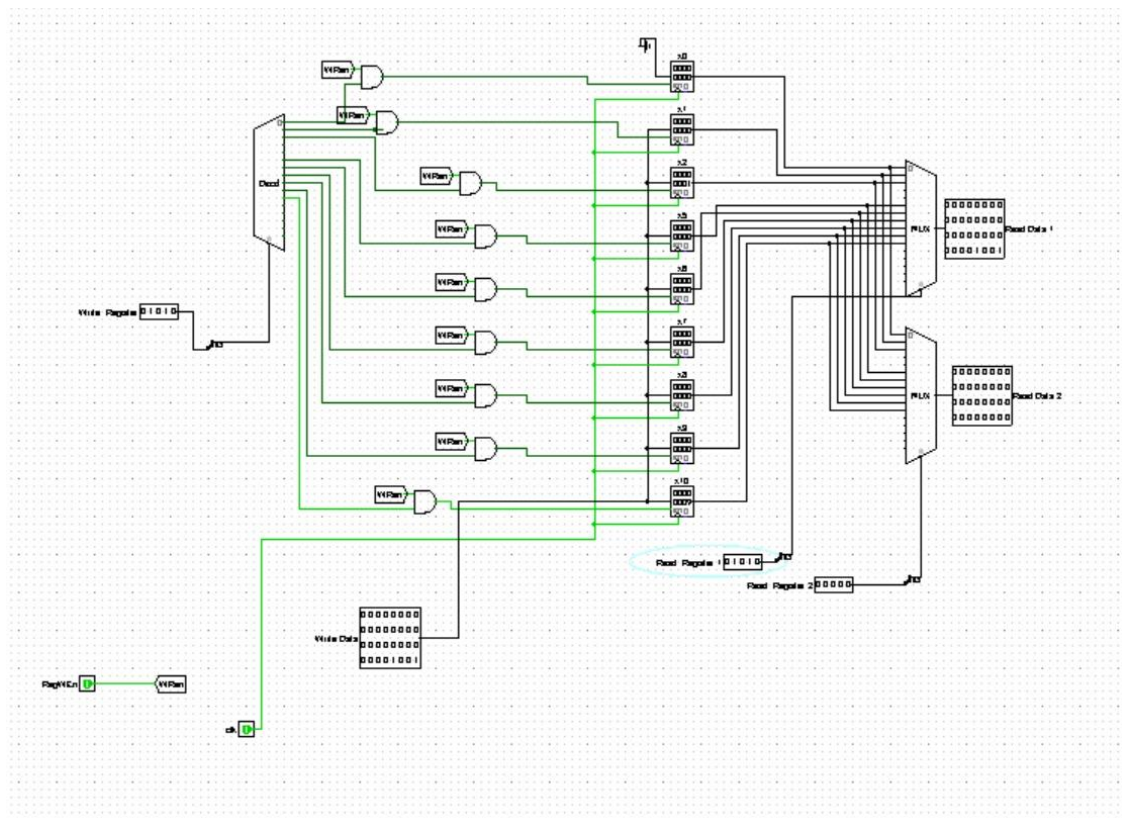
data written in register x2



state of register file before data is being written to x10



state of register after data being written to register x10



date being read and written to the same register simultaneously